SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

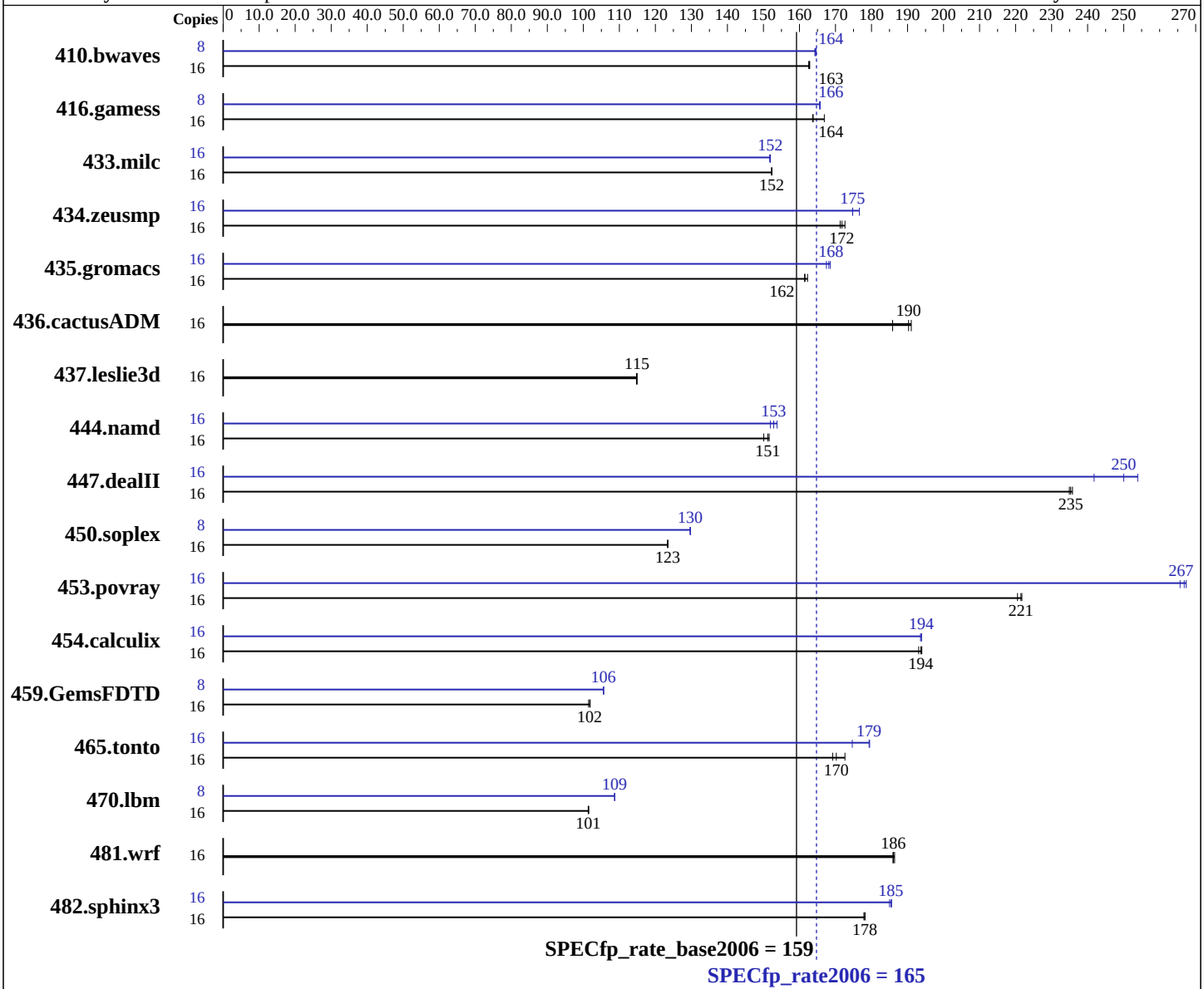
Express5800/R120a-1
(Intel Xeon E5530)

SPECfp®_rate2006 = 165

SPECfp_rate_base2006 = 159

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: May-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009



Hardware

CPU Name:	Intel Xeon E5530
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp
Compiler:	Intel C++ and Fortran Compiler Professional 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.081, l_cprof_p_11.0.081
Auto Parallel:	No
File System:	ReiserFS

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/R120a-1
(Intel Xeon E5530)

SPECfp_rate2006 = 165

SPECfp_rate_base2006 = 159

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: May-2009
Hardware Availability: Apr-2009
Software Availability: Feb-2009

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 X 4 GB PC3-8500R, 2 rank, CL7, ECC)
Disk Subsystem: 1x146.5 GB SAS, 15000 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1335	163	**1336**	**163**	1338	163	8	660	165	**661**	**164**	662	164
416.gamess	16	**1912**	**164**	1877	167	1914	164	8	946	166	945	166	**945**	**166**
433.milc	16	965	152	964	152	**964**	**152**	16	967	152	968	152	**967**	**152**
434.zeusmp	16	843	173	**847**	**172**	850	171	16	833	175	824	177	**833**	**175**
435.gromacs	16	708	161	704	162	**707**	**162**	16	678	169	682	167	**679**	**168**
436.cactusADM	16	1001	191	1029	186	**1005**	**190**	16	1001	191	1029	186	**1005**	**190**
437.leslie3d	16	**1309**	**115**	1311	115	1309	115	16	**1309**	**115**	1311	115	1309	115
444.namd	16	846	152	**849**	**151**	855	150	16	834	154	**840**	**153**	845	152
447.dealII	16	776	236	779	235	**778**	**235**	16	**732**	**250**	721	254	757	242
450.soplex	16	1081	123	**1081**	**123**	1082	123	8	515	130	**515**	**130**	514	130
453.povray	16	**384**	**221**	386	221	384	222	16	**319**	**267**	318	267	320	266
454.calculix	16	**681**	**194**	684	193	680	194	16	**681**	**194**	681	194	682	194
459.GemsFDTD	16	1673	101	**1668**	**102**	1666	102	8	804	106	804	106	**804**	**106**
465.tonto	16	**925**	**170**	930	169	912	173	16	877	179	901	175	**878**	**179**
470.lbm	16	2166	102	**2168**	**101**	2169	101	8	1011	109	**1011**	**109**	1011	109
481.wrf	16	959	186	**960**	**186**	961	186	16	959	186	**960**	**186**	961	186
482.sphinx3	16	1750	178	1753	178	**1750**	**178**	16	1680	186	**1682**	**185**	1686	185

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS setting:
NUMA configuration: Enabled
```

General Notes

```
The NEC Express5800/R120a-1(Intel Xeon E5530),
the NEC Express5800/R120a-2(Intel Xeon E5530),
the Bull NovaScale R440 E2 (Intel Xeon E5530, 2.40 GHz) and
the Bull NovaScale R460 E2 (Intel Xeon E5530, 2.40 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R120a-1(Intel Xeon E5530) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revG.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revE.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 00:11:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 June 2009.