SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

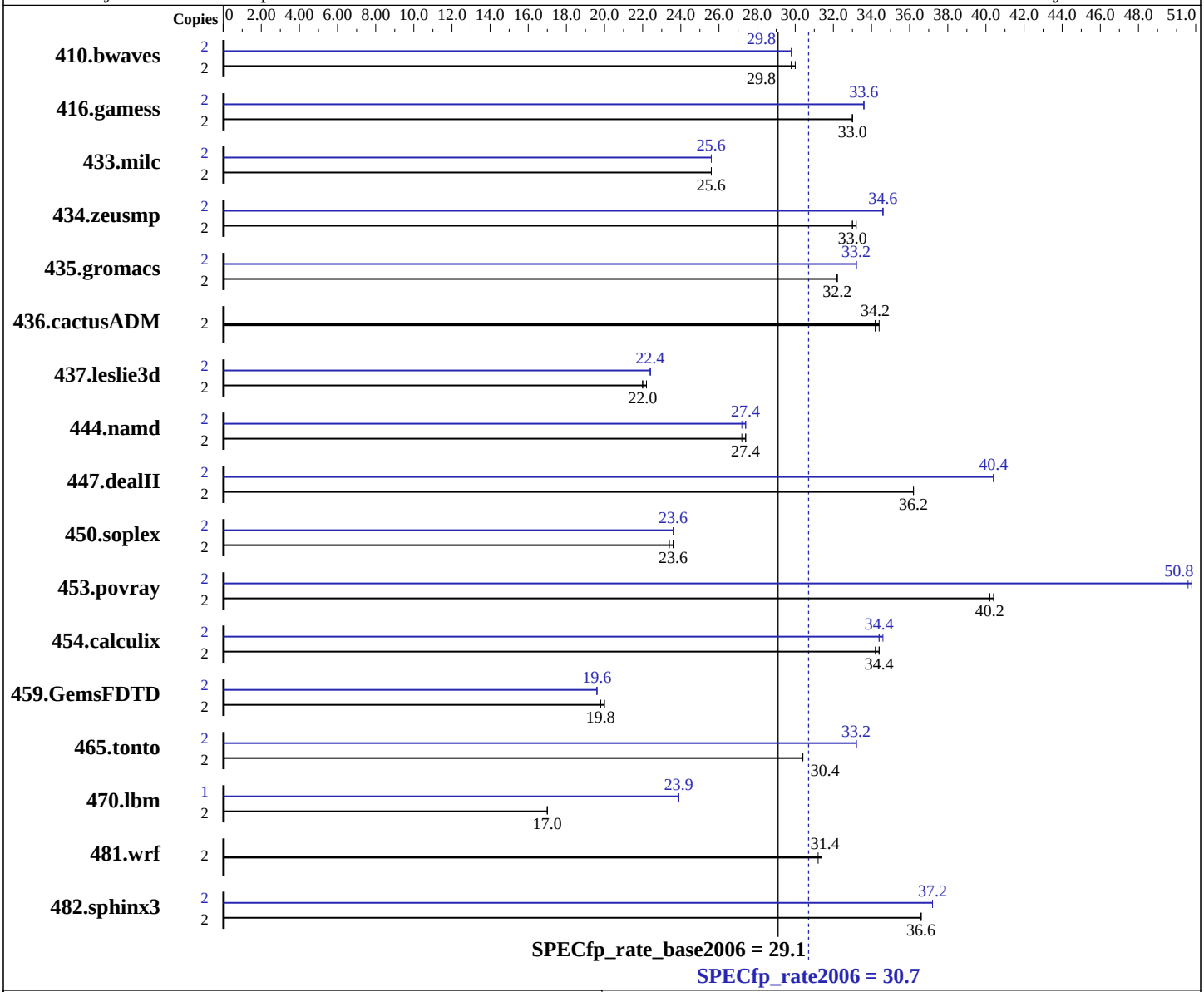
Intel DQ45CB motherboard (Intel Core 2 Duo E8200)

SPECfp®_rate2006 = 30.7

SPECfp_rate_base2006 = 29.1

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2009
Hardware Availability: May-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Core 2 Duo E8200
CPU Characteristics:	
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cproc_p_11.0.054 Intel Visual Fortran Compiler Professional 11.0 for IA32 Build 20080930 Package ID: w_cprof_p_11.0.054 Microsoft Visual Studio 2008 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem: Seagate 320 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	908	30.0	911	29.8	**911**	**29.8**	2	**911**	**29.8**	910	29.8	913	29.8
416.gamess	2	1185	33.0	**1185**	**33.0**	1185	33.0	2	1167	33.6	1166	33.6	**1166**	**33.6**
433.milc	2	720	25.6	**720**	**25.6**	719	25.6	2	718	25.6	718	25.6	**718**	**25.6**
434.zeusmp	2	551	33.0	**550**	**33.0**	549	33.2	2	527	34.6	**526**	**34.6**	526	34.6
435.gromacs	2	443	32.2	**443**	**32.2**	443	32.2	2	**431**	**33.2**	431	33.2	431	33.2
436.cactusADM	2	**697**	**34.2**	700	34.2	697	34.4	2	**697**	**34.2**	700	34.2	697	34.4
437.leslie3d	2	**851**	**22.0**	854	22.0	849	22.2	2	841	22.4	**840**	**22.4**	839	22.4
444.namd	2	588	27.2	587	27.4	**587**	**27.4**	2	589	27.2	587	27.4	**587**	**27.4**
447.dealII	2	632	36.2	**633**	**36.2**	634	36.2	2	567	40.4	567	40.4	**567**	**40.4**
450.soplex	2	710	23.4	709	23.6	**710**	**23.6**	2	706	23.6	705	23.6	**706**	**23.6**
453.povray	2	265	40.2	**264**	**40.2**	264	40.4	2	210	50.6	**210**	**50.8**	210	50.8
454.calculix	2	**480**	**34.4**	480	34.4	481	34.2	2	478	34.6	**479**	**34.4**	479	34.4
459.GemsFDTD	2	1066	20.0	**1068**	**19.8**	1069	19.8	2	1088	19.6	1087	19.6	**1087**	**19.6**
465.tonto	2	**648**	**30.4**	647	30.4	648	30.4	2	**594**	**33.2**	594	33.2	594	33.2
470.lbm	2	**1611**	**17.0**	1611	17.0	1611	17.0	1	**574**	**23.9**	574	23.9	574	23.9
481.wrf	2	**712**	**31.4**	711	31.4	714	31.2	2	**712**	**31.4**	711	31.4	714	31.2
482.sphinx3	2	1064	36.6	**1063**	**36.6**	1063	36.6	2	1050	37.2	1048	37.2	**1048**	**37.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
Antec Truepower Trio power supply TP3-650
Binaries were built on Windows Vista Ultimate (32-bit)
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

482.sphinx3: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

447.dealII: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

450.soplex: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

453.povray: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000`

416.gamess: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000`

434.zeusmp: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- /F1000000000`

437.leslie3d: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000`

459.GemsFDTD: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000`

465.tonto: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000`

Benchmarks using both Fortran and C:

435.gromacs: `-QxSSE4.1`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-QxSSE4.1 -Qipo -O3 -Qprec-div- /F1000000000`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml`

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