



# SPEC® CFP2006 Result

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GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Intel Corporation)

Gigabyte MA78GM-S2H Motherboard (AMD Phenom II X4 805)

**SPECfp®\_rate2006 = 37.2**

**SPECfp\_rate\_base2006 = 36.5**

CPU2006 license: 13

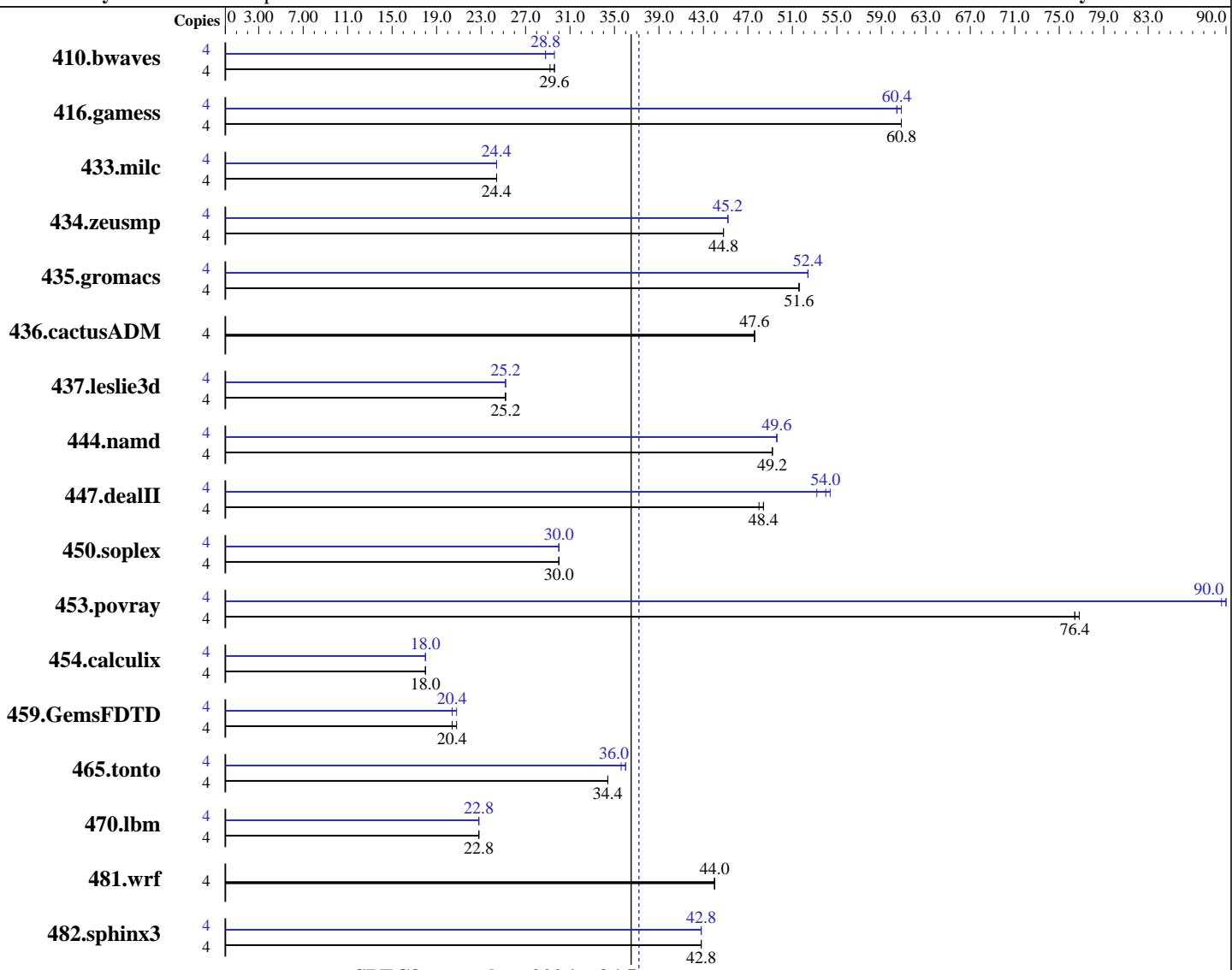
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: May-2009

Hardware Availability: Feb-2009

Software Availability: Nov-2008



**SPECfp\_rate\_base2006 = 36.5**

**SPECfp\_rate2006 = 37.2**

## Hardware

CPU Name: AMD Phenom II X4 805  
 CPU Characteristics:  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 512 KB I+D on chip per core

## Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)  
 Compiler: Intel C++ Compiler Professional 11.0 for IA32  
 Build 20080930 Package ID: w\_cproc\_p\_11.0.054  
 Intel Visual Fortran Compiler Professional 11.0  
 for IA32  
 Build 20080930 Package ID: w\_cprof\_p\_11.0.054  
 Microsoft Visual Studio 2008 (for libraries)  
 Auto Parallel: No  
 File System: NTFS

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L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 4 GB (4x1GB DDR2-800 CL5)  
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM  
 Other Hardware: None

System State: Default  
 Base Pointers: 32-bit  
 Peak Pointers: 32-bit  
 Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1845	29.6	<b>1847</b>	<b>29.6</b>	1852	29.2	4	<b>1878</b>	<b>28.8</b>	1846	29.6	1879	28.8
416.gamess	4	1290	60.8	<b>1291</b>	<b>60.8</b>	1292	60.8	4	<b>1293</b>	<b>60.4</b>	1293	60.4	1292	60.8
433.milc	4	1503	24.4	<b>1503</b>	<b>24.4</b>	1504	24.4	4	<b>1504</b>	<b>24.4</b>	1503	24.4	<b>1504</b>	<b>24.4</b>
434.zeusmp	4	<b>811</b>	<b>44.8</b>	810	44.8	811	44.8	4	<b>808</b>	<b>45.2</b>	<b>805</b>	<b>45.2</b>	804	45.2
435.gromacs	4	554	51.6	<b>554</b>	<b>51.6</b>	554	51.6	4	<b>547</b>	<b>52.4</b>	546	52.4	547	52.4
436.cactusADM	4	1003	47.6	<b>1002</b>	<b>47.6</b>	1002	47.6	4	<b>1003</b>	<b>47.6</b>	<b>1002</b>	<b>47.6</b>	1002	47.6
437.leslie3d	4	1487	25.2	<b>1486</b>	<b>25.2</b>	1486	25.2	4	<b>1488</b>	<b>25.2</b>	1488	25.2	1486	25.2
444.namd	4	651	49.2	<b>651</b>	<b>49.2</b>	651	49.2	4	<b>647</b>	<b>49.6</b>	647	49.6	646	49.6
447.dealII	4	942	48.4	<b>946</b>	<b>48.4</b>	952	48.0	4	<b>845</b>	<b>54.0</b>	861	53.2	843	54.4
450.soplex	4	1107	30.0	<b>1110</b>	<b>30.0</b>	1112	30.0	4	<b>1111</b>	<b>30.0</b>	<b>1109</b>	<b>30.0</b>	1109	30.0
453.povray	4	277	76.8	279	76.4	<b>278</b>	<b>76.4</b>	4	237	90.0	<b>237</b>	<b>90.0</b>	238	89.6
454.calculix	4	1838	18.0	1837	18.0	<b>1837</b>	<b>18.0</b>	4	1838	18.0	<b>1837</b>	<b>18.0</b>	1837	18.0
459.GemsFDTD	4	<b>2065</b>	<b>20.4</b>	2078	20.4	2046	20.8	4	<b>2058</b>	20.8	<b>2070</b>	<b>20.4</b>	2081	20.4
465.tonto	4	1145	34.4	1142	34.4	<b>1142</b>	<b>34.4</b>	4	<b>1099</b>	<b>36.0</b>	1098	36.0	1103	35.6
470.lbm	4	<b>2396</b>	<b>22.8</b>	2396	22.8	2397	22.8	4	<b>2396</b>	<b>22.8</b>	2395	22.8	2397	22.8
481.wrf	4	1014	44.0	<b>1019</b>	<b>44.0</b>	1019	44.0	4	1014	44.0	<b>1019</b>	<b>44.0</b>	1019	44.0
482.sphinx3	4	1821	42.8	<b>1826</b>	<b>42.8</b>	1826	42.8	4	<b>1826</b>	<b>42.8</b>	1822	42.8	<b>1826</b>	<b>42.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## General Notes

Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply  
 Binaries were built on Windows Vista Ultimate (32-bit)

## Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

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## Base Compiler Invocation (Continued)

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qc99 ifort
```

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG

453.povray: -DSPEC\_CPU\_WINDOWS\_ICL

454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:

```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F10000000000
```

C++ benchmarks:

```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
/F10000000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F10000000000
```

Benchmarks using both Fortran and C:

```
/arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F10000000000
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qc99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icl -Qvc9 -Qc99 ifort

## Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

## Peak Optimization Flags

C benchmarks:

```
433.milc: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Oa /F1000000000
470.lbm: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
           /F1000000000
482.sphinx3: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000
```

C++ benchmarks:

```
444.namd: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
           -link /FORCE:MULTIPLE
447.dealII: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
           -Qscalar-rep- /F1000000000 shlw32m.lib
           -link /FORCE:MULTIPLE
450.soplex: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
           -link /FORCE:MULTIPLE
453.povray: /arch:SSE2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
           -Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
           shlw32m.lib
           -link /FORCE:MULTIPLE
```

Fortran benchmarks:

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## Peak Optimization Flags (Continued)

410.bwaves: /arch:SSE2 -Qipo -O3 -Qprec-div- -Qopt-prefetch  
/F1000000000

416.gamess: /arch:SSE2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll12 -Ob0 -Qansi-alias  
-Qscalar-rep- /F1000000000

434.zeusmp: /arch:SSE2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: /arch:SSE2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: /arch:SSE2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll12 -Ob0 -Qopt-prefetch  
/F1000000000

465.tonto: /arch:SSE2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qunroll14 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: /arch:SSE2(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2)  
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: /arch:SSE2 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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