



SPEC® CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 136

PowerEdge M805 (AMD Opteron 2431, 2.40 GHz)

SPECfp_rate_base2006 = 124

CPU2006 license: 55

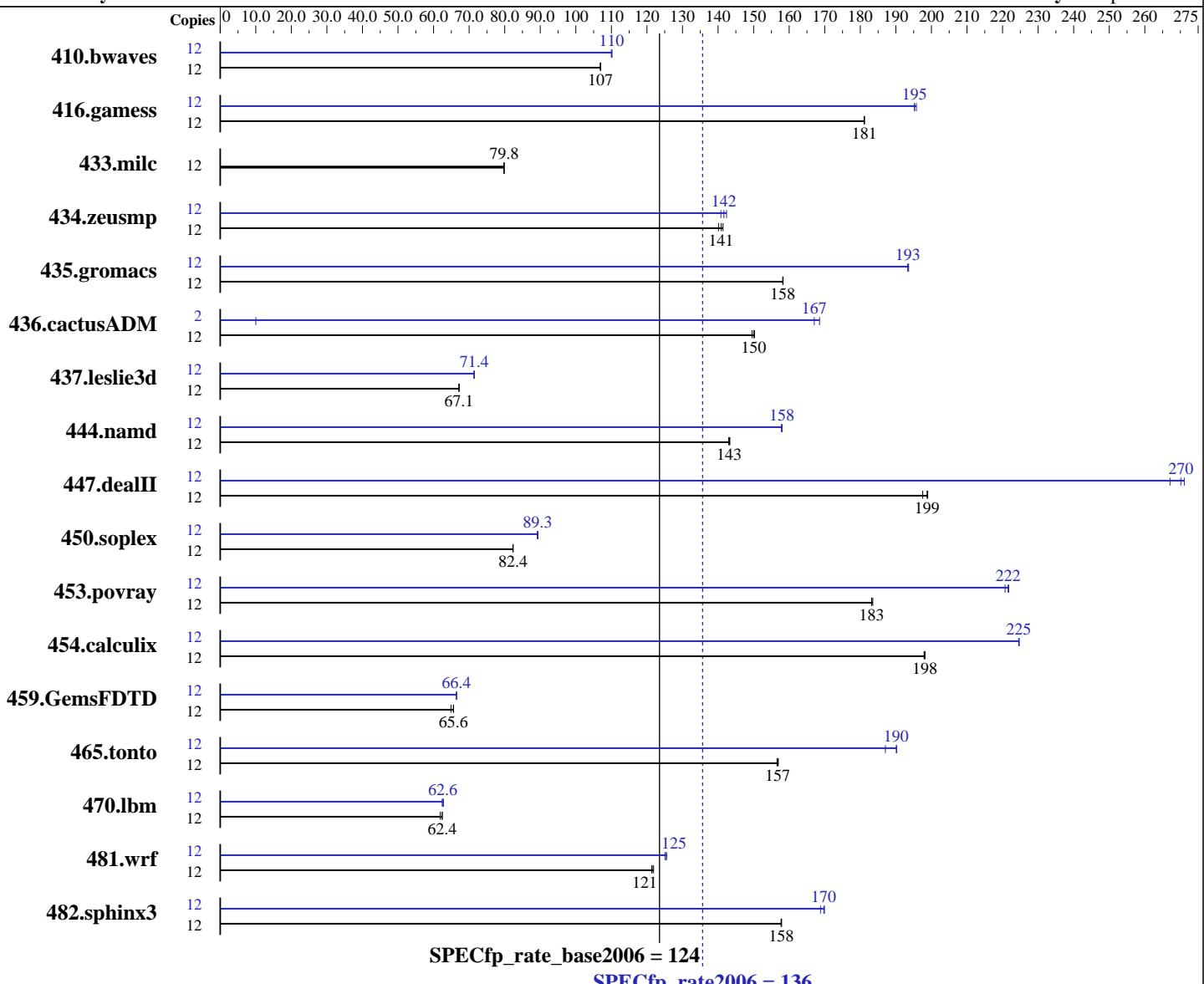
Test date: Jun-2009

Test sponsor: Dell Inc.

Hardware Availability: Jul-2009

Tested by: Dell Inc.

Software Availability: Apr-2009



Hardware

CPU Name: AMD Opteron 2431
CPU Characteristics:
CPU MHz:
FPU:
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
CPU(s) orderable: 2 chips
Primary Cache: 64 KB I + 64 KB D on chip per core
Secondary Cache: 512 KB I+D on chip per core

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
Compiler: PGI Server Complete Version 8.0 x86 Open64 4.2.2 Compiler Suite (from AMD)
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (Full multiuser with network)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit

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L3 Cache: 6 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (8 x 4 GB DDR2-800)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS
 Other Hardware: None

Other Software: binutils 2.18

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1526	107	1525	107	1525	107	12	1480	110	1482	110	1483	110
416.gamess	12	1297	181	1297	181	1298	181	12	1200	196	1204	195	1203	195
433.milc	12	1380	79.8	1380	79.8	1380	79.8	12	1380	79.8	1380	79.8	1380	79.8
434.zeusmp	12	779	140	775	141	772	141	12	776	141	771	142	767	142
435.gromacs	12	542	158	541	158	542	158	12	443	193	443	193	443	194
436.cactusADM	12	955	150	959	150	956	150	2	143	167	142	169	2386	10.0
437.leslie3d	12	1680	67.1	1680	67.2	1680	67.1	12	1581	71.3	1579	71.5	1579	71.4
444.namd	12	672	143	671	143	673	143	12	610	158	609	158	609	158
447.dealII	12	690	199	695	198	691	199	12	508	270	514	267	506	271
450.soplex	12	1216	82.3	1215	82.4	1215	82.4	12	1120	89.4	1122	89.2	1121	89.3
453.povray	12	348	183	348	184	349	183	12	289	221	288	222	288	222
454.calculix	12	500	198	500	198	500	198	12	441	225	441	225	441	225
459.GemsFDTD	12	1940	65.6	1942	65.6	1962	64.9	12	1917	66.4	1915	66.5	1916	66.4
465.tonto	12	753	157	752	157	754	157	12	621	190	621	190	631	187
470.lbm	12	2642	62.4	2665	61.9	2641	62.4	12	2641	62.4	2626	62.8	2633	62.6
481.wrf	12	1104	121	1100	122	1104	121	12	1072	125	1068	126	1070	125
482.sphinx3	12	1483	158	1481	158	1482	158	12	1376	170	1385	169	1378	170

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm.nr_hugepages=5400 in /etc/sysctl.conf
 mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

Environment variables set by runspec before the start of the run:

```
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd0905is-libs/64:/root/cpu2006-1.1/amd0905is-libs/32"
NCPUS = "6"
PGI_HUGE_PAGES = "450"
```

<http://developer.amd.com/cpu/open64>

Base Compiler Invocation

C benchmarks:

pgcc

C++ benchmarks:

pgcpp

Fortran benchmarks:

pgf95

Benchmarks using both Fortran and C:

pgcc pgf95

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

```
-fastsse -Msmartralloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Bstatic_pgi
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fastsse -Msmartalloc=huge -Mfprelaxed --zc_eh -Mipa=fast  
-Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

Fortran benchmarks:

```
-fastsse -Msmartalloc=huge -Mfprelaxed -Mvect=short -Mipa=fast  
-Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

Benchmarks using both Fortran and C:

```
-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline  
-tp shanghai-64 -Mvect=short -Bstatic_pgi
```

Base Other Flags

C benchmarks:

```
-Mipa=jobs:4
```

C++ benchmarks:

```
-Mipa=jobs:4
```

Fortran benchmarks:

```
-Mipa=jobs:4
```

Benchmarks using both Fortran and C:

```
-Mipa=jobs:4
```

Peak Compiler Invocation

C benchmarks:

```
pgcc
```

C++ benchmarks (except as noted below):

```
openCC
```

```
444.namd: pgcpp
```

Fortran benchmarks (except as noted below):

```
openf95
```

```
410.bwaves: pgf95
```

```
434.zeusmp: pgf95
```

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Peak Compiler Invocation (Continued)

437.leslie3d: pgf95

Benchmarks using both Fortran and C (except as noted below):

pgcc pgf95

435.gromacs: opencc openf95

Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
    437.leslie3d: -DSPEC_CPU_LP64
        444.namd: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -fastsse -Msmartralloc=huge -Mprefetch=t0 -Mloop32
 -Mfprelaxed -Mipa=fast -Mipa=inline -tp shanghai-64
 -Bstatic_pgi

482.sphinx3: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
 -Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
 -Mfprelaxed -Msmartralloc -tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

444.namd: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
 -Mipa=inline(pass 2) -fastsse -Munroll=n:4 -Munroll=m:8
 -Msmartralloc=huge -Mnodepchk -Mfprelaxed --zc_eh
 -tp shanghai-64 -Bstatic_pgi

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Peak Optimization Flags (Continued)

```
447.dealII: -march=barcelona -Ofast -static -INLINE:aggressive=on
             -LNO:opt=0 -WF,-fno-exceptions -m32 -OPT:unroll_times_max=8
             -OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
             -GRA:unspill=on -CG:cmp_peep=on -TENV:frame_pointer=off
```

```
450.soplex: -march=barcelona -fb_create fbdata(pass 1)
             -fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
             -OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
             -OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
             -CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m
```

```
453.povray: -march=barcelona -fb_create fbdata(pass 1)
             -fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on
             -HP:bdt=2m:heap=2m
```

Fortran benchmarks:

```
410.bwaves: -fastsse -Msmartralloc -Mprefetch=nta -Mfprelaxed
             -Mipa=fast -Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

```
416.gamess: -march=barcelona -fb_create fbdata(pass 1)
             -fb_opt fbdata(pass 2) -O2 -OPT:Ofast -OPT:ro=3
             -OPT:unroll_size=256 -HP:bdt=2m:heap=2m
```

```
434.zeusmp: -fastsse -Mfprelaxed -Mprefetch=distance:8 -Mprefetch=t0
             -Msmartralloc=huge -Msmartralloc=hugebss -Mipa=fast
             -Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

```
437.leslie3d: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
               -Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
               -Mvect=fuse -Msmartralloc=huge -Mprefetch=distance:8
               -Mprefetch=t0 -Mfprelaxed -tp shanghai-64 -Bstatic_pgi
```

```
459.GemsFDTD: -march=barcelona -Ofast -LNO:fission=2 -LNO:simd=2
                -LNO:prefetch_ahead=1 -CG:load_exe=0 -HP
```

```
465.tonto: -march=barcelona -Ofast -OPT:alias=no_f90_pointer_alias
             -LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP
```

Benchmarks using both Fortran and C:

```
435.gromacs: -march=barcelona -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m
```

```
436.cactusADM: -fastsse -Mconcur -Msmartralloc=huge -Mfprelaxed -Mipa=fast
                 -Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

```
454.calculix: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
               -Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
               -Mvect=short -Msmartralloc=huge -Mprefetch=t0 -Mpre
               -Mfprelaxed -tp shanghai-64 -Bstatic_pgi
```

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Peak Optimization Flags (Continued)

```
481.wrf: -fastsse -Mvect=noaltcode -Msmartalloc=huge  
          -Mprefetch=distance:8 -Mfrelaxed -tp shanghai-64  
          -Bstatic_pgi
```

Peak Other Flags

C benchmarks:

```
-Mipa=jobs:4(pass 2)
```

C++ benchmarks:

```
444.namd: -Mipa=jobs:4(pass 2)
```

Fortran benchmarks:

```
410.bwaves: -Mipa=jobs:4
```

```
434.zeusmp: -Mipa=jobs:4
```

```
437.leslie3d: -Mipa=jobs:4(pass 2)
```

Benchmarks using both Fortran and C:

```
436.cactusADM: -Mipa=jobs:4
```

```
454.calculix: -Mipa=jobs:4(pass 2)
```

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090710.html
<http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revB.html>
<http://www.spec.org/cpu2006/flags/amd-platform.html>

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090710.xml
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