



SPEC® CFP2006 Result

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IBM Corporation

SPECfp®_rate2006 = 26.4

IBM System x3200 M2 (Intel Pentium E5300)

SPECfp_rate_base2006 = 26.5

CPU2006 license: 11

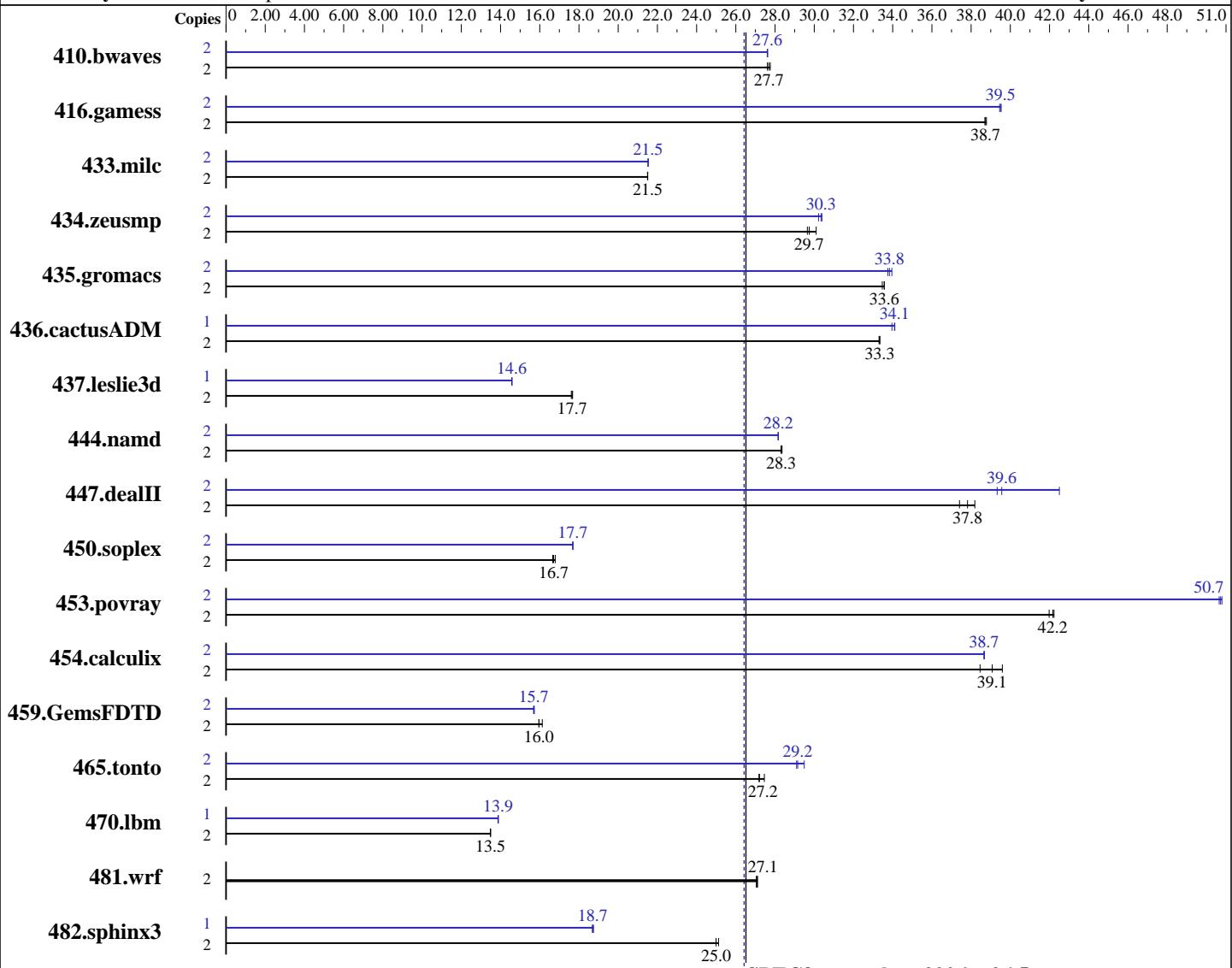
Test date: Jun-2009

Test sponsor: IBM Corporation

Hardware Availability: Apr-2009

Tested by: IBM Corporation

Software Availability: Nov-2008



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Hardware

CPU Name: Intel Pentium E5300
CPU Characteristics: 800 MHz system bus
CPU MHz: 2600
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per chip

Software

Operating System: SuSE Linux Enterprise Server 10(x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: None
 Other Cache: None
 Memory: 8 GB(4 x 2 GB DDR2-6400E ECC)
 Disk Subsystem: 1 x 250 GB SATA, 7200RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|--------|------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 2 | 979 | 27.8 | 982 | 27.7 | 984 | 27.6 | 2 | 985 | 27.6 | 984 | 27.6 | 984 | 27.6 |
| 416.gamess | 2 | 1011 | 38.7 | 1010 | 38.8 | 1012 | 38.7 | 2 | 991 | 39.5 | 991 | 39.5 | 993 | 39.5 |
| 433.milc | 2 | 854 | 21.5 | 854 | 21.5 | 854 | 21.5 | 2 | 853 | 21.5 | 853 | 21.5 | 853 | 21.5 |
| 434.zeusmp | 2 | 605 | 30.1 | 614 | 29.6 | 612 | 29.7 | 2 | 599 | 30.4 | 602 | 30.2 | 600 | 30.3 |
| 435.gromacs | 2 | 427 | 33.5 | 425 | 33.6 | 425 | 33.6 | 2 | 422 | 33.8 | 421 | 34.0 | 423 | 33.8 |
| 436.cactusADM | 2 | 717 | 33.3 | 718 | 33.3 | 717 | 33.3 | 1 | 352 | 34.0 | 350 | 34.1 | 351 | 34.1 |
| 437.leslie3d | 2 | 1065 | 17.7 | 1064 | 17.7 | 1068 | 17.6 | 1 | 645 | 14.6 | 645 | 14.6 | 645 | 14.6 |
| 444.namd | 2 | 567 | 28.3 | 566 | 28.4 | 567 | 28.3 | 2 | 569 | 28.2 | 569 | 28.2 | 570 | 28.2 |
| 447.dealII | 2 | 612 | 37.4 | 599 | 38.2 | 605 | 37.8 | 2 | 538 | 42.5 | 578 | 39.6 | 582 | 39.3 |
| 450.soplex | 2 | 993 | 16.8 | 998 | 16.7 | 1001 | 16.7 | 2 | 943 | 17.7 | 943 | 17.7 | 943 | 17.7 |
| 453.povray | 2 | 252 | 42.2 | 253 | 42.0 | 252 | 42.2 | 2 | 209 | 50.8 | 210 | 50.7 | 210 | 50.7 |
| 454.calculix | 2 | 417 | 39.6 | 429 | 38.5 | 422 | 39.1 | 2 | 427 | 38.7 | 427 | 38.7 | 427 | 38.7 |
| 459.GemsFDTD | 2 | 1330 | 16.0 | 1329 | 16.0 | 1316 | 16.1 | 2 | 1353 | 15.7 | 1352 | 15.7 | 1350 | 15.7 |
| 465.tonto | 2 | 717 | 27.5 | 723 | 27.2 | 724 | 27.2 | 2 | 667 | 29.5 | 675 | 29.2 | 676 | 29.1 |
| 470.lbm | 2 | 2037 | 13.5 | 2038 | 13.5 | 2038 | 13.5 | 1 | 990 | 13.9 | 990 | 13.9 | 990 | 13.9 |
| 481.wrf | 2 | 825 | 27.1 | 824 | 27.1 | 826 | 27.0 | 2 | 825 | 27.1 | 824 | 27.1 | 826 | 27.0 |
| 482.sphinx3 | 2 | 1560 | 25.0 | 1552 | 25.1 | 1560 | 25.0 | 1 | 1040 | 18.7 | 1043 | 18.7 | 1042 | 18.7 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

taskset was used to bind processes to cores except for 436.cactusADM peak

Hardware Sector Prefetch Enable and Adjacent Sector Prefetch Enable

OMP_NUM_THREADS set to number of cores

KMP_AFFINITY set to "physical,0"

KMP_STACKSIZE set to 64M



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -Obo -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -Obo -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll12 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.xml>

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