SPEC® CFP2006 Result

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Dell Inc.

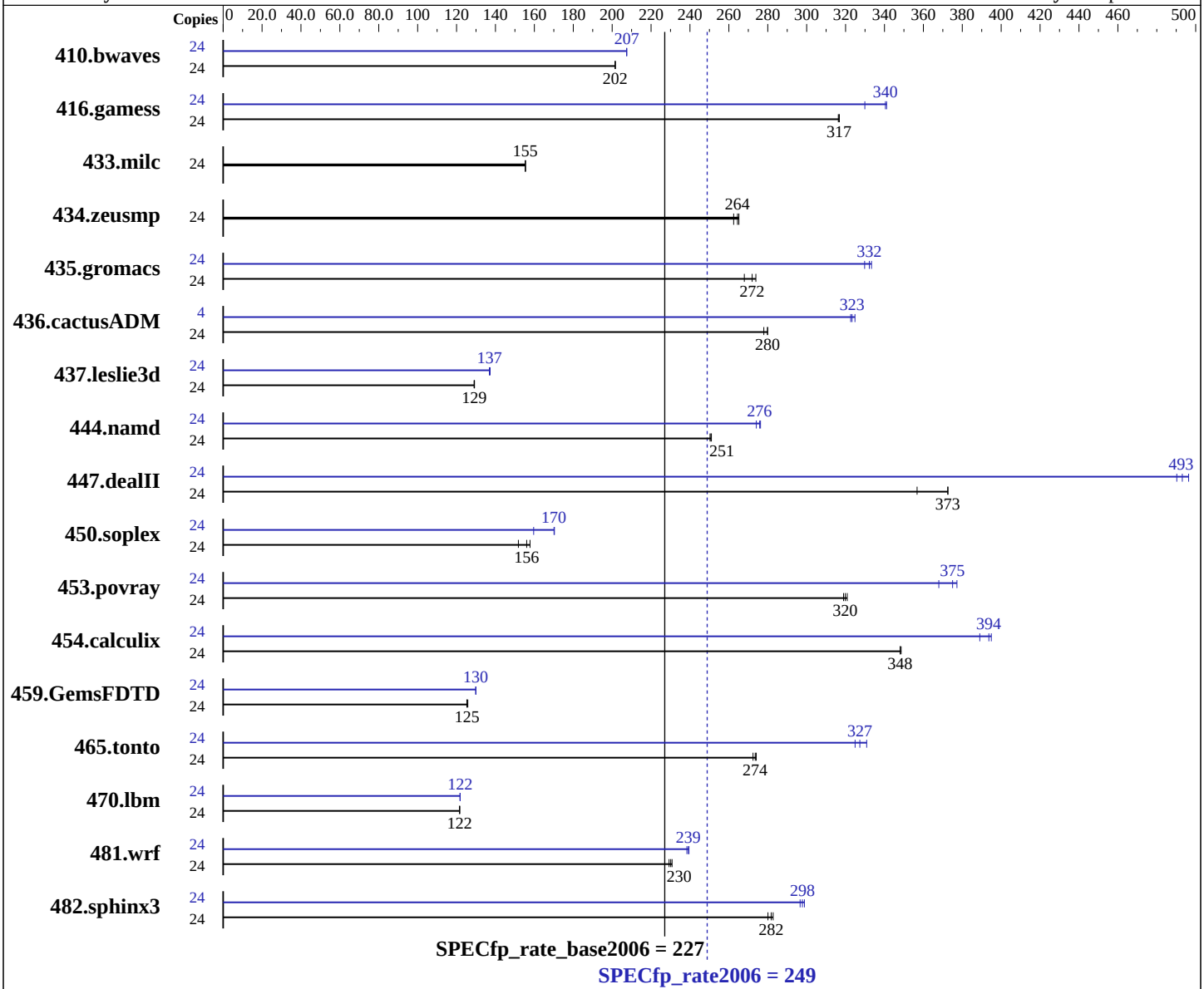
PowerEdge M905 (AMD Opteron 8425 HE, 2.10 GHz)

SPECfp®_rate2006 = 249

SPECfp_rate_base2006 = 227

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Aug-2009
Hardware Availability: Aug-2009
Software Availability: Apr-2009



Hardware

CPU Name:	AMD Opteron 8425 HE
CPU Characteristics:	
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip
CPU(s) orderable:	4 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 5.3, Kernel 2.6.18-128.el5
Compiler:	PGI Server Complete Version 8.0 x86 Open64 4.2.2 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16 x 4 GB DDR2-800)
Disk Subsystem: 1 x 73 GB 15000 RPM SAS
Other Hardware: None

Other Software: binutils 2.18

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	**1618**	**202**	1618	202	1619	201	24	1572	207	**1572**	**207**	1572	207
416.gamess	24	**1484**	**317**	1483	317	1486	316	24	1424	330	1378	341	**1380**	**340**
433.milc	24	1418	155	**1418**	**155**	1418	155	24	1418	155	**1418**	**155**	1418	155
434.zeusmp	24	832	262	**826**	**264**	824	265	24	832	262	**826**	**264**	824	265
435.gromacs	24	**630**	**272**	640	268	626	274	24	**516**	**332**	520	330	514	333
436.cactusADM	24	1032	278	**1025**	**280**	1025	280	4	148	323	147	325	**148**	**323**
437.leslie3d	24	**1747**	**129**	1746	129	1749	129	24	**1647**	**137**	1644	137	1648	137
444.namd	24	769	250	**768**	**251**	767	251	24	702	274	**698**	**276**	697	276
447.dealII	24	**737**	**373**	737	373	770	357	24	560	490	553	496	**557**	**493**
450.soplex	24	1319	152	**1283**	**156**	1269	158	24	1254	160	**1177**	**170**	1175	170
453.povray	24	398	321	**399**	**320**	400	319	24	338	377	**340**	**375**	347	368
454.calculix	24	569	348	568	349	**569**	**348**	24	**503**	**394**	501	395	509	389
459.GemsFDTD	24	2033	125	**2031**	**125**	2024	126	24	1961	130	1959	130	**1961**	**130**
465.tonto	24	867	272	862	274	**863**	**274**	24	727	325	714	331	**721**	**327**
470.lbm	24	**2712**	**122**	2712	122	2713	122	24	2706	122	2709	122	**2706**	**122**
481.wrf	24	**1165**	**230**	1162	231	1169	229	24	1119	239	**1121**	**239**	1124	238
482.sphinx3	24	**1660**	**282**	1670	280	1654	283	24	**1571**	**298**	1565	299	1577	297

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=10800 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Platform Notes

```
HyperTransport Technology = HT 1 (Default = HT 3)
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd0905is-libs/64:/root/cpu2006-1.1/amd0905is-libs/32"
NCPUS = "6"
PGI_HUGE_PAGES = "450"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
```

Base Compiler Invocation

C benchmarks:
pgcc

C++ benchmarks:
pgcpp

Fortran benchmarks:
pgf95

Benchmarks using both Fortran and C:
pgcc pgf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -Mnomain
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Bstatic_pgi
```

C++ benchmarks:
```
-fastsse -Msmartalloc=huge -Mfprelaxed --zc_eh -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

Fortran benchmarks:
```
-fastsse -Msmartalloc=huge -Mfprelaxed -Mvect=short -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi
```

Benchmarks using both Fortran and C:
```
-fastsse -Msmartalloc=huge -Mfprelaxed -Mipa=fast -Mipa=inline
-tp shanghai-64 -Mvect=short -Bstatic_pgi
```

Base Other Flags

C benchmarks:
```
-Mipa=jobs:4
```

C++ benchmarks:
```
-Mipa=jobs:4
```

Fortran benchmarks:
```
-Mipa=jobs:4
```

Benchmarks using both Fortran and C:
```
-Mipa=jobs:4
```

Peak Compiler Invocation

C benchmarks:
```
pgcc
```

C++ benchmarks (except as noted below):
```
openCC
```

444.namd: `pgcpp`

Fortran benchmarks (except as noted below):
```
openf95
```

410.bwaves: `pgf95`

Continued on next page

Peak Compiler Invocation (Continued)

434.zeusmp: pgf95

437.leslie3d: pgf95

Benchmarks using both Fortran and C (except as noted below):
pgcc pgf95

435.gromacs: opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -Mnomain
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -Mnomain
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -fastsse -Msmartalloc=huge -Mprefetch=t0 -Mloop32
-Mfprelaxed -Mipa=fast -Mipa=inline -tp shanghai-64
-Bstatic_pgi

482.sphinx3: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mfprelaxed -Msmartalloc -tp shanghai-64 -Bstatic_pgi

C++ benchmarks:

444.namd: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -fastsse -Munroll=n:4 -Munroll=m:8
-Msmartalloc=huge -Mnodepchk -Mfprelaxed --zc_eh
-tp shanghai-64 -Bstatic_pgi

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Peak Optimization Flags (Continued)

447.dealII: -march=barcelona -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -Wf,-fno-exceptions -m32 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
-GRA:unspill=on -CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on
-HP:bdt=2m:heap=2m

Fortran benchmarks:

410.bwaves: -fastsse -Msmartalloc -Mprefetch=nta -Mfprelaxed
-Mipa=fast -Mipa=inline -tp shanghai-64 -Bstatic_pgi

416.gamess: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O2 -OPT:Ofast -OPT:ro=3
-OPT:unroll_size=256 -HP:bdt=2m:heap=2m

434.zeusmp: basepeak = yes

437.leslie3d: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=fuse -Msmartalloc=huge -Mprefetch=distance:8
-Mprefetch=t0 -Mfprelaxed -tp shanghai-64 -Bstatic_pgi

459.GemsFDTD: -march=barcelona -Ofast -LNO:fission=2 -LNO:simd=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -HP

465.tonto: -march=barcelona -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m

436.cactusADM: -fastsse -Mconcur -Msmartalloc=huge -Mfprelaxed -Mipa=fast
-Mipa=inline -tp shanghai-64 -Bstatic_pgi

454.calculix: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Mvect=short -Msmartalloc=huge -Mprefetch=t0 -Mpre
-Mfprelaxed -tp shanghai-64 -Bstatic_pgi

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Peak Optimization Flags (Continued)

481.wrf: `-fastsse -Mvect=noaltcode -Msmartalloc=huge -Mprefetch=distance:8 -Mfprelaxed -tp shanghai-64 -Bstatic_pgi`

Peak Other Flags

C benchmarks:
`-Mipa=jobs:4`(pass 2)

C++ benchmarks:

444.namd: `-Mipa=jobs:4`(pass 2)

Fortran benchmarks:

410.bwaves: `-Mipa=jobs:4`

434.zeusmp: `-Mipa=jobs:4`

437.leslie3d: `-Mipa=jobs:4`(pass 2)

Benchmarks using both Fortran and C:

436.cactusADM: `-Mipa=jobs:4`

454.calculix: `-Mipa=jobs:4`(pass 2)

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090914.html`
`http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revE.html`
`http://www.spec.org/cpu2006/flags/amd-platform.20090914.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/pgi80_linux_flags.20090914.xml`
`http://www.spec.org/cpu2006/flags/x86-open64-4.2.2-flags-revE.xml`
`http://www.spec.org/cpu2006/flags/amd-platform.20090914.xml`

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