



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX620 S5, Intel Xeon X5560, 2.80 GHz

SPECfp®2006 = 39.3

SPECfp_base2006 = 36.7

CPU2006 license: 19

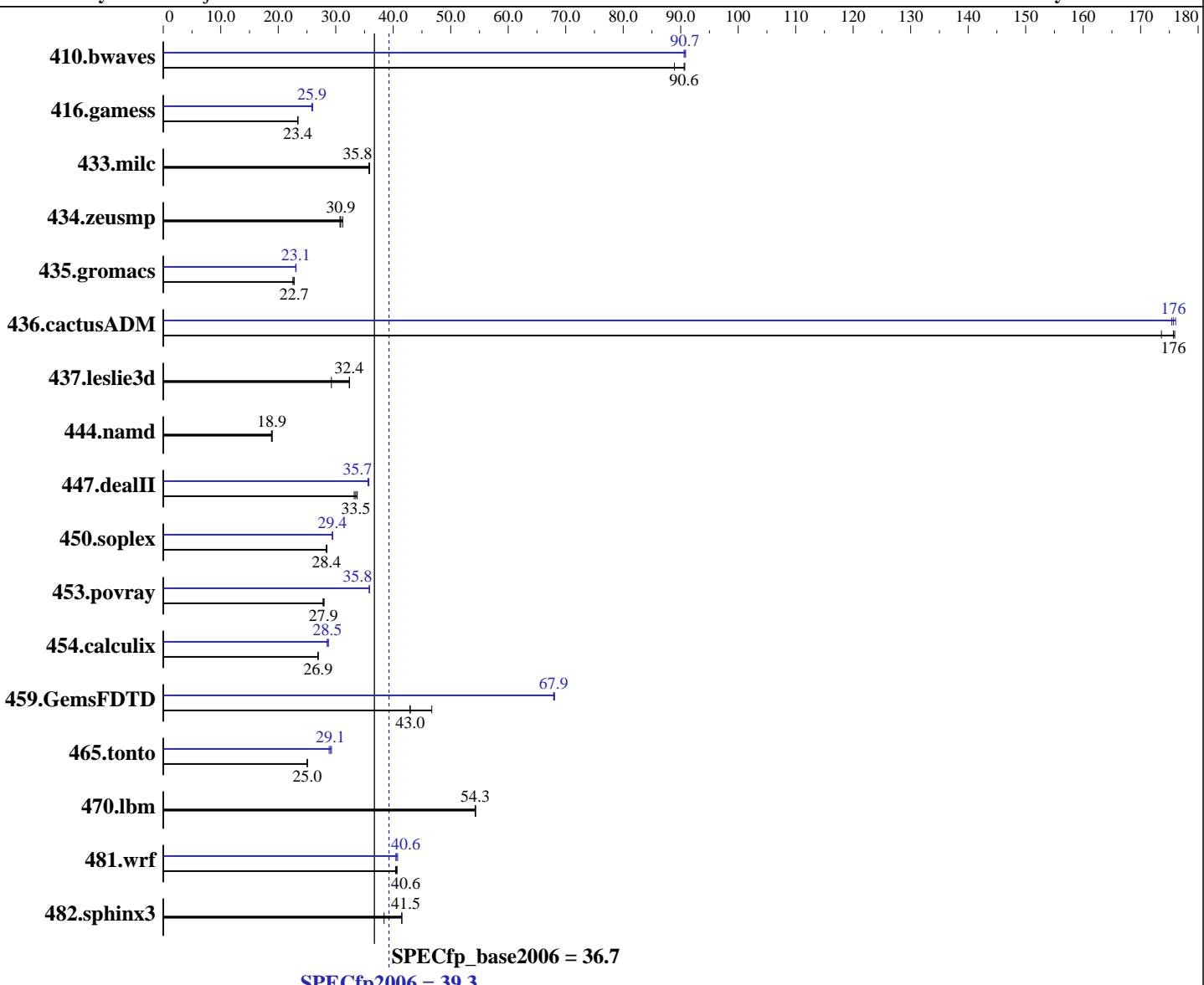
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Oct-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5560
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
Auto Parallel: Yes
File System: ext3
System State: Multi-User Run Level 3
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	153	88.9	<u>150</u>	<u>90.6</u>	150	90.7	<u>150</u>	<u>90.7</u>	150	90.6	150	90.9
416.gamess	837	23.4	<u>836</u>	<u>23.4</u>	836	23.4	<u>758</u>	<u>25.8</u>	<u>756</u>	<u>25.9</u>	754	26.0
433.milc	256	35.9	<u>256</u>	<u>35.8</u>	256	35.8	<u>256</u>	<u>35.9</u>	<u>256</u>	<u>35.8</u>	256	35.8
434.zeusmp	<u>295</u>	<u>30.9</u>	296	30.7	292	31.2	<u>295</u>	<u>30.9</u>	296	30.7	292	31.2
435.gromacs	314	22.7	<u>314</u>	<u>22.7</u>	317	22.5	<u>309</u>	<u>23.1</u>	310	23.0	309	23.1
436.cactusADM	67.9	176	68.8	174	<u>68.0</u>	<u>176</u>	68.1	175	<u>68.0</u>	<u>176</u>	67.9	176
437.leslie3d	290	32.4	<u>291</u>	<u>32.4</u>	321	29.3	<u>290</u>	<u>32.4</u>	<u>291</u>	<u>32.4</u>	321	29.3
444.namd	426	18.8	<u>424</u>	<u>18.9</u>	423	19.0	<u>426</u>	<u>18.8</u>	<u>424</u>	<u>18.9</u>	423	19.0
447.dealII	344	33.2	339	33.7	<u>342</u>	<u>33.5</u>	321	35.6	<u>321</u>	<u>35.7</u>	320	35.8
450.soplex	293	28.5	<u>294</u>	<u>28.4</u>	294	28.3	<u>284</u>	<u>29.4</u>	283	29.5	<u>284</u>	<u>29.4</u>
453.povray	<u>191</u>	<u>27.9</u>	191	27.8	190	28.0	<u>148</u>	35.9	148	35.8	<u>148</u>	<u>35.8</u>
454.calculix	306	26.9	<u>306</u>	<u>26.9</u>	307	26.9	<u>287</u>	<u>28.7</u>	<u>289</u>	<u>28.5</u>	289	28.5
459.GemsFDTD	227	46.7	247	42.9	<u>247</u>	<u>43.0</u>	156	67.9	156	68.1	<u>156</u>	<u>67.9</u>
465.tonto	393	25.0	392	25.1	<u>393</u>	<u>25.0</u>	<u>338</u>	<u>29.1</u>	341	28.9	336	29.3
470.lbm	253	54.3	<u>253</u>	<u>54.3</u>	253	54.3	<u>253</u>	<u>54.3</u>	<u>253</u>	<u>54.3</u>	253	54.3
481.wrf	276	40.4	275	40.7	<u>275</u>	<u>40.6</u>	276	40.4	274	40.8	<u>275</u>	<u>40.6</u>
482.sphinx3	470	41.5	507	38.4	<u>470</u>	<u>41.5</u>	<u>470</u>	<u>41.5</u>	507	38.4	<u>470</u>	<u>41.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M

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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks (except as noted below):
icpc
450.soplex: icpc -m32

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

```
447.dealII: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -unroll2 -ansi-alias -scalar-rep -opt-prefetch
```

```
450.soplex: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
             -opt-malloc-options=3
```

```
453.povray: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll4 -ansi-alias
```

Fortran benchmarks:

```
410.bwaves: -xsSE4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch
             -parallel
```

```
416.gamess: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll2 -Ob0 -ansi-alias -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                 -unroll2 -Ob0 -opt-prefetch -parallel
```

```
465.tonto: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
              -unroll4 -auto
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                -opt-prefetch -auto-ilp32
```

```
436.cactusADM: -xsSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                  -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
                  -unroll2 -opt-prefetch -parallel -auto-ilp32
```

454.calculix: -xsSE4 .2 -ipo -O3 -no-prec-div -static -auto-ilp32

```
481.wrf: -xsSE4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch
            -parallel -auto-ilp32
```



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20091013.xml>

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