SPEC® CFP2006 Result

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Acer Incorporated

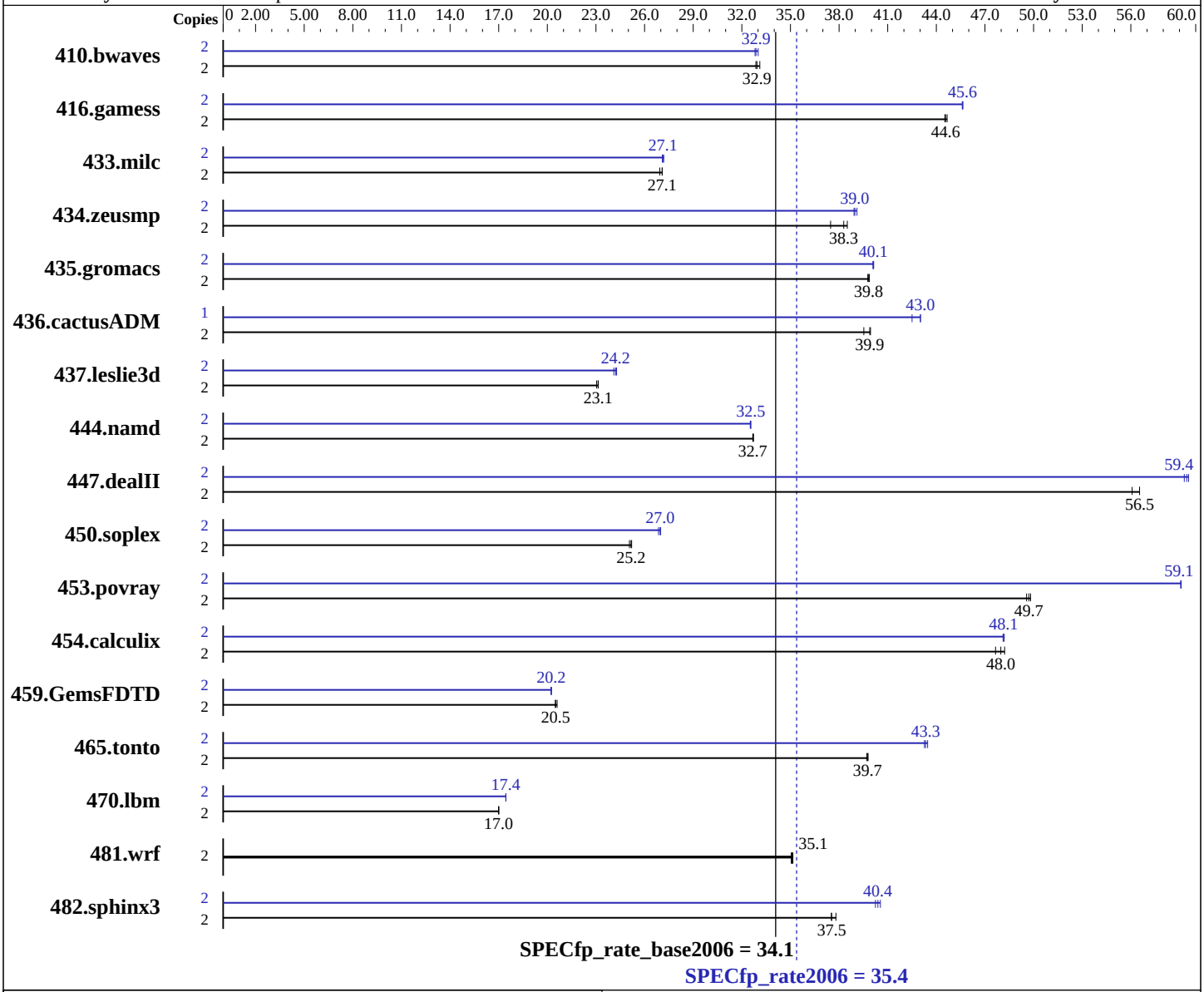
Gateway GT110 F1 (Intel Xeon E3110)

SPECfp®_rate2006 = 35.4

SPECfp_rate_base2006 = 34.1

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Nov-2009
Hardware Availability: Dec-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E3110
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066, l_cprof_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, level 3
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1
Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	821	33.1	**825**	**32.9**	827	32.9	2	828	32.8	823	33.0	**827**	**32.9**
416.gamess	2	**879**	**44.6**	880	44.5	877	44.7	2	858	45.6	859	45.6	**858**	**45.6**
433.milc	2	682	26.9	678	27.1	**678**	**27.1**	2	**677**	**27.1**	675	27.2	678	27.1
434.zeusmp	2	473	38.5	486	37.5	**475**	**38.3**	2	**467**	**39.0**	465	39.1	468	38.9
435.gromacs	2	**359**	**39.8**	358	39.9	359	39.8	2	**356**	**40.1**	356	40.1	356	40.1
436.cactusADM	2	605	39.5	598	39.9	**599**	**39.9**	1	**278**	**43.0**	278	43.0	281	42.5
437.leslie3d	2	816	23.0	813	23.1	**813**	**23.1**	2	774	24.3	**776**	**24.2**	780	24.1
444.namd	2	491	32.7	490	32.7	**491**	**32.7**	2	493	32.5	493	32.6	**493**	**32.5**
447.dealII	2	408	56.1	405	56.5	**405**	**56.5**	2	386	59.3	**385**	**59.4**	384	59.6
450.soplex	2	665	25.1	**663**	**25.2**	662	25.2	2	**619**	**27.0**	618	27.0	621	26.9
453.povray	2	214	49.8	215	49.6	**214**	**49.7**	2	180	59.1	180	59.1	**180**	**59.1**
454.calculix	2	346	47.7	**344**	**48.0**	342	48.2	2	343	48.1	**343**	**48.1**	342	48.2
459.GemsFDTD	2	1036	20.5	**1034**	**20.5**	1030	20.6	2	1049	20.2	1048	20.3	**1049**	**20.2**
465.tonto	2	495	39.8	**495**	**39.7**	496	39.7	2	**454**	**43.3**	455	43.3	453	43.5
470.lbm	2	1616	17.0	1618	17.0	**1617**	**17.0**	2	1575	17.4	**1576**	**17.4**	1576	17.4
481.wrf	2	636	35.2	**637**	**35.1**	637	35.1	2	636	35.2	**637**	**35.1**	637	35.1
482.sphinx3	2	1031	37.8	**1038**	**37.5**	1039	37.5	2	**965**	**40.4**	961	40.5	969	40.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was set for stacksize unlimited
```

General Notes

```
taskset was used to bind processes to cores except
for 436.cactusADM peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

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General Notes (Continued)

```
This result was measured on the Gateway GT110 F1.
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `icpc -m32`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `ifort -m32`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100209.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revF.20100209.xml`

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