



SPEC® CINT2006 Result

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Intel Corporation

Asus P5E3 Pro motherboard (Intel Core 2 Extreme QX9770)

SPECint_rate2006 = 85.5

SPECint_rate_base2006 = 81.0

CPU2006 license: 13

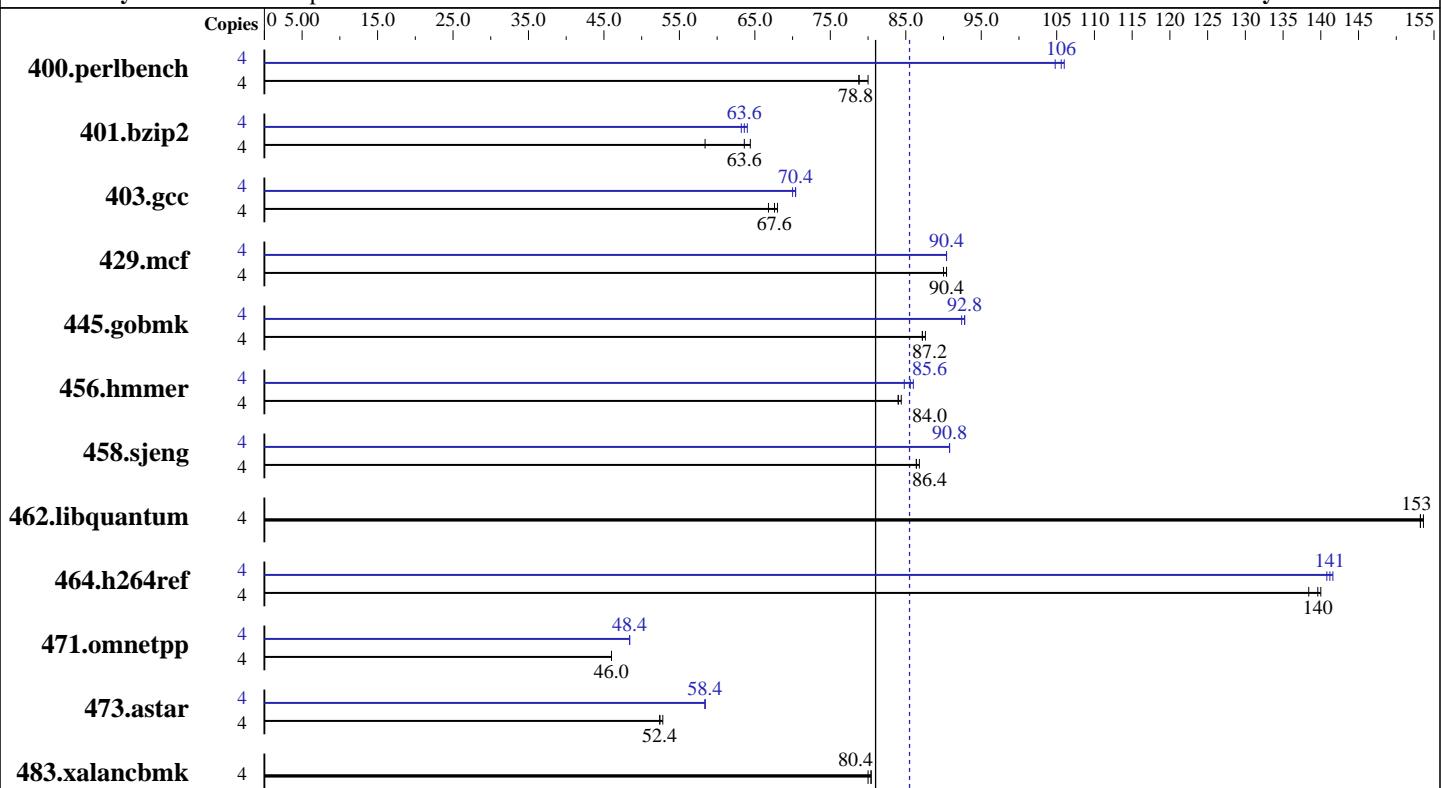
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Feb-2010

Hardware Availability: Jun-2009

Software Availability: Oct-2009



SPECint_rate_base2006 = 81.0

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Hardware

CPU Name:	Intel Core 2 Extreme QX9770
CPU Characteristics:	
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	4 GB (2x2GB Micron MT16JTF25664AZ-1G4 DDR3-1333 CL9)
Disk Subsystem:	Intel X25-M 80GB SSD
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.1 for IA32 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None SmartHeap Library Version 8.1 from http://www.microquill.com/



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	488	80.0	496	78.8	496	78.8	4	373	105	370	106	368	106
401.bzip2	4	606	63.6	659	58.4	598	64.4	4	606	63.6	604	64.0	611	63.2
403.gcc	4	475	67.6	481	66.8	472	68.0	4	457	70.4	460	70.0	458	70.4
429.mcf	4	404	90.4	405	90.0	404	90.4	4	403	90.4	404	90.4	404	90.4
445.gobmk	4	481	87.2	480	87.6	481	87.2	4	454	92.4	453	92.8	453	92.8
456.hammer	4	443	84.0	443	84.4	444	84.0	4	439	84.8	436	85.6	435	86.0
458.sjeng	4	560	86.4	559	86.4	558	86.8	4	534	90.8	533	90.8	533	90.8
462.libquantum	4	540	154	540	153	540	153	4	540	154	540	153	540	153
464.h264ref	4	632	140	639	138	635	140	4	629	141	627	141	626	142
471.omnetpp	4	541	46.0	541	46.0	542	46.0	4	518	48.4	517	48.4	518	48.4
473.astar	4	535	52.4	534	52.4	533	52.8	4	480	58.4	482	58.4	482	58.4
483.xalancbmk	4	345	80.0	344	80.4	344	80.4	4	345	80.0	344	80.4	344	80.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
Windows start command was used to bind copies to processors

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
System was configured with nVidia GTX 280 discrete graphics card

Base Compiler Invocation

C benchmarks:

 icl -Qvc9 -Qstd=c99

C++ benchmarks:

 icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES

483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features /F512000000 shlw32M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32

464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES

483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll12 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll14 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll12 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32M.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gnu: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.xml>



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Tested with SPEC CPU2006 v1.1.

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