SPEC® CFP2006 Result

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Acer Incorporated

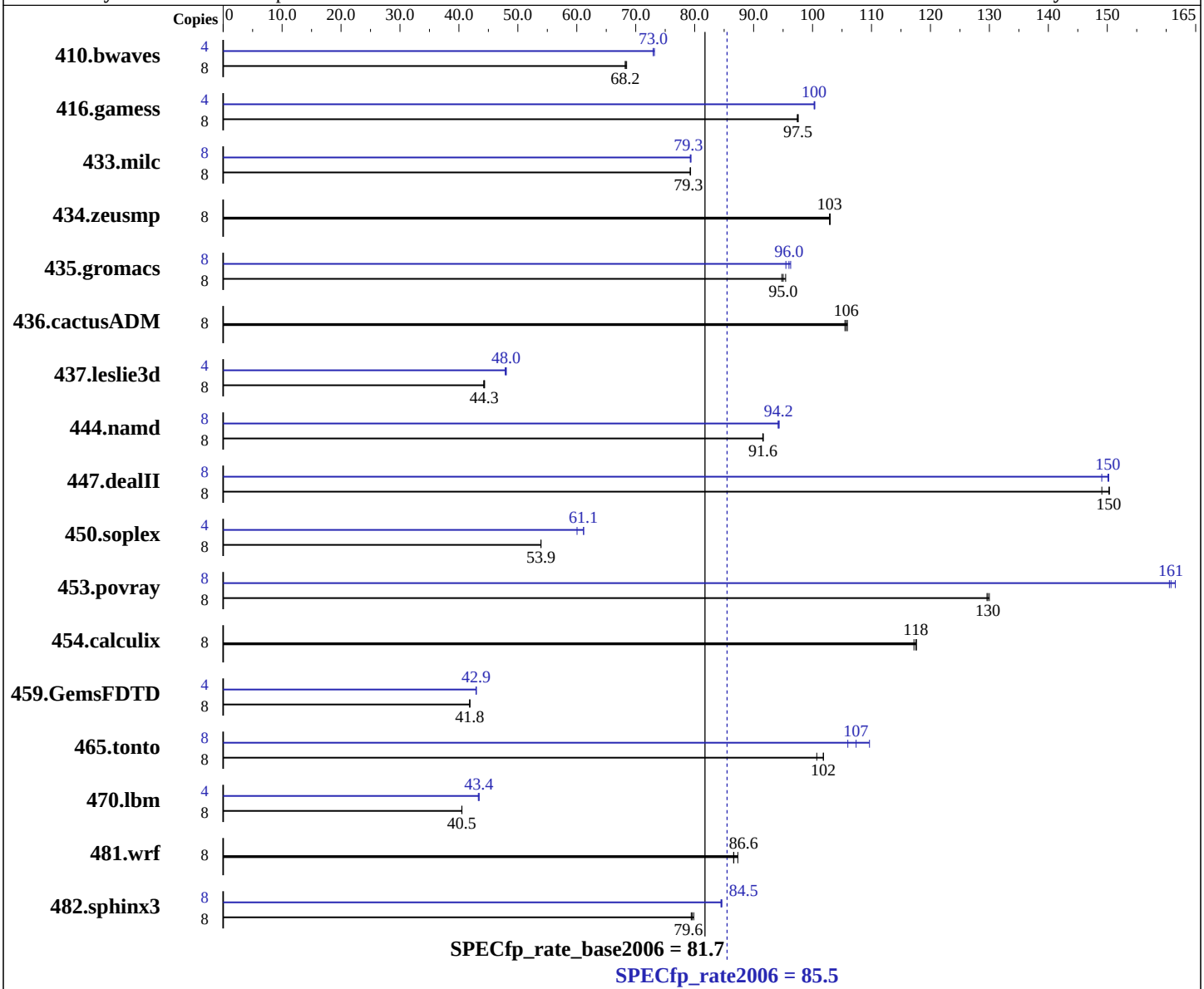
Gateway GR320 F1 (Intel Xeon X3470)

SPECfp®_rate2006 = 85.5

SPECfp_rate_base2006 = 81.7

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon X3470
CPU Characteristics:	Intel Turbo Boost Technology up to 3.6 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Sserver 11 (x86_64) Kernel 2.6.27.19-5
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, level 3

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4 x 4096 MB ECC Unbuffered DDR3-1333)
Disk Subsystem: 1 x 1000 GB 7200RPM SATA HDD
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1593	68.2	1588	68.5	**1593**	**68.2**	4	**745**	**73.0**	743	73.2	745	73.0
416.gamess	8	1608	97.4	1605	97.6	**1606**	**97.5**	4	780	100	781	100	**781**	**100**
433.milc	8	927	79.2	926	79.3	**927**	**79.3**	8	**926**	**79.3**	927	79.3	925	79.4
434.zeusmp	8	707	103	**707**	**103**	708	103	8	707	103	**707**	**103**	708	103
435.gromacs	8	599	95.4	603	94.8	**601**	**95.0**	8	**595**	**96.0**	598	95.5	593	96.3
436.cactusADM	8	903	106	906	105	**904**	**106**	8	903	106	906	105	**904**	**106**
437.leslie3d	8	1695	44.4	**1697**	**44.3**	1701	44.2	4	783	48.0	786	47.8	**783**	**48.0**
444.namd	8	701	91.5	**701**	**91.6**	700	91.7	8	**681**	**94.2**	680	94.3	681	94.2
447.dealII	8	609	150	614	149	**609**	**150**	8	609	150	**610**	**150**	614	149
450.soplex	8	1237	53.9	1237	53.9	**1237**	**53.9**	4	556	60.0	**546**	**61.1**	545	61.2
453.povray	8	327	130	**328**	**130**	328	130	8	263	162	265	161	**265**	**161**
454.calculix	8	**562**	**118**	561	118	563	117	8	**562**	**118**	561	118	563	117
459.GemsFDTD	8	**2029**	**41.8**	2029	41.8	2028	41.9	4	989	42.9	988	43.0	**989**	**42.9**
465.tonto	8	**773**	**102**	773	102	782	101	8	743	106	718	110	**733**	**107**
470.lbm	8	2713	40.5	**2714**	**40.5**	2715	40.5	4	1270	43.3	1266	43.4	**1266**	**43.4**
481.wrf	8	1023	87.3	1032	86.6	**1031**	**86.6**	8	1023	87.3	1032	86.6	**1031**	**86.6**
482.sphinx3	8	1963	79.4	1953	79.8	**1959**	**79.6**	8	1846	84.5	**1845**	**84.5**	1843	84.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was set for stacksize unlimited
```

General Notes

```
This result was measured on the Gateway GT310 F1.
These 4 systems are electronically equivalent:
Acer AR320 F1, Acer AT310 F1, Gateway GR320 F1, and Gateway GT310 F1.
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-fno-alias -opt-prefetch`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3 -ansi-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml`

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