SPEC® CFP2006 Result

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Fujitsu

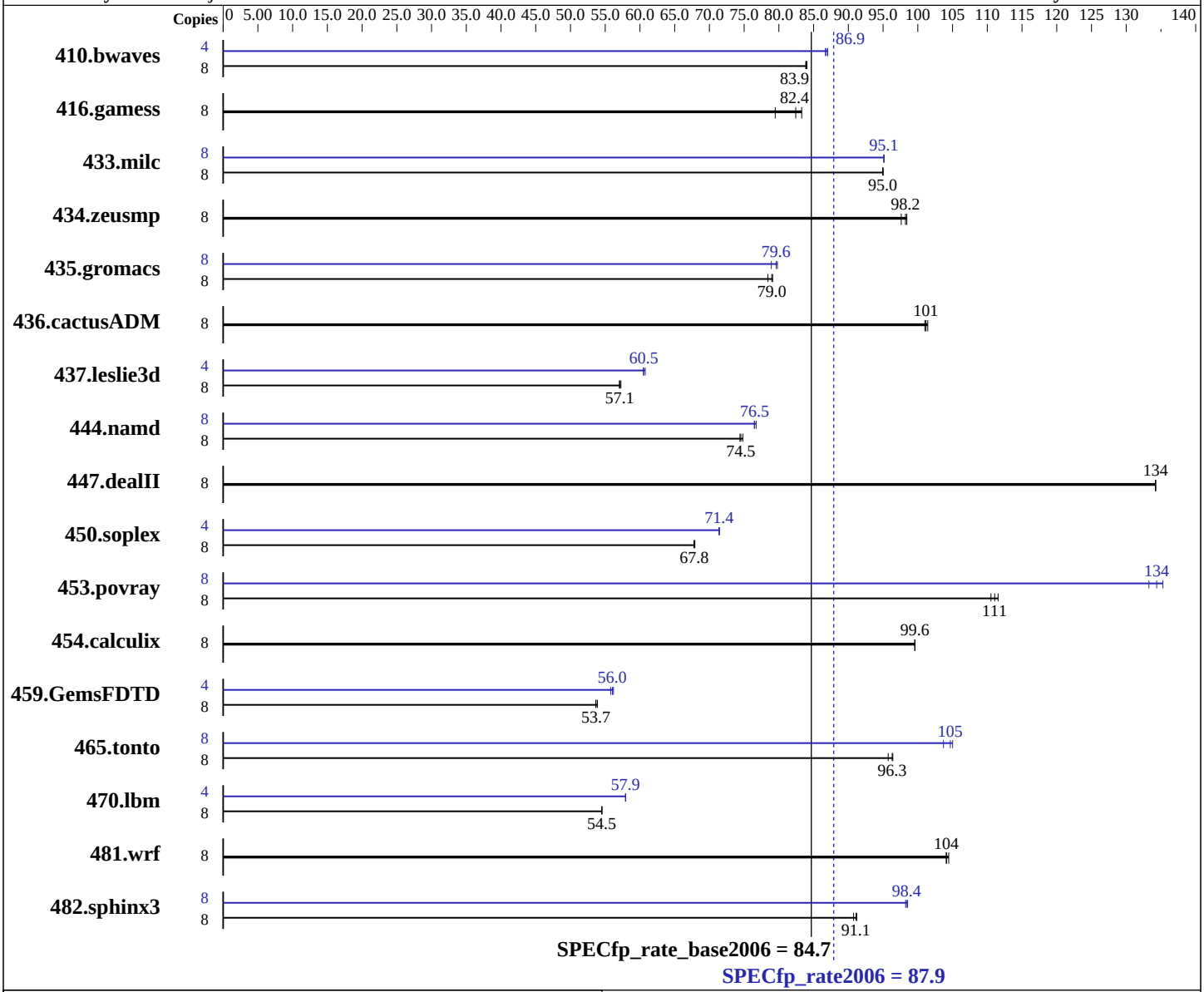
PRIMERGY TX300 S6, Intel Xeon E5620, 2.40 GHz

SPECfp®_rate2006 = 87.9

SPECfp_rate_base2006 = 84.7

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2010
Hardware Availability: Apr-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 250 GB, 5.4 krpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1294	84.0	**1295**	**83.9**	1296	83.9	4	627	86.7	625	87.0	**626**	**86.9**
416.gamess	8	**1900**	**82.4**	1881	83.3	1971	79.5	8	**1900**	**82.4**	1881	83.3	1971	79.5
433.milc	8	773	95.0	774	94.9	**773**	**95.0**	8	772	95.1	772	95.2	**772**	**95.1**
434.zeusmp	8	740	98.4	**741**	**98.2**	746	97.6	8	740	98.4	**741**	**98.2**	746	97.6
435.gromacs	8	722	79.1	**723**	**79.0**	728	78.4	8	724	78.9	**718**	**79.6**	716	79.8
436.cactusADM	8	946	101	**945**	**101**	942	101	8	946	101	**945**	**101**	942	101
437.leslie3d	8	1313	57.3	1318	57.0	**1317**	**57.1**	4	**621**	**60.5**	622	60.5	619	60.8
444.namd	8	**861**	**74.5**	862	74.4	858	74.8	8	836	76.7	839	76.5	**839**	**76.5**
447.dealII	8	**682**	**134**	682	134	682	134	8	**682**	**134**	682	134	682	134
450.soplex	8	983	67.9	**984**	**67.8**	984	67.8	4	467	71.4	467	71.4	**467**	**71.4**
453.povray	8	385	111	382	112	**383**	**111**	8	319	133	**317**	**134**	315	135
454.calculix	8	663	99.6	663	99.6	**663**	**99.6**	8	663	99.6	663	99.6	**663**	**99.6**
459.GemsFDTD	8	1582	53.7	1575	53.9	**1581**	**53.7**	4	**757**	**56.0**	761	55.8	755	56.2
465.tonto	8	817	96.4	**817**	**96.3**	822	95.7	8	759	104	750	105	**752**	**105**
470.lbm	8	2016	54.5	**2016**	**54.5**	2017	54.5	4	949	57.9	**949**	**57.9**	949	57.9
481.wrf	8	856	104	**858**	**104**	858	104	8	856	104	**858**	**104**	858	104
482.sphinx3	8	1710	91.2	**1712**	**91.1**	1718	90.8	8	**1585**	**98.4**	1582	98.5	1587	98.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
The system automatically configures the memory to run at 1066 MHz.
BIOS configuration:
Data Reuse optimization = Disable
```

General Notes

```
This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6
and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-fno-alias -opt-prefetch`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3 -ansi-alias -auto-ilp32`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

447.dealII: `basepeak = yes`

450.soplex: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3`

453.povray: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

416.gamess: `basepeak = yes`

434.zeusmp: `basepeak = yes`

437.leslie3d: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

459.GemsFDTD: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll2 -Ob0`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-static`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.00.xml`

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