



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SPECfp®_rate2006 = 75.2

Motherboard X8SIU-F (Intel Xeon L3426, 1.86 GHz)

SPECfp_rate_base2006 = 72.9

CPU2006 license: 001176

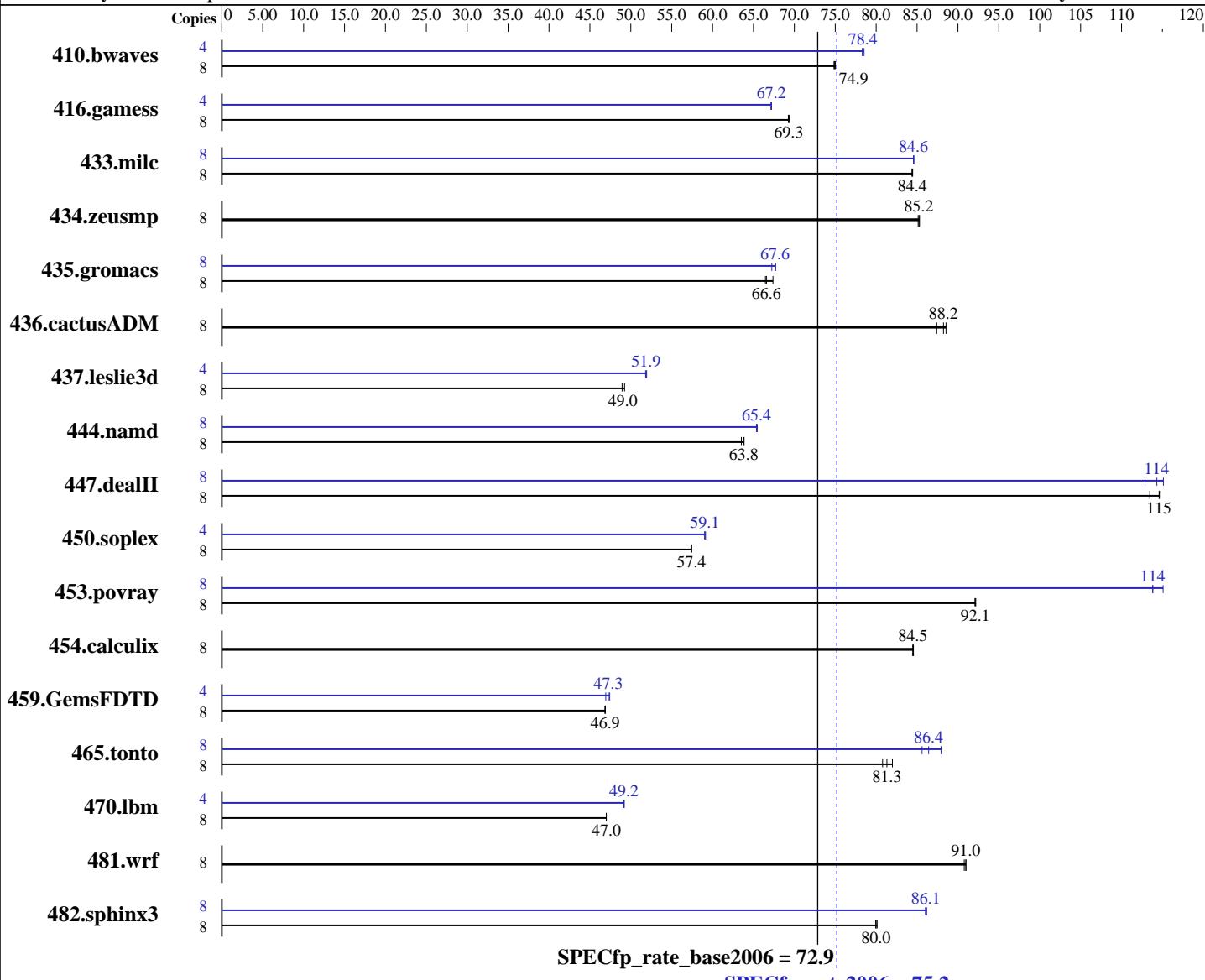
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L3426
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 1867
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Compiler: Kernel 2.6.27.19-5-default
Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064,
l_cprof_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Motherboard X8SIU-F (Intel Xeon L3426, 1.86 GHz)

SPECfp_rate2006 = 75.2

SPECfp_rate_base2006 = 72.9

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2010

L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 DDR3-1333 RDIMM, ECC, CL9)
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Hardware Availability: Apr-2010

Software Availability: Jan-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1453	74.8	1452	74.9	1449	75.0	4	694	78.3	694	78.4	692	78.5
416.gamess	8	2262	69.3	2257	69.4	2259	69.3	4	1167	67.1	1165	67.2	1165	67.2
433.milc	8	870	84.4	870	84.4	869	84.5	8	868	84.6	868	84.6	868	84.6
434.zeusmp	8	855	85.1	854	85.2	853	85.3	8	855	85.1	854	85.2	853	85.3
435.gromacs	8	848	67.4	859	66.5	858	66.6	8	844	67.7	845	67.6	849	67.2
436.cactusADM	8	1094	87.4	1080	88.5	1084	88.2	8	1094	87.4	1080	88.5	1084	88.2
437.leslie3d	8	1527	49.2	1537	48.9	1534	49.0	4	726	51.8	724	51.9	725	51.9
444.namd	8	1005	63.9	1006	63.8	1010	63.5	8	981	65.4	982	65.4	980	65.5
447.dealII	8	807	113	799	115	798	115	8	811	113	795	115	801	114
450.soplex	8	1162	57.4	1162	57.4	1163	57.4	4	565	59.1	564	59.1	565	59.0
453.povray	8	462	92.2	462	92.1	462	92.1	8	374	114	370	115	374	114
454.calculix	8	781	84.5	781	84.5	781	84.6	8	781	84.5	781	84.5	781	84.6
459.GemsFDTD	8	1810	46.9	1812	46.9	1811	46.9	4	898	47.3	904	46.9	895	47.4
465.tonto	8	974	80.8	968	81.3	960	82.0	8	920	85.6	895	87.9	911	86.4
470.lbm	8	2339	47.0	2338	47.0	2339	47.0	4	1119	49.1	1117	49.2	1117	49.2
481.wrf	8	982	91.0	982	91.0	984	90.8	8	982	91.0	982	91.0	984	90.8
482.sphinx3	8	1950	79.9	1949	80.0	1946	80.1	8	1811	86.1	1809	86.2	1813	86.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
 As tested, the system used a Supermicro CSE-815TQ-330UB chassis.
 The chassis is bundled with a PWS-333-1H20 power supply, a SNK-P0046P heatsink,
 and 4 FAN-0086L4 cooling fans.



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Motherboard X8SIU-F (Intel Xeon L3426, 1.86 GHz)

SPECfp_rate2006 = 75.2

SPECfp_rate_base2006 = 72.9

CPU2006 license: 001176

Test date: May-2010

Test sponsor: Supermicro

Hardware Availability: Apr-2010

Tested by: Supermicro

Software Availability: Jan-2010

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Motherboard X8SIU-F (Intel Xeon L3426, 1.86 GHz)

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

SPECfp_rate2006 = 75.2

SPECfp_rate_base2006 = 72.9

Test date: May-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Motherboard X8SIU-F (Intel Xeon L3426, 1.86 GHz)

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

SPECfp_rate2006 = 75.2

SPECfp_rate_base2006 = 72.9

Test date: May-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

433.milc: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xsse4 .2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4 .2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xsse4 .2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xsse4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Motherboard X8SIU-F (Intel Xeon L3426, 1.86 GHz)

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

SPECfp_rate2006 = 75.2

SPECfp_rate_base2006 = 72.9

Test date: May-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

435.gromacs: -xSSE4 .2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100915.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 10:21:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 14 September 2010.