SPEC® CINT2006 Result

Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5507, 2.27 GHz

SPECint®_rate2006 =   152

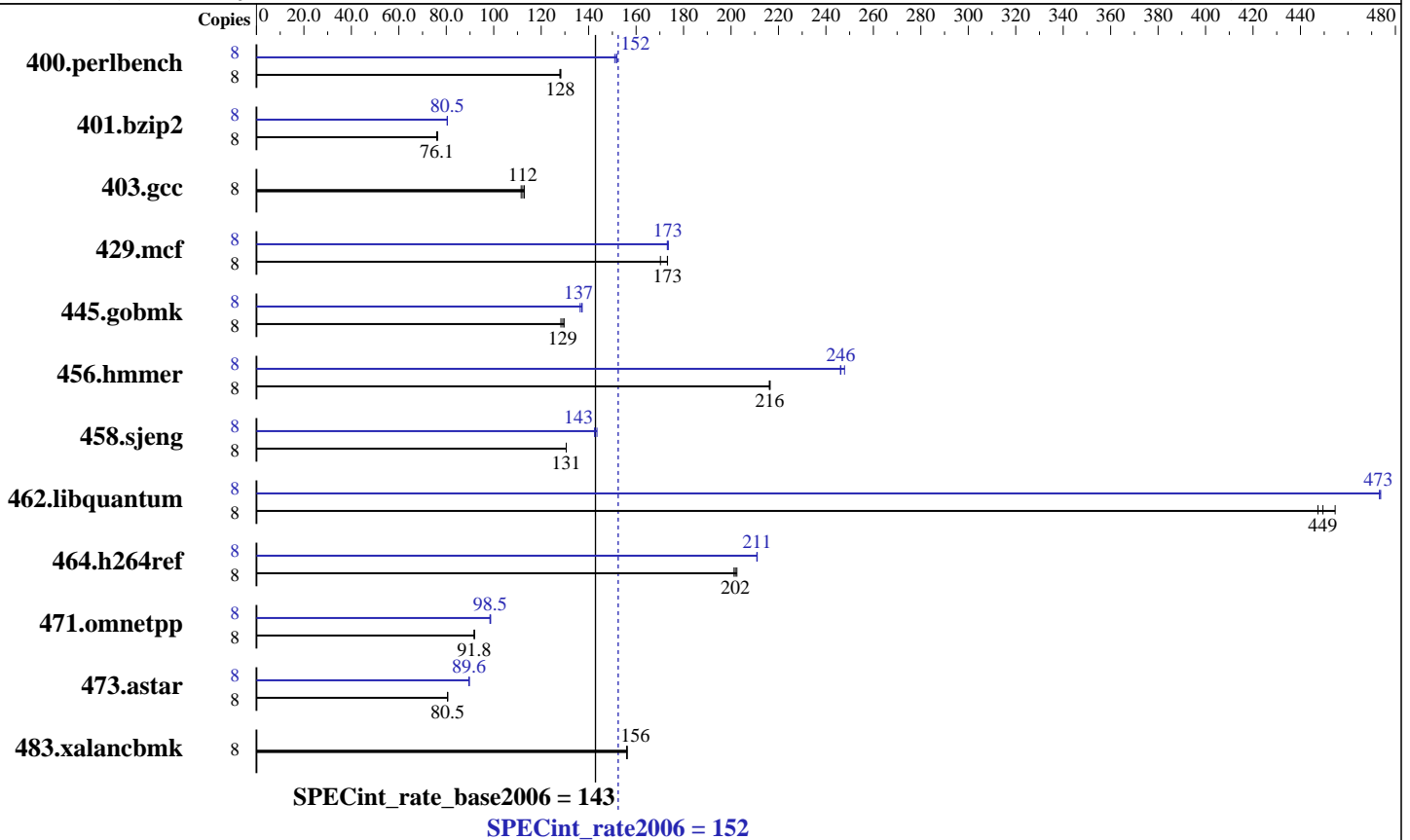SPECint_rate_base2006 = 143

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

SPECint_rate_base2006 = 143
SPECint_rate2006 = 152

Hardware

CPU Name:	Intel Xeon E5507
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SSD SATA, 64 GB
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5507, 2.27 GHz

SPECint_rate2006 =   152

SPECint_rate_base2006 =   143

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	611	128	**610**	**128**	609	128	8	515	152	518	151	**515**	**152**
401.bzip2	8	**1015**	**76.1**	1016	76.0	1011	76.4	8	959	80.5	**959**	**80.5**	960	80.4
403.gcc	8	570	113	577	112	**574**	**112**	8	570	113	577	112	**574**	**112**
429.mcf	8	429	170	**421**	**173**	421	173	8	420	174	421	173	**421**	**173**
445.gobmk	8	654	128	**650**	**129**	647	130	8	611	137	**612**	**137**	615	136
456.hmmer	8	345	216	345	216	**345**	**216**	8	301	248	303	246	**303**	**246**
458.sjeng	8	741	131	**741**	**131**	741	131	8	**679**	**143**	674	144	679	143
462.libquantum	8	365	455	371	447	**369**	**449**	8	350	473	**350**	**473**	350	474
464.h264ref	8	**877**	**202**	880	201	874	202	8	**839**	**211**	839	211	840	211
471.omnetpp	8	544	91.8	545	91.8	**545**	**91.8**	8	508	98.4	**507**	**98.5**	507	98.7
473.astar	8	697	80.6	**697**	**80.5**	698	80.5	8	626	89.7	627	89.6	**627**	**89.6**
483.xalancbmk	8	354	156	353	156	**354**	**156**	8	354	156	353	156	**354**	**156**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

      The config file option 'submit' was used.
       numactl was used to bind copies to the cores

Operating System Notes

      'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

      The system automatically configures the memory to run at 800 MHz.

General Notes

      For information about Fujitsu please visit: http://www.fujitsu.com
      Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
      icc -m32

C++ benchmarks:
      icpc -m32

Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5507, 2.27 GHz

SPECint_rate2006 =   152

SPECint_rate_base2006 = 143

CPU2006 license: 19		**Test date:**	May-2010
Test sponsor: Fujitsu		**Hardware Availability:**	Aug-2010
Tested by: Fujitsu		**Software Availability:**	Jan-2010

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

```
462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
```

Continued on next page

Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5507, 2.27 GHz

SPECint_rate2006 =   152

SPECint_rate_base2006 =   143

CPU2006 license: 19		**Test date:**	May-2010
Test sponsor: Fujitsu		**Hardware Availability:**	Aug-2010
Tested by: Fujitsu		**Software Availability:**	Jan-2010

Peak Portability Flags (Continued)

```
  456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
   473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

     403.gcc: basepeak = yes

     429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
               -opt-prefetch

  464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
  471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

   473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY BX920 S2, Intel Xeon E5507, 2.27 GHz

SPECint_rate2006 =   152

SPECint_rate_base2006 = 143

CPU2006 license: 19		**Test date:**	May-2010
Test sponsor: Fujitsu		**Hardware Availability:**	Aug-2010
Tested by: Fujitsu		**Software Availability:**	Jan-2010

Peak Optimization Flags (Continued)

473.astar (continued):
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.02.xml

SPEC and SPECint are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 11:08:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 July 2010.