SPEC® CFP2006 Result

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Acer Incorporated

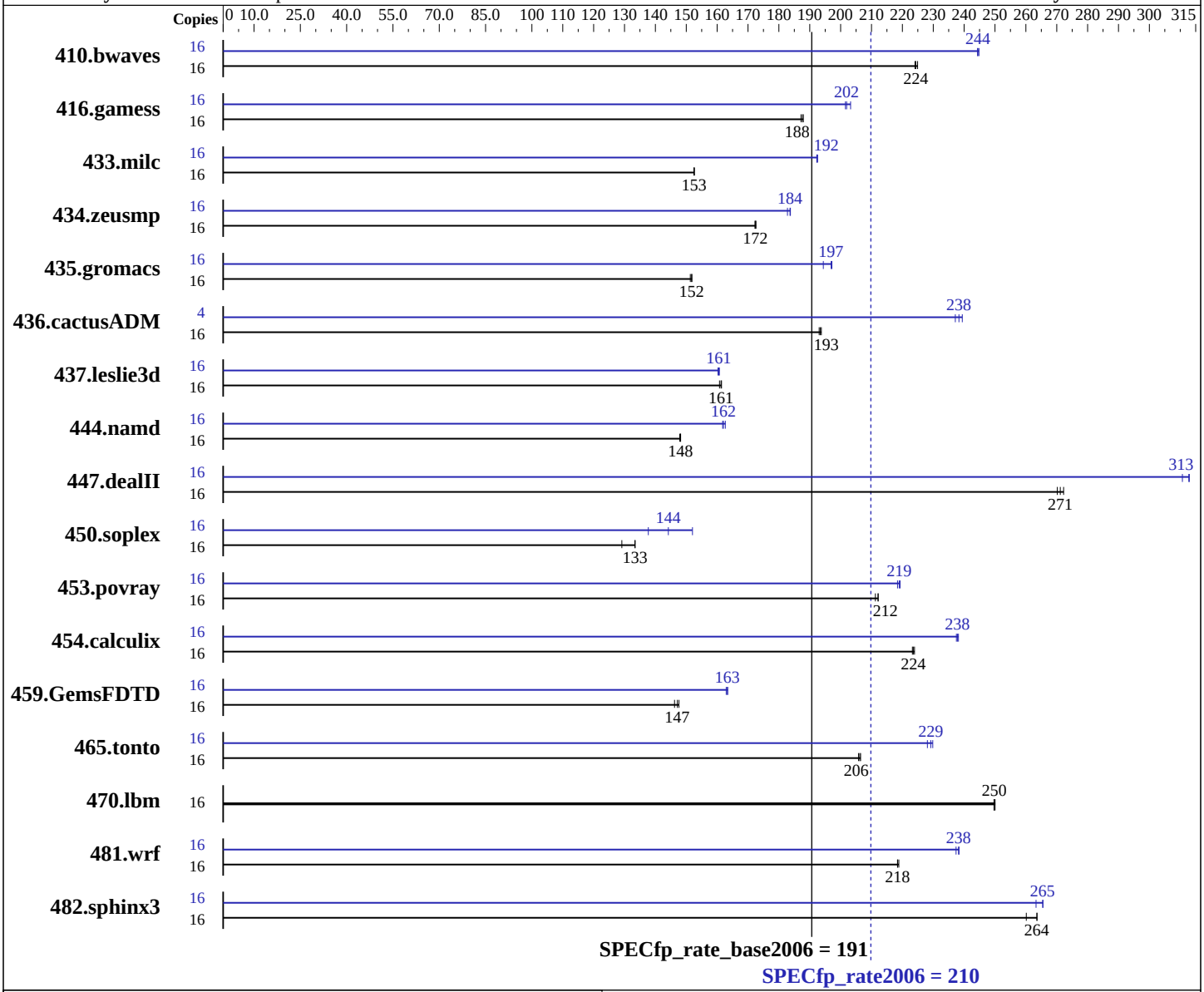
Gateway GW2000h-GW175hq F1 (AMD Opteron 6124 HE)

SPECfp®_rate2006 = 210

SPECfp_rate_base2006 = 191

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2010
Hardware Availability: Aug-2010
Software Availability: Feb-2010



Hardware

CPU Name:	AMD Opteron 6124 HE
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 5.5, Kernel 2.6.18-194.el5
Compiler:	x86 Open64 4.2.3 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	binutils 2.18

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores
Other Cache: None
Memory: 64 GB (16 x 4 GB DDR3-1333 RDIMM)
Disk Subsystem: 1 x 320 GB SATA, 7200 RPM
Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	**969**	**224**	967	225	970	224	16	890	244	888	245	**889**	**244**
416.gamess	16	**1670**	**188**	1667	188	1674	187	16	1554	202	1542	203	**1551**	**202**
433.milc	16	**963**	**153**	963	152	962	153	16	764	192	**764**	**192**	763	193
434.zeusmp	16	845	172	844	173	**844**	**172**	16	797	183	**793**	**184**	792	184
435.gromacs	16	752	152	755	151	**753**	**152**	16	579	197	**580**	**197**	588	194
436.cactusADM	16	987	194	990	193	**988**	**193**	4	**201**	**238**	202	237	200	239
437.leslie3d	16	936	161	932	161	**933**	**161**	16	**937**	**161**	936	161	938	160
444.namd	16	**866**	**148**	866	148	868	148	16	793	162	**792**	**162**	789	163
447.dealII	16	672	272	677	270	**675**	**271**	16	589	311	585	313	**585**	**313**
450.soplex	16	1033	129	**1000**	**133**	1000	133	16	969	138	**925**	**144**	878	152
453.povray	16	403	211	**401**	**212**	401	212	16	**389**	**219**	388	219	390	218
454.calculix	16	589	224	**591**	**224**	591	223	16	**555**	**238**	556	237	554	238
459.GemsFDTD	16	1150	148	**1154**	**147**	1161	146	16	1039	163	**1039**	**163**	1042	163
465.tonto	16	765	206	763	206	**764**	**206**	16	685	230	690	228	**687**	**229**
470.lbm	16	880	250	879	250	**880**	**250**	16	880	250	879	250	**880**	**250**
481.wrf	16	818	218	**818**	**218**	817	219	16	753	237	**750**	**238**	750	238
482.sphinx3	16	1199	260	**1183**	**264**	1183	264	16	1184	263	1174	266	**1175**	**265**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/usr/cpu2006/amd1002mc-rate-libs-revA/64:/usr/cpu2006/amd1002mc-rate-libs-revA/32"
OMP_NUM_THREADS = "4"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

This result was measured on the Gateway GW2000h-GW175hq F1.
The Acer AW2000h-AW175hq F1 and Gateway GW2000h-GW175hq F1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
`opencc`

C++ benchmarks:
`openCC`

Fortran benchmarks:
`openf95`

Benchmarks using both Fortran and C:
`opencc openf95`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -fno-second-underscore`
482.sphinx3: `-DSPEC_CPU_LP64`

Base Optimization Flags

C benchmarks:
```
-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m
```

C++ benchmarks:
```
-march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-OPT:malloc_alg=1 -HP:bdt=2m
```

Fortran benchmarks:
```
-march=barcelona -mso -Ofast -HP
```

Benchmarks using both Fortran and C:
```
-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP
```

Peak Compiler Invocation

C benchmarks:
```
opencc
```

C++ benchmarks:
```
openCC
```

Fortran benchmarks:
```
openf95
```

Benchmarks using both Fortran and C:
```
opencc openf95
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

Continued on next page

Peak Optimization Flags (Continued)

437.leslie3d: `-march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m`

459.GemsFDTD: `-march=barcelona -mso -Ofast -LNO:fission=2 -LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1 -HP`

465.tonto: `-march=barcelona -mso -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP`

Benchmarks using both Fortran and C:

435.gromacs: `-march=barcelona -mso -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m`

436.cactusADM: `-march=barcelona -mso -fb_create fbdata`(pass 1) `-fb_opt fbdata`(pass 2) `-Ofast -apo -LNO:prefetch_ahead=1 -HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100`

454.calculix: `-march=barcelona -mso -Ofast -CG:load_exe=0 -CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on -LNO:prefetch_ahead=30 -WOPT:unroll=2 -GRA:optimize_boundary=on -HP:bdt=2m:heap=2m`

481.wrf: `-march=barcelona -mso -Ofast -LNO:blocking=off -LNO:prefetch_ahead=10 -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow -HP`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/x86-open64-4.2.3-flags-revA.20100721.html`
`http://www.spec.org/cpu2006/flags/amd-platform-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/x86-open64-4.2.3-flags-revA.20100721.xml`
`http://www.spec.org/cpu2006/flags/amd-platform-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 11:57:53 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 August 2010.