



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®2006 = 22.6

Acer AB2x280 F1 (Intel Xeon L5609, 1.86 GHz)

SPECint_base2006 = 21.3

CPU2006 license: 97

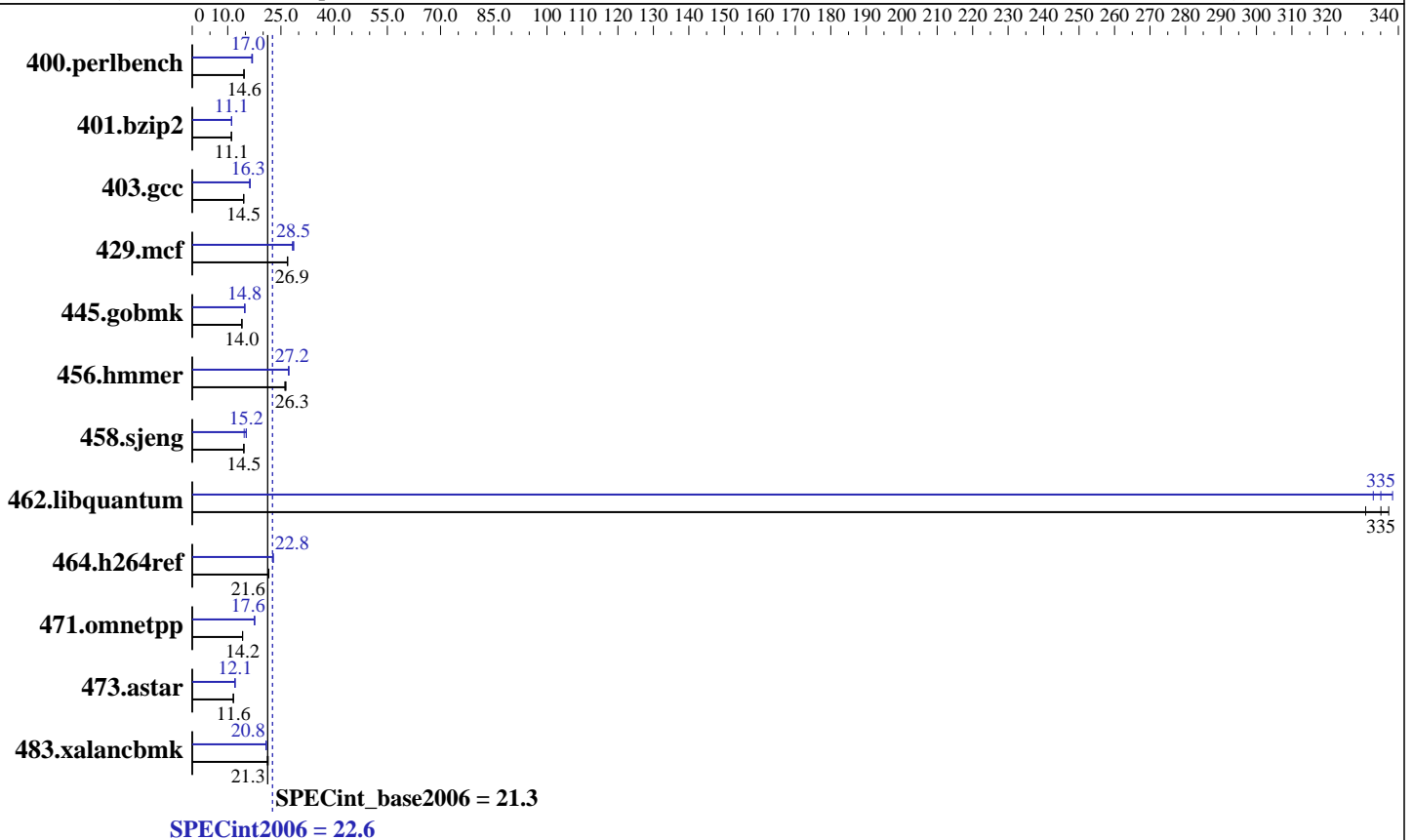
Test date: May-2010

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L5609
 CPU Characteristics: 1866
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB DDR3-1333 RDIMM, ECC, CL9, memory runs at 1066 MHz)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|--------------------|--------------------|-------------------|--------------------|---------|-------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 667 | 14.6 | <u>668</u> | <u>14.6</u> | 673 | 14.5 | <u>576</u> | <u>17.0</u> | 582 | 16.8 | 576 | 17.0 |
| 401.bzip2 | 870 | 11.1 | <u>872</u> | <u>11.1</u> | 876 | 11.0 | <u>869</u> | <u>11.1</u> | 869 | 11.1 | 869 | 11.1 |
| 403.gcc | <u>555</u> | <u>14.5</u> | 554 | 14.5 | 556 | 14.5 | <u>494</u> | <u>16.3</u> | 498 | 16.2 | 493 | 16.3 |
| 429.mcf | <u>339</u> | <u>26.9</u> | 339 | 26.9 | 340 | 26.8 | 323 | 28.2 | 319 | 28.6 | <u>320</u> | <u>28.5</u> |
| 445.gobmk | <u>750</u> | <u>14.0</u> | 749 | 14.0 | 750 | 14.0 | 707 | 14.8 | 710 | 14.8 | <u>709</u> | <u>14.8</u> |
| 456.hmmer | <u>355</u> | <u>26.3</u> | 357 | 26.1 | 353 | 26.4 | 343 | 27.2 | 343 | 27.2 | <u>343</u> | <u>27.2</u> |
| 458.sjeng | <u>832</u> | <u>14.5</u> | 831 | 14.6 | 833 | 14.5 | 794 | 15.2 | <u>795</u> | <u>15.2</u> | 826 | 14.6 |
| 462.libquantum | <u>61.8</u> | <u>335</u> | 61.4 | 337 | 62.6 | 331 | <u>61.8</u> | <u>335</u> | 62.2 | 333 | 61.2 | 338 |
| 464.h264ref | <u>1025</u> | <u>21.6</u> | 1029 | 21.5 | 1024 | 21.6 | 970 | 22.8 | <u>970</u> | <u>22.8</u> | 972 | 22.8 |
| 471.omnetpp | <u>440</u> | <u>14.2</u> | 440 | 14.2 | 441 | 14.2 | 355 | 17.6 | 356 | 17.6 | <u>356</u> | <u>17.6</u> |
| 473.astar | 605 | 11.6 | <u>605</u> | <u>11.6</u> | 606 | 11.6 | 582 | 12.1 | <u>582</u> | <u>12.1</u> | 582 | 12.1 |
| 483.xalancbmk | <u>325</u> | <u>21.3</u> | 322 | 21.4 | 325 | 21.2 | 331 | 20.8 | 331 | 20.9 | <u>331</u> | <u>20.8</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Platform Notes

Fan speed set to full Speed (ie. Enterprise Blade mode) with Smart Blade Console through CMM (Chassis Management Module)

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AB2x280 F1, and Gateway GB2x280 F1 are electronically equivalent.
This result was measured on Gateway GB2x280 F1.

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

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Peak Compiler Invocation (Continued)

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

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Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact webmaster@spec.org.

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