SPEC® CFP2006 Result

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Fujitsu

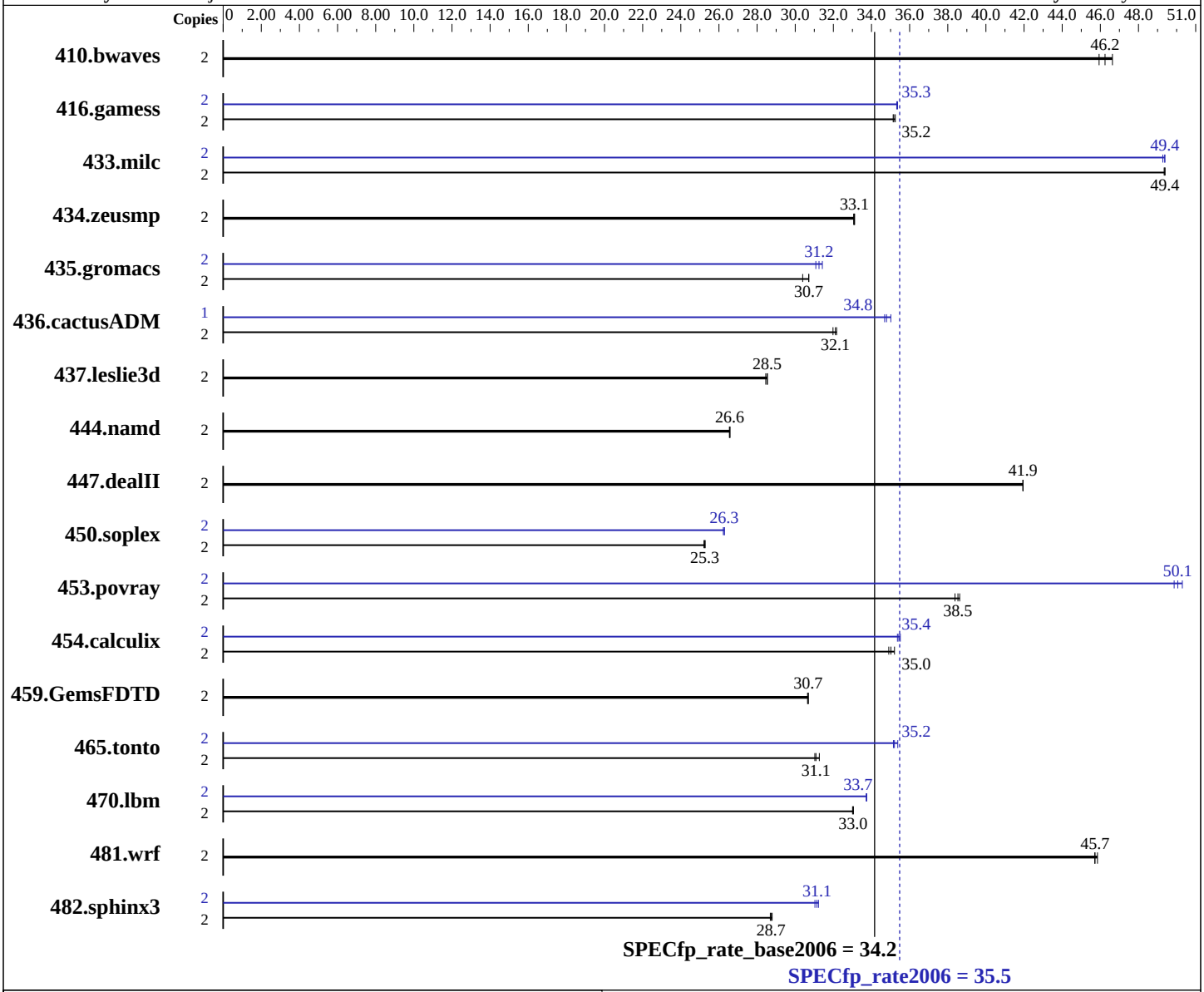
PRIMERGY TX100 S2, Intel Celeron G1101, 2.27 GHz

SPECfp®_rate2006 = 35.5

SPECfp_rate_base2006 = 34.2

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jul-2010
Hardware Availability: Aug-2010
Software Availability: May-2010



Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-User Run Level 3

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	583	46.6	**588**	**46.2**	592	45.9	2	583	46.6	**588**	**46.2**	592	45.9
416.gamess	2	**1113**	**35.2**	1111	35.2	1115	35.1	2	1107	35.4	1109	35.3	**1108**	**35.3**
433.milc	2	372	49.3	**372**	**49.4**	372	49.4	2	372	49.4	373	49.3	**372**	**49.4**
434.zeusmp	2	**550**	**33.1**	550	33.1	550	33.1	2	**550**	**33.1**	550	33.1	550	33.1
435.gromacs	2	**465**	**30.7**	470	30.4	465	30.7	2	454	31.4	459	31.1	**457**	**31.2**
436.cactusADM	2	743	32.2	747	32.0	**744**	**32.1**	1	**343**	**34.8**	344	34.7	341	35.0
437.leslie3d	2	661	28.5	**659**	**28.5**	659	28.5	2	661	28.5	**659**	**28.5**	659	28.5
444.namd	2	604	26.6	604	26.5	**604**	**26.6**	2	604	26.6	604	26.5	**604**	**26.6**
447.dealII	2	546	41.9	545	42.0	**546**	**41.9**	2	546	41.9	545	42.0	**546**	**41.9**
450.soplex	2	**660**	**25.3**	662	25.2	660	25.3	2	635	26.3	**635**	**26.3**	636	26.2
453.povray	2	**276**	**38.5**	277	38.4	275	38.6	2	213	49.9	212	50.3	**213**	**50.1**
454.calculix	2	469	35.2	**471**	**35.0**	473	34.9	2	465	35.5	**466**	**35.4**	467	35.4
459.GemsFDTD	2	**692**	**30.7**	692	30.7	692	30.6	2	**692**	**30.7**	692	30.7	692	30.6
465.tonto	2	629	31.3	**633**	**31.1**	634	31.0	2	556	35.4	**559**	**35.2**	560	35.1
470.lbm	2	831	33.1	**832**	**33.0**	833	33.0	2	814	33.7	**814**	**33.7**	815	33.7
481.wrf	2	487	45.9	**489**	**45.7**	489	45.7	2	487	45.9	**489**	**45.7**	489	45.7
482.sphinx3	2	1354	28.8	1359	28.7	**1357**	**28.7**	2	1256	31.0	1249	31.2	**1252**	**31.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind copies to the cores except
 for 436.cactusADM peak
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
The system automatically configures the memory to run at 1067 MHz.
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch`

Fortran benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -fno-alias`

470.lbm: `-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

482.sphinx3: `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `basepeak = yes`

450.soplex: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -opt-malloc-options=3`

453.povray: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32`

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Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml`

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