SPEC® CFP2006 Result

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Fujitsu

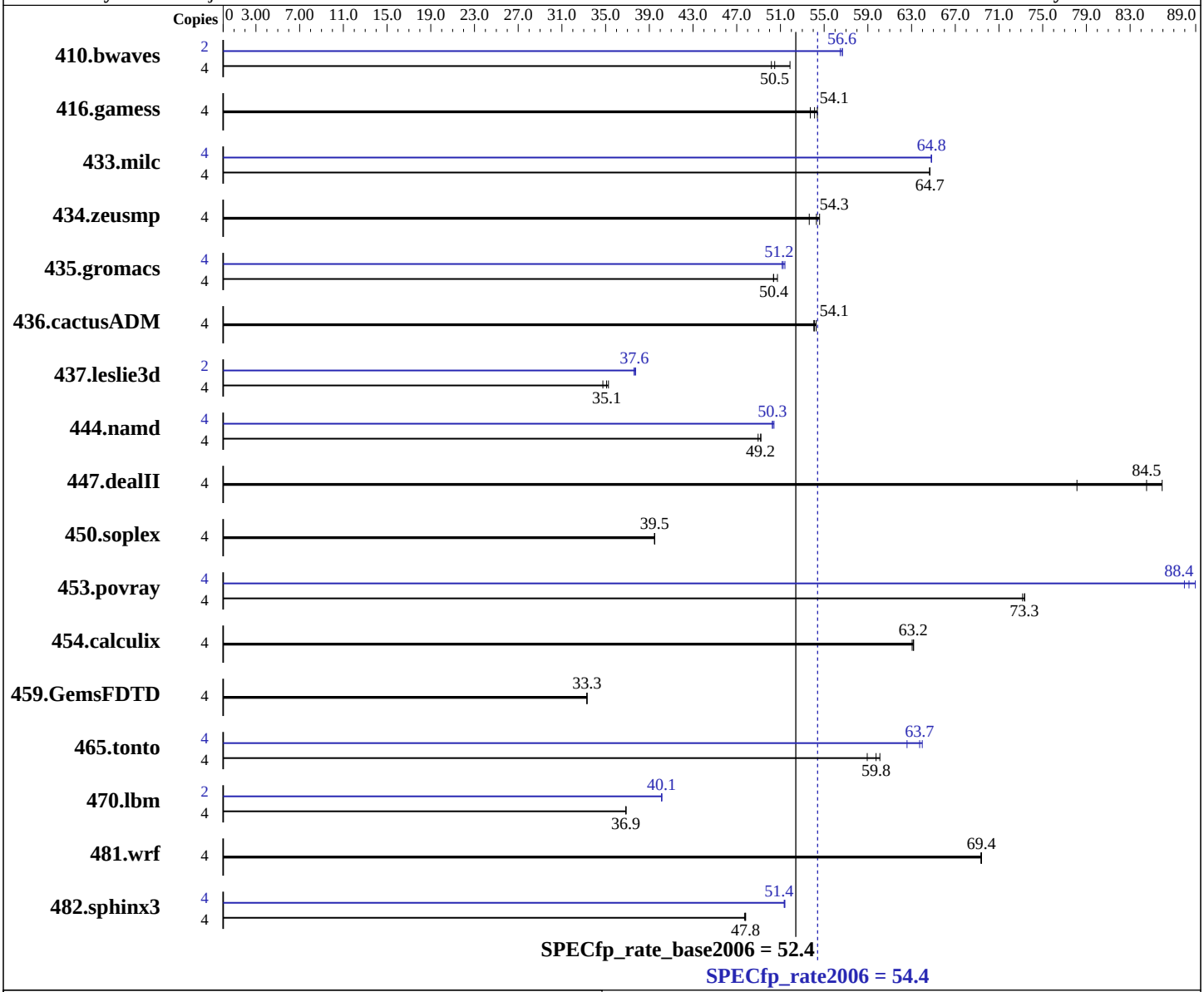
PRIMERGY RX100 S6, Intel Core i3-560, 3.33 GHz

SPECfp®_rate2006 = 54.4

SPECfp_rate_base2006 = 52.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2010
Hardware Availability: Aug-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Core i3-560
CPU Characteristics:	
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run Level 3 (multi user)

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (4x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SSD SATA, 64 GB
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1084	50.2	1048	51.9	**1077**	**50.5**	2	480	56.7	481	56.5	**480**	**56.6**
416.gamess	4	**1447**	**54.1**	1440	54.4	1458	53.7	4	**1447**	**54.1**	1440	54.4	1458	53.7
433.milc	4	568	64.7	**568**	**64.7**	568	64.7	4	**566**	**64.8**	567	64.8	566	64.8
434.zeusmp	4	**670**	**54.3**	679	53.6	667	54.6	4	**670**	**54.3**	679	53.6	667	54.6
435.gromacs	4	567	50.4	**567**	**50.4**	563	50.7	4	556	51.4	**557**	**51.2**	558	51.2
436.cactusADM	4	**883**	**54.1**	881	54.3	884	54.1	4	**883**	**54.1**	881	54.3	884	54.1
437.leslie3d	4	**1072**	**35.1**	1066	35.3	1082	34.8	2	498	37.8	500	37.6	**499**	**37.6**
444.namd	4	655	49.0	652	49.2	**652**	**49.2**	4	636	50.4	638	50.3	**638**	**50.3**
447.dealII	4	586	78.1	**541**	**84.5**	533	85.9	4	586	78.1	**541**	**84.5**	533	85.9
450.soplex	4	845	39.5	**845**	**39.5**	845	39.5	4	845	39.5	**845**	**39.5**	845	39.5
453.povray	4	290	73.4	**290**	**73.3**	291	73.2	4	239	89.0	242	88.0	**241**	**88.4**
454.calculix	4	**522**	**63.2**	522	63.2	523	63.1	4	**522**	**63.2**	522	63.2	523	63.1
459.GemsFDTD	4	1275	33.3	1274	33.3	**1275**	**33.3**	4	1275	33.3	1274	33.3	**1275**	**33.3**
465.tonto	4	655	60.1	**659**	**59.8**	668	58.9	4	615	64.0	629	62.6	**618**	**63.7**
470.lbm	4	1490	36.9	**1491**	**36.9**	1492	36.8	2	684	40.2	**685**	**40.1**	685	40.1
481.wrf	4	644	69.4	**644**	**69.4**	644	69.3	4	644	69.4	**644**	**69.4**	644	69.3
482.sphinx3	4	**1631**	**47.8**	1634	47.7	1630	47.8	4	**1518**	**51.4**	1518	51.3	1516	51.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

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'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

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For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml

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For other inquiries, please contact webmaster@spec.org.

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