# SPEC® CFP2006 Result

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## Fujitsu

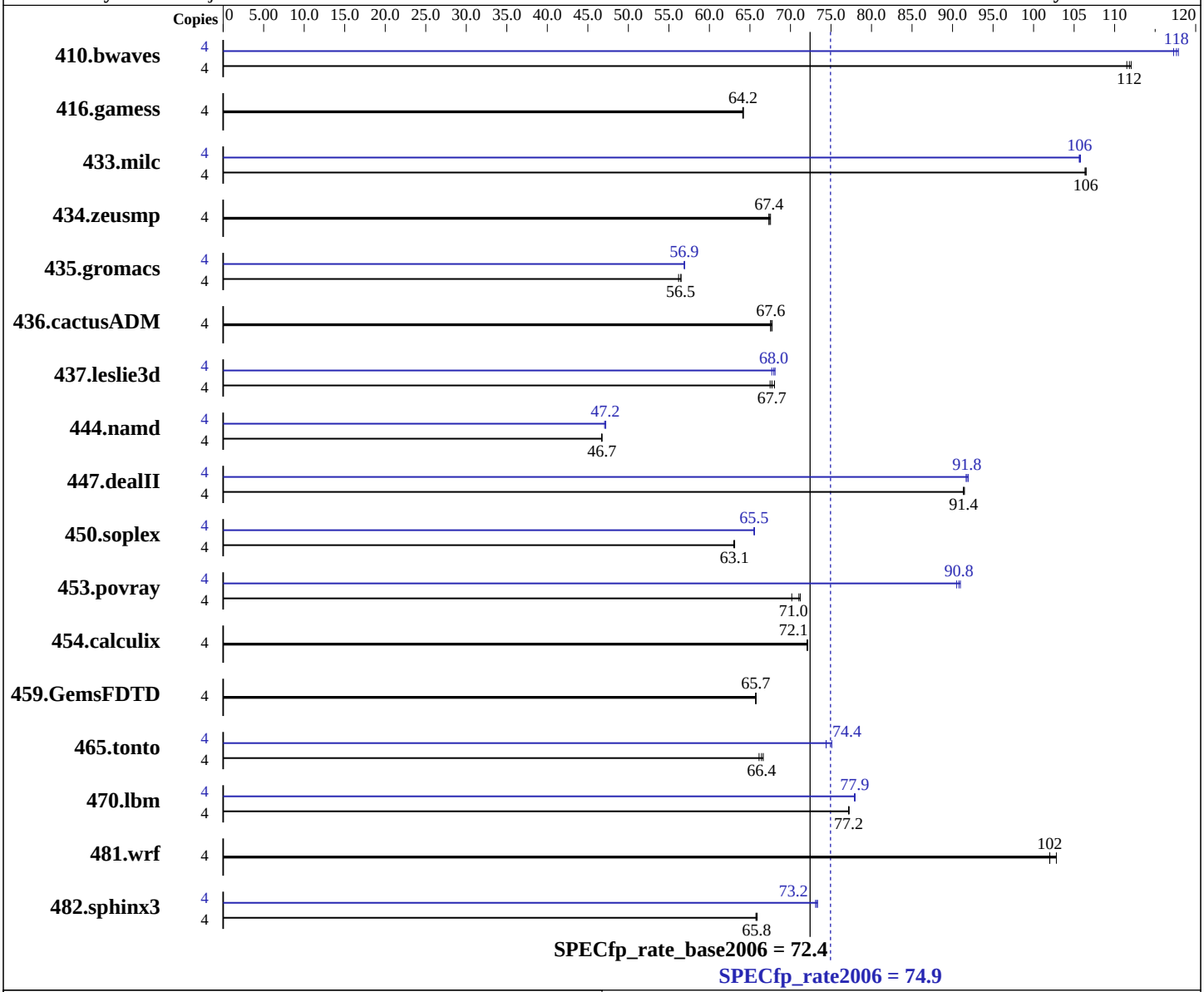
PRIMERGY TX200 S6, Intel Xeon E5503, 2.0 GHz

SPECfp®_rate2006 = 74.9

SPECfp_rate_base2006 = 72.4

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Aug-2010
**Hardware Availability:** Jul-2010
**Software Availability:** Jan-2010




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5503 |
| CPU Characteristics: | |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 2 chips, 2 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default |
| Compiler: | Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Multi-User Run Level 3 |

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 4 | 485 | 112 | **486** | **112** | 488 | 112 | 4 | 464 | 117 | **462** | **118** | 461 | 118 |
| 416.gamess | 4 | 1222 | 64.1 | **1221** | **64.2** | 1220 | 64.2 | 4 | 1222 | 64.1 | **1221** | **64.2** | 1220 | 64.2 |
| 433.milc | 4 | 345 | 106 | 345 | 106 | **345** | **106** | 4 | 348 | 106 | 347 | 106 | **347** | **106** |
| 434.zeusmp | 4 | 541 | 67.3 | **540** | **67.4** | 539 | 67.5 | 4 | 541 | 67.3 | **540** | **67.4** | 539 | 67.5 |
| 435.gromacs | 4 | **506** | **56.5** | 506 | 56.5 | 508 | 56.2 | 4 | **502** | **56.9** | 501 | 56.9 | 502 | 56.9 |
| 436.cactusADM | 4 | 708 | 67.6 | **707** | **67.6** | 706 | 67.7 | 4 | 708 | 67.6 | **707** | **67.6** | 706 | 67.7 |
| 437.leslie3d | 4 | 557 | 67.5 | **555** | **67.7** | 553 | 68.0 | 4 | 552 | 68.1 | 555 | 67.7 | **553** | **68.0** |
| 444.namd | 4 | **686** | **46.7** | 686 | 46.7 | 687 | 46.7 | 4 | **680** | **47.2** | 682 | 47.1 | 679 | 47.2 |
| 447.dealII | 4 | **501** | **91.4** | 501 | 91.3 | 500 | 91.5 | 4 | 498 | 92.0 | **499** | **91.8** | 499 | 91.7 |
| 450.soplex | 4 | **529** | **63.1** | 530 | 63.0 | 529 | 63.1 | 4 | **509** | **65.5** | 509 | 65.6 | 509 | 65.5 |
| 453.povray | 4 | 299 | 71.2 | 303 | 70.2 | **300** | **71.0** | 4 | 234 | 91.0 | **234** | **90.8** | 235 | 90.5 |
| 454.calculix | 4 | 458 | 72.1 | 458 | 72.1 | **458** | **72.1** | 4 | 458 | 72.1 | 458 | 72.1 | **458** | **72.1** |
| 459.GemsFDTD | 4 | 646 | 65.7 | **646** | **65.7** | 645 | 65.8 | 4 | 646 | 65.7 | **646** | **65.7** | 645 | 65.8 |
| 465.tonto | 4 | 591 | 66.6 | **592** | **66.4** | 595 | 66.1 | 4 | **529** | **74.4** | 524 | 75.1 | 529 | 74.4 |
| 470.lbm | 4 | 712 | 77.2 | 712 | 77.2 | **712** | **77.2** | 4 | 705 | 78.0 | **705** | **77.9** | 706 | 77.9 |
| 481.wrf | 4 | **438** | **102** | 438 | 102 | 435 | 103 | 4 | **438** | **102** | 438 | 102 | 435 | 103 |
| 482.sphinx3 | 4 | **1184** | **65.8** | 1186 | 65.7 | 1183 | 65.9 | 4 | **1065** | **73.2** | 1063 | 73.3 | 1066 | 73.1 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

## Platform Notes

```
The system automatically configures the memory to run at 800 MHz.
BIOS configuration:
Data Reuse Optimization = Disable
```

## General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

Continued on next page

## Peak Optimization Flags (Continued)

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml`


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For other inquiries, please contact webmaster@spec.org.

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