



SPEC® CFP2006 Result

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Oracle Corporation

Sun Fire X2270 M2 (Intel Xeon E5620 2.4 GHz)

SPECfp_rate2006 = 170

SPECfp_rate_base2006 = 164

CPU2006 license: 6

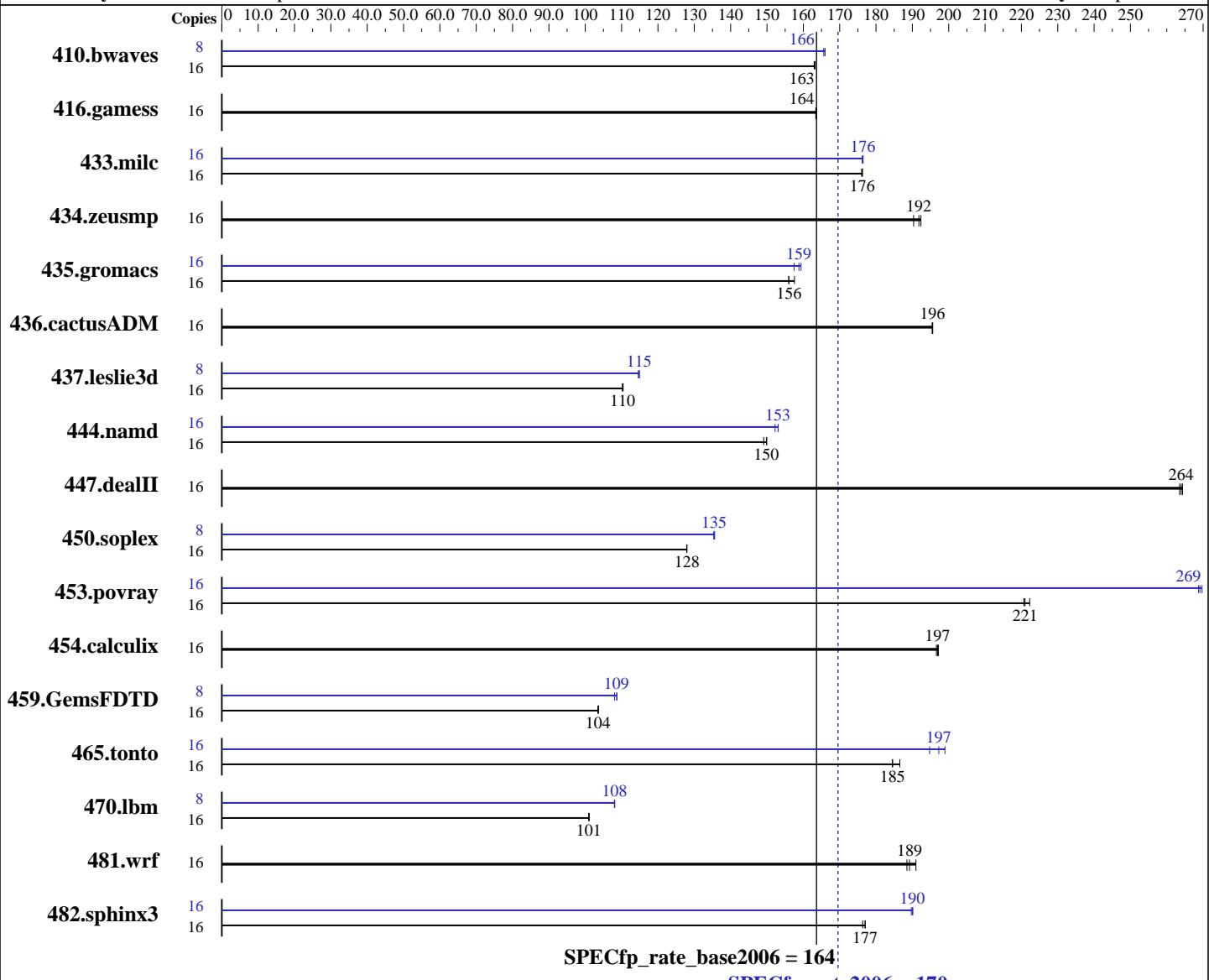
Test sponsor: Oracle Corporation

Tested by: Oracle Corporation

Test date: Nov-2010

Hardware Availability: Jun-2010

Software Availability: Apr-2010



Hardware

CPU Name: Intel Xeon E5620
CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1 or 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: Oracle Enterprise Linux Server release 5.5 kernel 2.6.18-194.el5
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 1 x 1 TB, SATA, 7200 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1334	163	1332	163	<u>1333</u>	<u>163</u>	8	<u>655</u>	<u>166</u>	655	166	656	166	656	166
416.gamess	16	1915	164	1917	163	<u>1915</u>	<u>164</u>	16	1915	164	1917	163	<u>1915</u>	<u>164</u>	1915	<u>164</u>
433.milc	16	833	176	834	176	<u>833</u>	<u>176</u>	16	832	176	834	176	<u>833</u>	<u>176</u>	833	<u>176</u>
434.zeusmp	16	765	190	<u>759</u>	<u>192</u>	757	192	16	765	190	<u>759</u>	<u>192</u>	757	192	757	192
435.gromacs	16	732	156	725	158	<u>732</u>	<u>156</u>	16	717	159	<u>719</u>	<u>159</u>	725	157	725	157
436.cactusADM	16	<u>978</u>	<u>196</u>	978	195	977	196	16	<u>978</u>	<u>196</u>	978	195	977	196	977	196
437.leslie3d	16	1364	110	<u>1364</u>	<u>110</u>	1362	110	8	655	115	<u>655</u>	<u>115</u>	656	115	656	115
444.namd	16	856	150	861	149	<u>856</u>	<u>150</u>	16	<u>838</u>	<u>153</u>	843	152	838	153	838	153
447.dealII	16	692	264	695	264	<u>693</u>	<u>264</u>	16	692	264	695	264	<u>693</u>	<u>264</u>	693	<u>264</u>
450.soplex	16	1043	128	1043	128	<u>1043</u>	<u>128</u>	8	493	135	<u>492</u>	<u>135</u>	492	136	492	136
453.povray	16	<u>385</u>	<u>221</u>	383	222	386	221	16	317	269	<u>316</u>	<u>269</u>	316	270	316	270
454.calculix	16	671	197	670	197	<u>670</u>	<u>197</u>	16	671	197	670	197	<u>670</u>	<u>197</u>	670	<u>197</u>
459.GemsFDTD	16	1641	103	1638	104	<u>1640</u>	<u>104</u>	8	781	109	786	108	<u>781</u>	<u>109</u>	781	<u>109</u>
465.tonto	16	844	187	853	184	<u>853</u>	<u>185</u>	16	<u>798</u>	<u>197</u>	808	195	791	199	791	199
470.lbm	16	2175	101	<u>2177</u>	<u>101</u>	2178	101	8	1017	108	<u>1017</u>	<u>108</u>	1018	108	1018	108
481.wrf	16	<u>945</u>	<u>189</u>	936	191	948	189	16	<u>945</u>	<u>189</u>	936	191	948	189	948	189
482.sphinx3	16	<u>1762</u>	<u>177</u>	1761	177	1768	176	16	<u>1641</u>	<u>190</u>	1640	190	1644	190	1644	190

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS Settings were used.



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xsSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Obo

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.html>
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE-pathfix-smartheap.20101027.xml>
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