SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

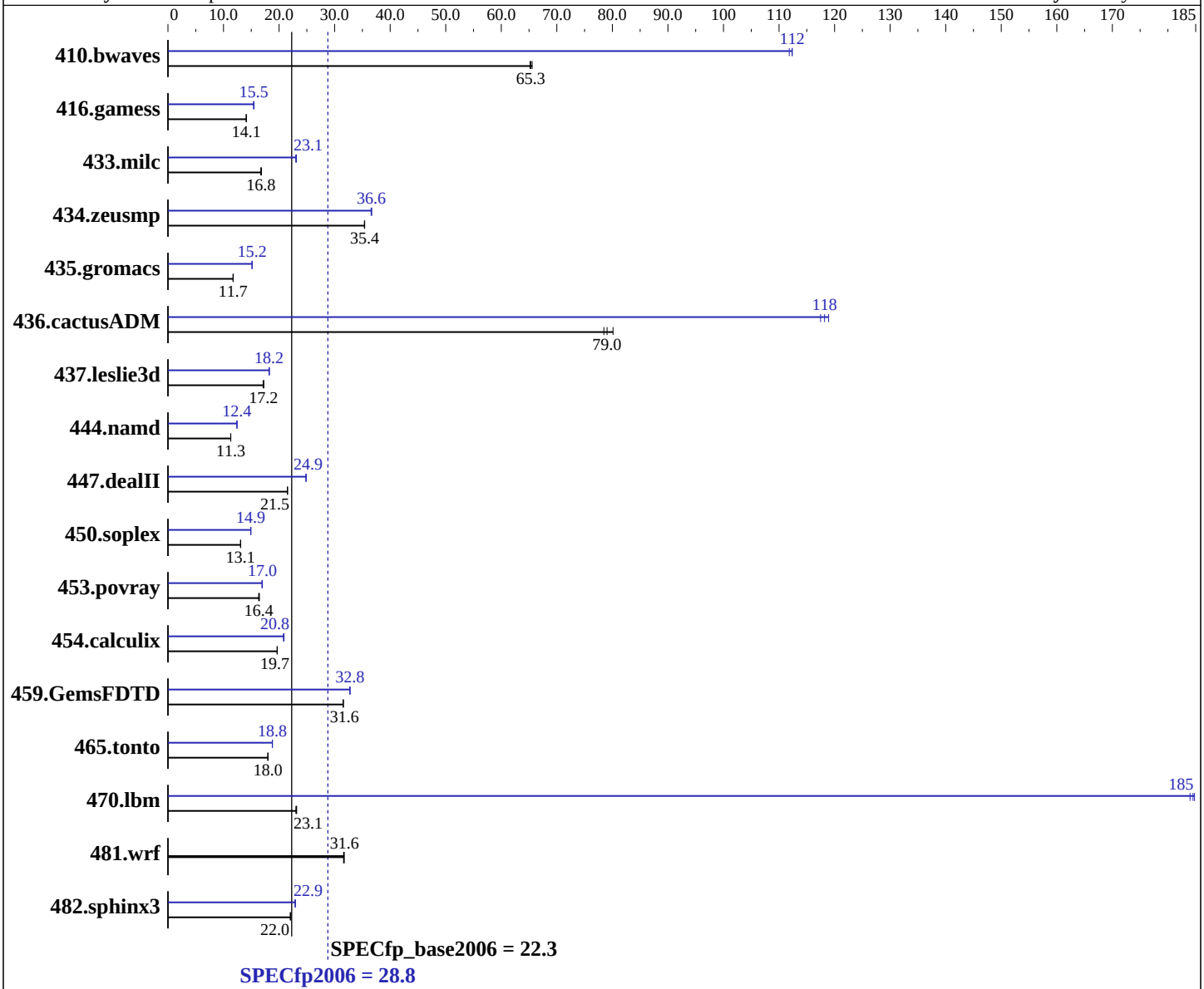
Supermicro A+ Server AS-1012G-MTF,
AMD Opteron 6174

SPECfp®2006 = 28.8

SPECfp_base2006 = 22.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Dec-2010
Hardware Availability: Mar-2010
Software Availability: May-2010



Hardware

CPU Name:	AMD Opteron 6174
CPU Characteristics:	
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	12 cores, 1 chip, 12 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 5.5, Kernel 2.6.18-194.el5
Compiler:	x86 Open64 4.2.3.2 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	binutils 2.18

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

Supermicro A+ Server AS-1012G-MTF,
AMD Opteron 6174

SPECfp2006 = 28.8

SPECfp_base2006 = 22.3

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Dec-2010
Hardware Availability: Mar-2010
Software Availability: May-2010

L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 6 cores
Other Cache: None
Memory: 32 GB (8 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	209	65.2	**208**	**65.3**	207	65.6	122	112	121	112	**121**	**112**
416.gamess	1390	14.1	**1388**	**14.1**	1387	14.1	1270	15.4	**1267**	**15.5**	1263	15.5
433.milc	**548**	**16.8**	546	16.8	549	16.7	399	23.0	**398**	**23.1**	397	23.1
434.zeusmp	257	35.4	**257**	**35.4**	257	35.4	248	36.7	**249**	**36.6**	249	36.6
435.gromacs	609	11.7	**609**	**11.7**	608	11.7	472	15.1	**471**	**15.2**	471	15.2
436.cactusADM	152	78.5	149	80.1	**151**	**79.0**	100	119	**101**	**118**	102	117
437.leslie3d	545	17.3	**546**	**17.2**	549	17.1	**517**	**18.2**	517	18.2	514	18.3
444.namd	709	11.3	710	11.3	**709**	**11.3**	646	12.4	645	12.4	**646**	**12.4**
447.dealII	**532**	**21.5**	532	21.5	532	21.5	**460**	**24.9**	460	24.9	461	24.8
450.soplex	637	13.1	639	13.1	**638**	**13.1**	**560**	**14.9**	558	14.9	560	14.9
453.povray	323	16.5	325	16.4	**325**	**16.4**	313	17.0	**313**	**17.0**	315	16.9
454.calculix	420	19.7	419	19.7	**420**	**19.7**	397	20.8	395	20.9	**396**	**20.8**
459.GemsFDTD	336	31.5	336	31.6	**336**	**31.6**	324	32.8	324	32.8	**324**	**32.8**
465.tonto	547	18.0	547	18.0	**547**	**18.0**	**524**	**18.8**	524	18.8	523	18.8
470.lbm	593	23.2	**594**	**23.1**	597	23.0	74.7	184	**74.5**	**185**	74.4	185
481.wrf	352	31.7	**353**	**31.6**	353	31.6	352	31.7	**353**	**31.6**	353	31.6
482.sphinx3	882	22.1	885	22.0	**884**	**22.0**	851	22.9	**850**	**22.9**	850	22.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr_hugepages=2000 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

cpuspeed stop was used to set the CPU frequency to its maximum.
```

Platform Notes

```
Fan speed set to Full Speed in BIOS Setup.
The system uses a Supermicro H8SGL-F motherboard.
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/amd1002-speed-libs-revA/64:/usr/cpu2006/amd1002-speed-libs-revA/32"
O64_OMP_AFFINITY_MAP = "0,1,2,3,4,5,6,7,8,9,10,11"
O64_OMP_SPIN_USER_LOCK = "true"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore

Continued on next page

Base Portability Flags (Continued)

482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-march=barcelona -Ofast -HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -Ofast -static -INLINE:aggressive=on
-HP:bdt=2m:heap=2m

Fortran benchmarks:
-march=barcelona -Ofast -apo -LNO:parallel_overhead=10000
-LNO:fusion_peeling_limit=0 -HP:bdt=2m:heap=2m

Benchmarks using both Fortran and C:
-march=barcelona -Ofast -HP:bdt=2m:heap=2m -apo
-LNO:parallel_overhead=10000 -LNO:fusion_peeling_limit=0

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -Ofast -apo -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-CG:compute_to=on -HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: -march=barcelona -Ofast -mso -apo -CG:sse_cse_regs=0
-LNO:prefetch_ahead=4 -CG:locs_shallow_depth=1
-CG:cmp_peep=on -CG:compute_to=on -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-OPT:alias=restricted -m3dnow -IPA:inline=off

482.sphinx3: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=barcelona -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -CG:load_exe=0 -fno-exceptions
-m32 -HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

453.povray: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on
-HP:bdt=2m:heap=2m

Fortran benchmarks:

410.bwaves: -march=barcelona -Ofast -apo -OPT:malloc_alg=2
-CG:use_prefetchnta=on -CG:cmp_peep=on -LNO:blocking=off
-LNO:prefetch=3 -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -LNO:apo_use_feedback=on
-WOPT:aggstr=0

416.gamess: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -Ofast -apo -LNO:blocking=off
-LNO:interchange=off -LNO:fusion_peeling_limit=0
-OPT:treeheight=on -OPT:unroll_size=256 -CG:cmp_peep=on
-CG:compute_to=on -GRA:prioritize_by_density=on
-HP:bdt=2m:heap=2m

437.leslie3d: -march=barcelona -Ofast -apo -OPT:unroll_size=256
-LNO:prefetch_ahead=4 -LNO:parallel_overhead=32768
-GRA:prioritize_by_density=on -m3dnow -HP:bdt=2m:heap=2m

459.GemsFDTD: -march=barcelona -Ofast -apo -LNO:fission=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1
-HP

465.tonto: -march=barcelona -Ofast -apo
-OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -Ofast -apo -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -apo
-LANG:heap_allocation_threshold=1000 -LNO:prefetch_ahead=1
-HP:bdt=2m:heap=2m

454.calculix: -march=barcelona -Ofast -LNO:prefetch_ahead=30
-CG:load_exe=0 -CG:ptr_load_use=0 -CG:local_sched_alg=2
-CG:compute_to=on -WOPT:unroll=2 -GRA:optimize_boundary=on
-HP:bdt=2m:heap=2m -apo

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/x86-open64-423-flags-speed-revA.20101207.html`
`http://www.spec.org/cpu2006/flags/amd-platform-speed-revA.20101207.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/x86-open64-423-flags-speed-revA.20101207.xml`
`http://www.spec.org/cpu2006/flags/amd-platform-speed-revA.20101207.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 16:57:06 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 18 January 2011.