SPEC® CFP2006 Result

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Fujitsu

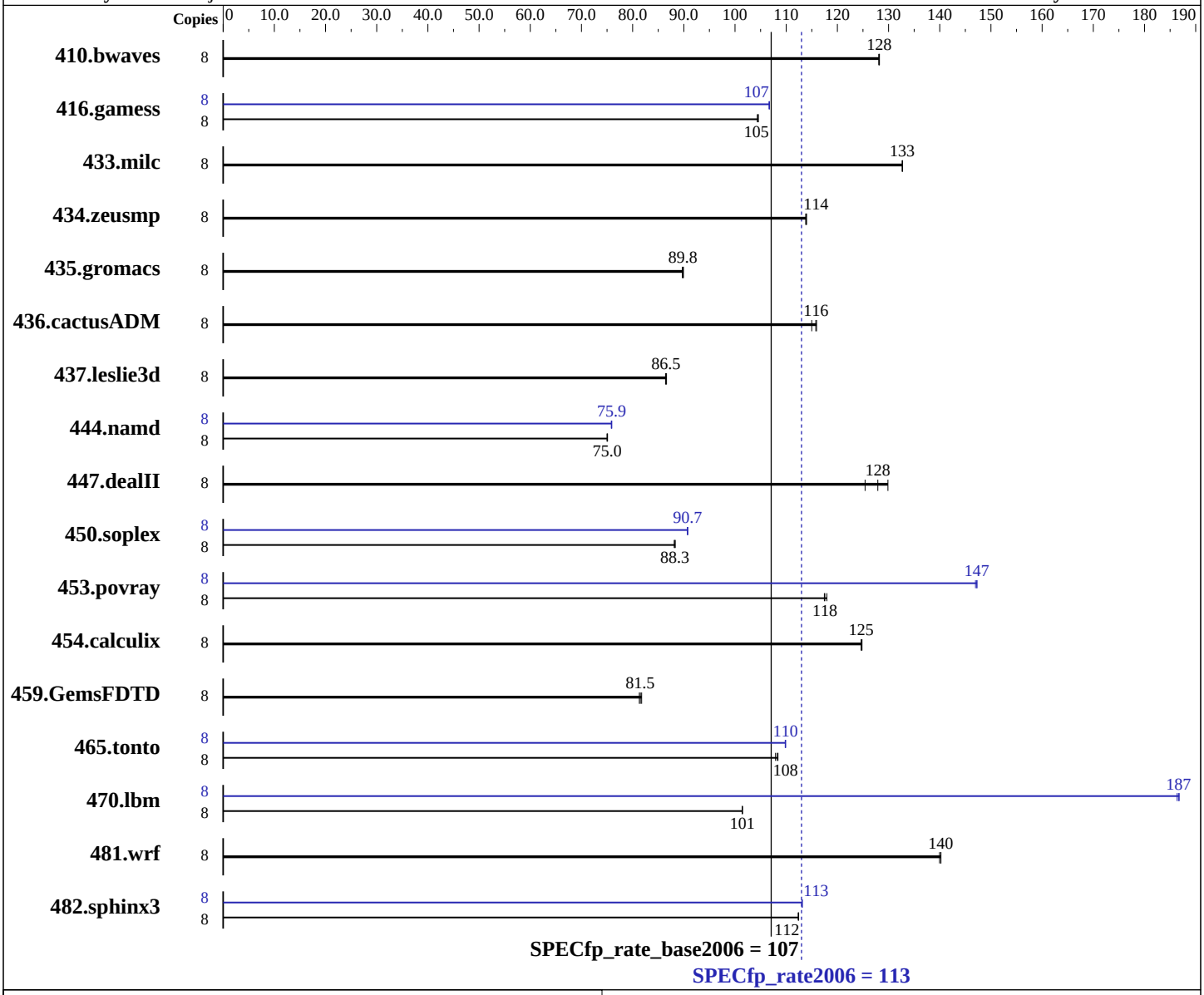
PRIMERGY TX200 S6, Intel Xeon E5603, 1.60 GHz

SPECfp®_rate2006 = 113

SPECfp_rate_base2006 = 107

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Jul-2010
Software Availability: Nov-2010



Hardware

CPU Name:	Intel Xeon E5603
CPU Characteristics:	
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.0.082 Build 20101006
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**848**	**128**	848	128	849	128	8	**848**	**128**	848	128	849	128
416.gamess	8	1501	104	**1499**	**105**	1498	105	8	1468	107	**1468**	**107**	1469	107
433.milc	8	554	133	553	133	**553**	**133**	8	554	133	553	133	**553**	**133**
434.zeusmp	8	639	114	**639**	**114**	640	114	8	639	114	**639**	**114**	640	114
435.gromacs	8	635	89.9	637	89.7	**636**	**89.8**	8	635	89.9	637	89.7	**636**	**89.8**
436.cactusADM	8	825	116	831	115	**826**	**116**	8	825	116	831	115	**826**	**116**
437.leslie3d	8	868	86.6	870	86.4	**869**	**86.5**	8	868	86.6	870	86.4	**869**	**86.5**
444.namd	8	854	75.1	**855**	**75.0**	855	75.0	8	846	75.8	845	75.9	**845**	**75.9**
447.dealII	8	730	125	**716**	**128**	705	130	8	730	125	**716**	**128**	705	130
450.soplex	8	755	88.3	**756**	**88.3**	757	88.1	8	736	90.7	735	90.8	**735**	**90.7**
453.povray	8	362	117	**362**	**118**	361	118	8	**289**	**147**	290	147	289	147
454.calculix	8	529	125	**529**	**125**	529	125	8	529	125	**529**	**125**	529	125
459.GemsFDTD	8	**1041**	**81.5**	1044	81.3	1038	81.8	8	**1041**	**81.5**	1044	81.3	1038	81.8
465.tonto	8	**727**	**108**	729	108	726	108	8	717	110	716	110	**717**	**110**
470.lbm	8	**1084**	**101**	1084	101	1083	101	8	588	187	**589**	**187**	590	186
481.wrf	8	637	140	639	140	**637**	**140**	8	637	140	639	140	**637**	**140**
482.sphinx3	8	**1387**	**112**	1387	112	1388	112	8	1379	113	**1379**	**113**	1378	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 17:01:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 February 2011.