SPEC® CFP2006 Result

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Fujitsu

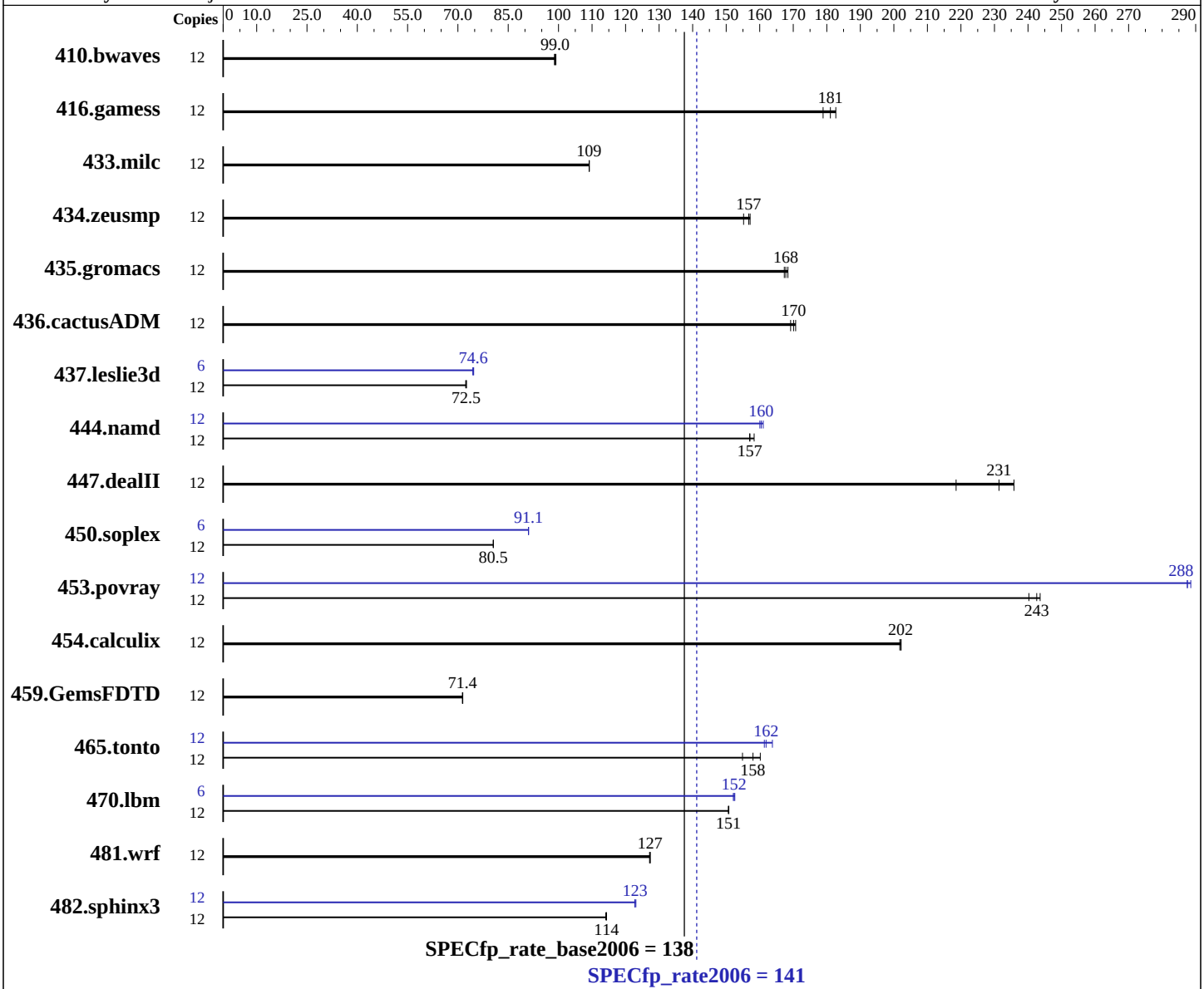
PRIMERGY TX300 S6, Intel Xeon X5690, 3.47 GHz

SPECfp®_rate2006 = 141

SPECfp_rate_base2006 = 138

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Dec-2010
Hardware Availability: Feb-2011
Software Availability: Nov-2010



Hardware

CPU Name:	Intel Xeon X5690
CPU Characteristics:	Intel Turbo Boost Technology up to 3.73 GHz
CPU MHz:	3467
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.0.082 Build 20101006
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	1643	99.2	**1648**	**99.0**	1651	98.8	12	1643	99.2	**1648**	**99.0**	1651	98.8
416.gamess	12	**1298**	**181**	1286	183	1313	179	12	**1298**	**181**	1286	183	1313	179
433.milc	12	**1009**	**109**	1009	109	1009	109	12	**1009**	**109**	1009	109	1009	109
434.zeusmp	12	695	157	**697**	**157**	704	155	12	695	157	**697**	**157**	704	155
435.gromacs	12	512	167	509	168	**511**	**168**	12	512	167	509	168	**511**	**168**
436.cactusADM	12	847	169	**843**	**170**	840	171	12	847	169	**843**	**170**	840	171
437.leslie3d	12	**1556**	**72.5**	1555	72.5	1560	72.3	6	758	74.4	755	74.7	**756**	**74.6**
444.namd	12	613	157	608	158	**613**	**157**	12	601	160	598	161	**600**	**160**
447.dealII	12	**593**	**231**	628	219	582	236	12	**593**	**231**	628	219	582	236
450.soplex	12	1242	80.6	1243	80.5	**1242**	**80.5**	6	550	91.0	**550**	**91.1**	549	91.1
453.povray	12	266	240	**263**	**243**	262	244	12	**222**	**288**	221	289	222	288
454.calculix	12	490	202	491	202	**490**	**202**	12	490	202	491	202	**490**	**202**
459.GemsFDTD	12	**1783**	**71.4**	1782	71.5	1784	71.4	12	**1783**	**71.4**	1782	71.5	1784	71.4
465.tonto	12	762	155	**747**	**158**	737	160	12	732	161	721	164	**729**	**162**
470.lbm	12	1093	151	1094	151	**1094**	**151**	6	540	153	**541**	**152**	542	152
481.wrf	12	**1053**	**127**	1053	127	1054	127	12	**1053**	**127**	1053	127	1054	127
482.sphinx3	12	**2046**	**114**	2050	114	2046	114	12	1900	123	**1904**	**123**	1905	123

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run
```

Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
```

General Notes

```
This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6
and the PRIMERGY TX300 S6 are electronically equivalent.

For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml`

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