SPEC® CINT2006 Result

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Dell Inc.

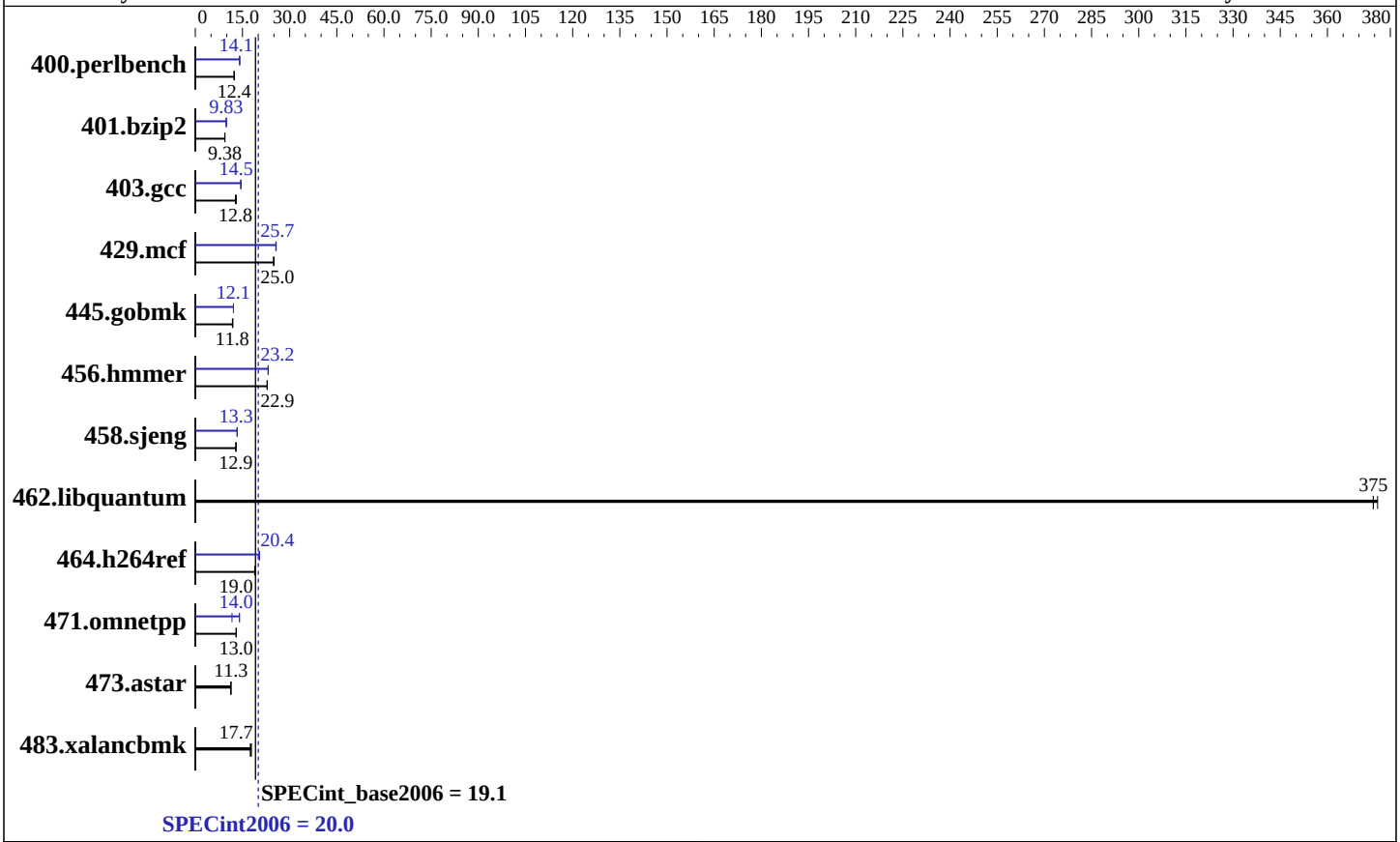
PowerEdge R610 (Intel Xeon E5603, 1.60 GHz)

SPECint®2006 = 20.0

SPECint_base2006 = 19.1

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Mar-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E5603
CPU Characteristics:	
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem:	1 x 146 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	793	12.3	788	12.4	**788**	**12.4**	692	14.1	694	14.1	**693**	**14.1**
401.bzip2	1023	9.44	1029	9.38	**1029**	**9.38**	976	9.89	982	9.82	**982**	**9.83**
403.gcc	**629**	**12.8**	618	13.0	630	12.8	555	14.5	556	14.5	**555**	**14.5**
429.mcf	366	24.9	365	25.0	**365**	**25.0**	356	25.6	**355**	**25.7**	355	25.7
445.gobmk	877	12.0	888	11.8	**887**	**11.8**	864	12.1	**865**	**12.1**	865	12.1
456.hmmer	**408**	**22.9**	408	22.8	408	22.9	403	23.1	**403**	**23.2**	402	23.2
458.sjeng	937	12.9	**936**	**12.9**	935	12.9	909	13.3	**909**	**13.3**	908	13.3
462.libquantum	55.1	376	**55.3**	**375**	55.3	375	55.1	376	**55.3**	**375**	55.3	375
464.h264ref	**1165**	**19.0**	1164	19.0	1172	18.9	**1087**	**20.4**	1087	20.4	1086	20.4
471.omnetpp	481	13.0	479	13.0	**480**	**13.0**	**445**	**14.0**	536	11.7	445	14.0
473.astar	621	11.3	620	11.3	**620**	**11.3**	621	11.3	620	11.3	**620**	**11.3**
483.xalancbmk	**389**	**17.7**	395	17.5	388	17.8	**389**	**17.7**	395	17.5	388	17.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)
```

General Notes

```
OMP_NUM_THREADS set to number of cores
The Dell PowerEdge R610 and
the Bull NovaScale R440 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R610 model.
Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

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Peak Compiler Invocation (Continued)

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-opt-prefetch -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div -prof-use`(pass 2) `-auto-ilp32`
`-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc`
`-opt-malloc-options=3 -auto-ilp32`
`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-auto-ilp32 -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

445.gobmk: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-prof-use`(pass 2)
`-auto-ilp32 -ansi-alias`
`-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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Peak Optimization Flags (Continued)

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml

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