



SPEC® CFP2006 Result

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Fujitsu

PRIMERGY BX924 S2, Intel Xeon X5675, 3.07 GHz

SPECfp®2006 = 61.1

SPECfp_base2006 = 57.6

CPU2006 license: 19

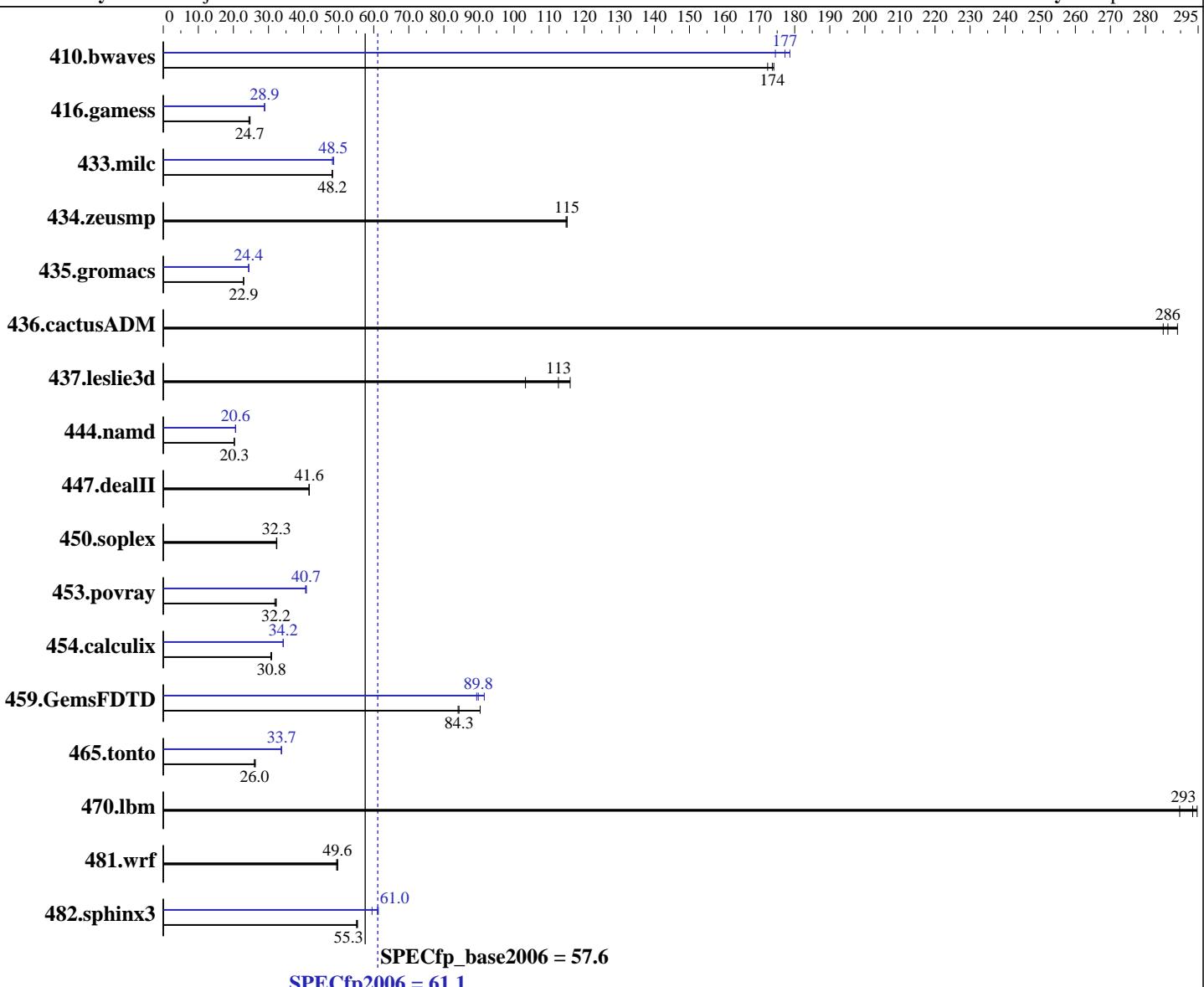
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2011

Hardware Availability: Feb-2011

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.47 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: --

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	78.3	174	78.9	172	78.1	174	77.9	174	76.7	177	76.1	179
416.gamess	802	24.4	794	24.7	791	24.7	678	28.9	677	28.9	679	28.8
433.milc	191	48.1	190	48.2	190	48.2	190	48.2	189	48.6	189	48.5
434.zeusmp	79.3	115	79.1	115	79.1	115	79.3	115	79.1	115	79.1	115
435.gromacs	312	22.9	312	22.9	312	22.9	293	24.3	293	24.4	293	24.4
436.cactusADM	41.9	285	41.3	289	41.7	286	41.9	285	41.3	289	41.7	286
437.leslie3d	91.0	103	81.0	116	83.4	113	91.0	103	81.0	116	83.4	113
444.namd	396	20.3	396	20.3	396	20.2	389	20.6	389	20.6	389	20.6
447.dealII	275	41.6	275	41.6	275	41.6	275	41.6	275	41.6	275	41.6
450.soplex	258	32.3	258	32.4	258	32.3	258	32.3	258	32.4	258	32.3
453.povray	165	32.2	165	32.2	167	31.8	131	40.7	130	40.8	131	40.6
454.calculix	267	30.9	269	30.7	268	30.8	241	34.2	241	34.2	241	34.2
459.GemsFDTD	117	90.3	126	84.3	126	84.0	118	89.8	116	91.5	119	89.3
465.tonto	378	26.0	375	26.3	379	26.0	293	33.6	292	33.7	292	33.7
470.lbm	46.8	293	46.6	295	47.4	290	46.8	293	46.6	295	47.4	290
481.wrf	226	49.4	225	49.6	225	49.8	226	49.4	225	49.6	225	49.8
482.sphinx3	352	55.4	354	55.1	353	55.3	328	59.5	318	61.3	319	61.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' was added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS configuration:
 Data Reuse Optimization = Disable
 Performance/Power Setting = Traditional
 Intel HT Technology = Disable



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General Notes

OMP_NUM_THREADS set to number of cores

For information about Fujitsu please visit: <http://www.fujitsu.com>

Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
 -ansi-alias

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

```
453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll14 -ansi-alias
            -B /usr/share/libhugetlbfsl -Wl,-melf_x86_64 -Wl,-hugetlbfsl-link=BDT
```

Fortran benchmarks:

```
410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
            -static
```

```
416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -scalar-rep -static
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

```
459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll12
            -inline-level=0 -opt-prefetch -parallel
            -B /usr/share/libhugetlbfsl -Wl,-melf_x86_64 -Wl,-hugetlbfsl-link=BDT
```

```
465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
            -opt-malloc-options=3 -auto -unroll14
            -B /usr/share/libhugetlbfsl -Wl,-melf_x86_64 -Wl,-hugetlbfsl-link=BDT
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
            -ansi-alias
```

436.cactusADM: basepeak = yes

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml>



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