# SPEC® CFP2006 Result

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## IBM Corporation

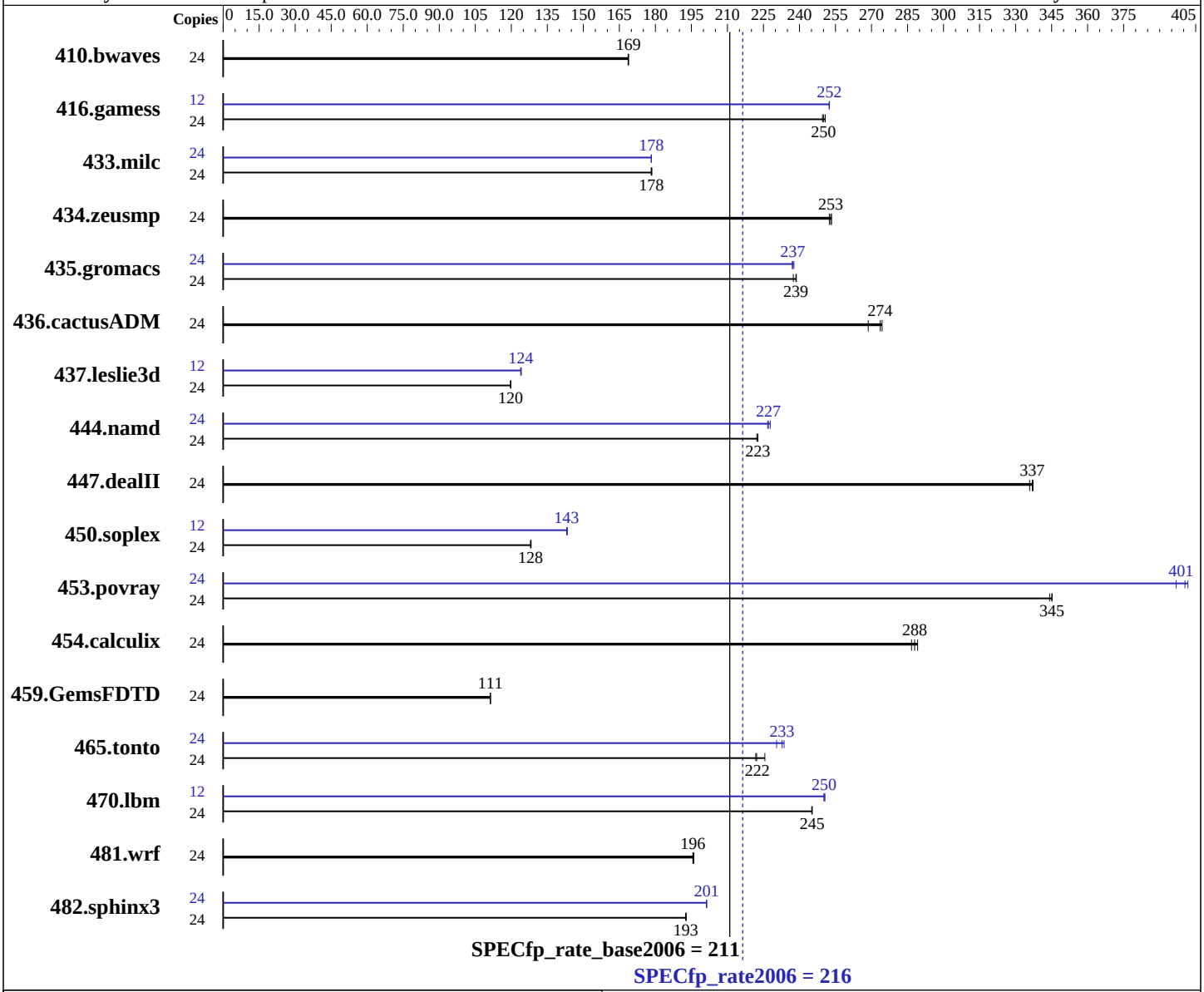
## IBM System x3620 M3 (Intel Xeon E5645)

SPECfp®_rate2006 = 216

SPECfp_rate_base2006 = 211

**CPU2006 license:** 11
**Test sponsor:** IBM Corporation
**Tested by:** IBM Corporation

**Test date:** Mar-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Jan-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5645 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2400 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 24 | **1933** | **169** | 1932 | 169 | 1934 | 169 | 24 | **1933** | **169** | 1932 | 169 | 1934 | 169 |
| 416.gamess | 24 | **1879** | **250** | 1874 | 251 | 1883 | 250 | 12 | 931 | 252 | 931 | 252 | **931** | **252** |
| 433.milc | 24 | 1236 | 178 | 1234 | 179 | **1235** | **178** | 24 | **1236** | **178** | 1235 | 178 | 1236 | 178 |
| 434.zeusmp | 24 | 862 | 253 | 865 | 252 | **863** | **253** | 24 | 862 | 253 | 865 | 252 | **863** | **253** |
| 435.gromacs | 24 | 718 | 239 | **718** | **239** | 722 | 237 | 24 | 721 | 238 | 723 | 237 | **722** | **237** |
| 436.cactusADM | 24 | **1048** | **274** | 1045 | 274 | 1068 | 269 | 24 | **1048** | **274** | 1045 | 274 | 1068 | 269 |
| 437.leslie3d | 24 | 1885 | 120 | **1884** | **120** | 1883 | 120 | 12 | 908 | 124 | **909** | **124** | 910 | 124 |
| 444.namd | 24 | 866 | 222 | **864** | **223** | 864 | 223 | 24 | 845 | 228 | 849 | 227 | **848** | **227** |
| 447.dealII | 24 | **815** | **337** | 818 | 336 | 814 | 337 | 24 | **815** | **337** | 818 | 336 | 814 | 337 |
| 450.soplex | 24 | **1563** | **128** | 1563 | 128 | 1561 | 128 | 12 | 700 | 143 | **699** | **143** | 698 | 143 |
| 453.povray | 24 | **370** | **345** | 371 | 344 | 370 | 345 | 24 | 318 | 402 | **319** | **401** | 322 | 397 |
| 454.calculix | 24 | **688** | **288** | 691 | 287 | 685 | 289 | 24 | **688** | **288** | 691 | 287 | 685 | 289 |
| 459.GemsFDTD | 24 | **2289** | **111** | 2289 | 111 | 2287 | 111 | 24 | **2289** | **111** | 2289 | 111 | 2287 | 111 |
| 465.tonto | 24 | 1065 | 222 | 1047 | 226 | **1063** | **222** | 24 | **1015** | **233** | 1011 | 234 | 1025 | 230 |
| 470.lbm | 24 | 1344 | 245 | 1345 | 245 | **1344** | **245** | 12 | **659** | **250** | 658 | 251 | 659 | 250 |
| 481.wrf | 24 | 1370 | 196 | 1368 | 196 | **1370** | **196** | 24 | 1370 | 196 | 1368 | 196 | **1370** | **196** |
| 482.sphinx3 | 24 | 2426 | 193 | **2427** | **193** | 2428 | 193 | 24 | 2324 | 201 | 2323 | 201 | **2324** | **201** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 10800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
Turbo Mode enabled in BIOS
Turbo Boost set to Traditional in BIOS
Power C-State enabled in BIOS
Data Reuse disabled in BIOS
Demand Scrub disabled in BIOS
```

## General Notes

```
Binaries compiled on RHEL5.5
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

# Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

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# Peak Optimization Flags (Continued)

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml


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