SPEC® CFP2006 Result

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Acer Incorporated

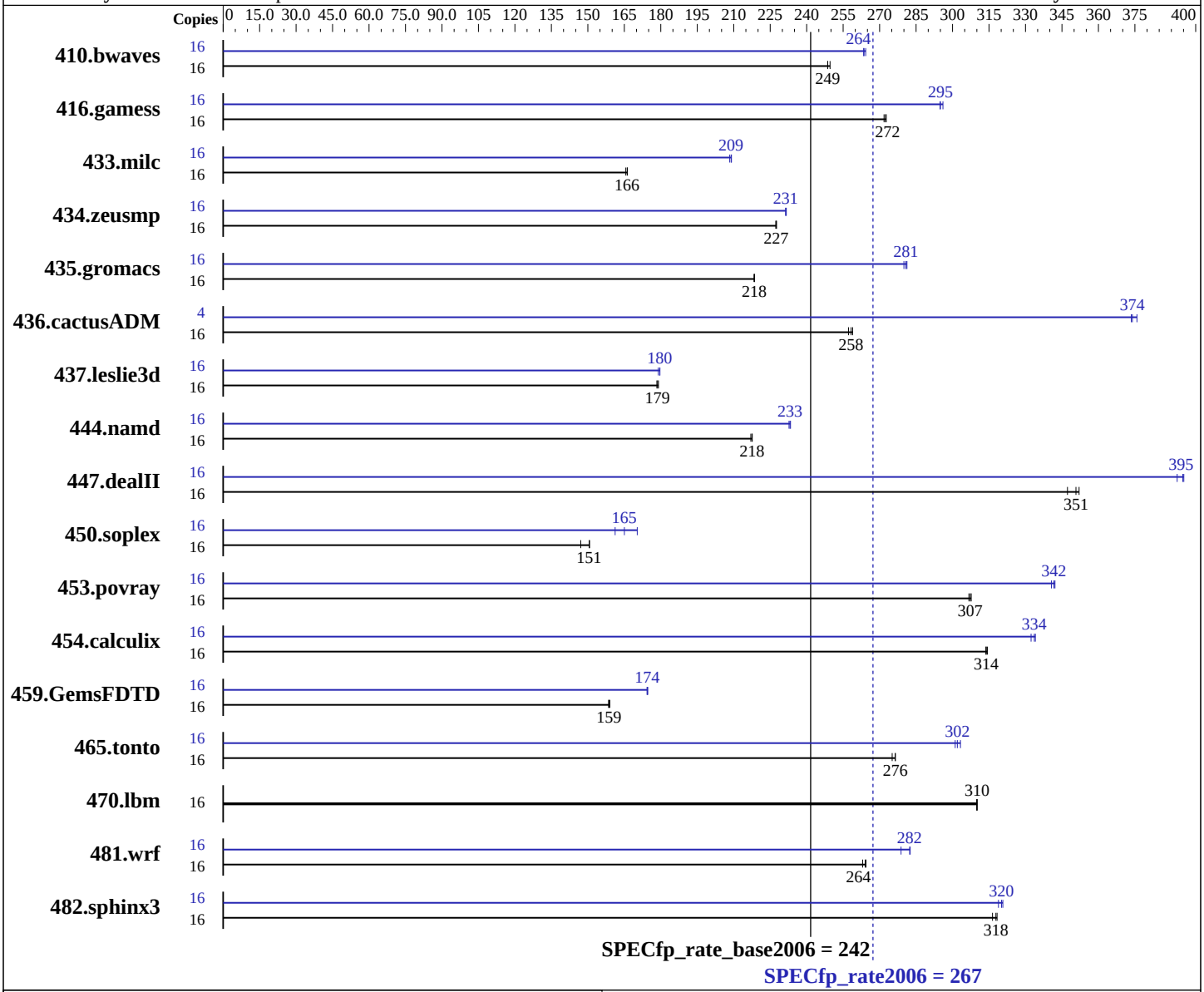
Acer AR585 F1 (AMD Opteron 6140)

SPECfp®_rate2006 = 267

SPECfp_rate_base2006 = 242

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2011
Hardware Availability: Feb-2011
Software Availability: Jul-2010



Hardware

CPU Name:	AMD Opteron 6140
CPU Characteristics:	
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1 Kernel 2.6.32.12-0.7-default
Compiler:	x86 Open64 4.2.4 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multiuser)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores
Other Cache: None
Memory: 64 GB (16 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	875	249	**874**	**249**	871	250	16	**825**	**264**	825	263	823	264
416.gamess	16	1149	273	**1150**	**272**	1153	272	16	1063	295	1058	296	**1062**	**295**
433.milc	16	883	166	**884**	**166**	887	166	16	703	209	**703**	**209**	705	208
434.zeusmp	16	640	228	**640**	**227**	641	227	16	629	231	**629**	**231**	629	232
435.gromacs	16	**523**	**218**	523	219	523	218	16	408	280	406	281	**407**	**281**
436.cactusADM	16	743	257	**740**	**258**	738	259	4	128	374	127	376	**128**	**374**
437.leslie3d	16	**842**	**179**	840	179	843	178	16	840	179	**838**	**180**	837	180
444.namd	16	591	217	**590**	**218**	590	218	16	551	233	**550**	**233**	550	233
447.dealII	16	**522**	**351**	520	352	527	347	16	466	392	463	395	**464**	**395**
450.soplex	16	907	147	**886**	**151**	885	151	16	828	161	**809**	**165**	783	170
453.povray	16	**277**	**307**	278	307	277	308	16	**249**	**342**	249	342	250	341
454.calculix	16	**421**	**314**	420	314	421	314	16	397	332	395	334	**396**	**334**
459.GemsFDTD	16	**1070**	**159**	1067	159	1071	159	16	972	175	974	174	**973**	**174**
465.tonto	16	**570**	**276**	572	275	569	276	16	**521**	**302**	519	303	523	301
470.lbm	16	**709**	**310**	709	310	709	310	16	**709**	**310**	709	310	709	310
481.wrf	16	**677**	**264**	676	264	680	263	16	641	279	**633**	**282**	632	283
482.sphinx3	16	985	316	980	318	**981**	**318**	16	978	319	**974**	**320**	972	321

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"
OMP_NUM_THREADS = "4"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
Binaries were compiled on SLES10 SP2 with binutils 2.18
This result was measured on the Gateway GR585 F1.
The Acer AR585 F1 and Gateway GR585 F1 are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m
```

C++ benchmarks:
```
-march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-OPT:malloc_alg=1 -HP:bdt=2m
```

Fortran benchmarks:
```
-march=barcelona -mso -Ofast -HP
```

Benchmarks using both Fortran and C:
```
-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP
```

Peak Compiler Invocation

C benchmarks:
```
opencc
```

C++ benchmarks:
```
openCC
```

Fortran benchmarks:
```
openf95
```

Benchmarks using both Fortran and C:
```
opencc openf95
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

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Peak Optimization Flags (Continued)

437.leslie3d: `-march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m`

459.GemsFDTD: `-march=barcelona -mso -Ofast -LNO:fission=2 -LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1 -HP`

465.tonto: `-march=barcelona -mso -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP`

Benchmarks using both Fortran and C:

435.gromacs: `-march=barcelona -mso -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m`

436.cactusADM: `-march=barcelona -mso -fb_create fbdata`(pass 1) `-fb_opt fbdata`(pass 2) `-Ofast -apo -LNO:prefetch_ahead=1 -HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100`

454.calculix: `-march=barcelona -mso -Ofast -CG:load_exe=0 -CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on -LNO:prefetch_ahead=30 -WOPT:unroll=2 -GRA:optimize_boundary=on -HP:bdt=2m:heap=2m`

481.wrf: `-march=barcelona -mso -Ofast -LNO:blocking=off -LNO:prefetch_ahead=10 -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow -HP`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml

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