SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5667)

$SPECfp®\_rate2006 = \quad 224$

$SPECfp\_rate\_base2006 = \quad 219$
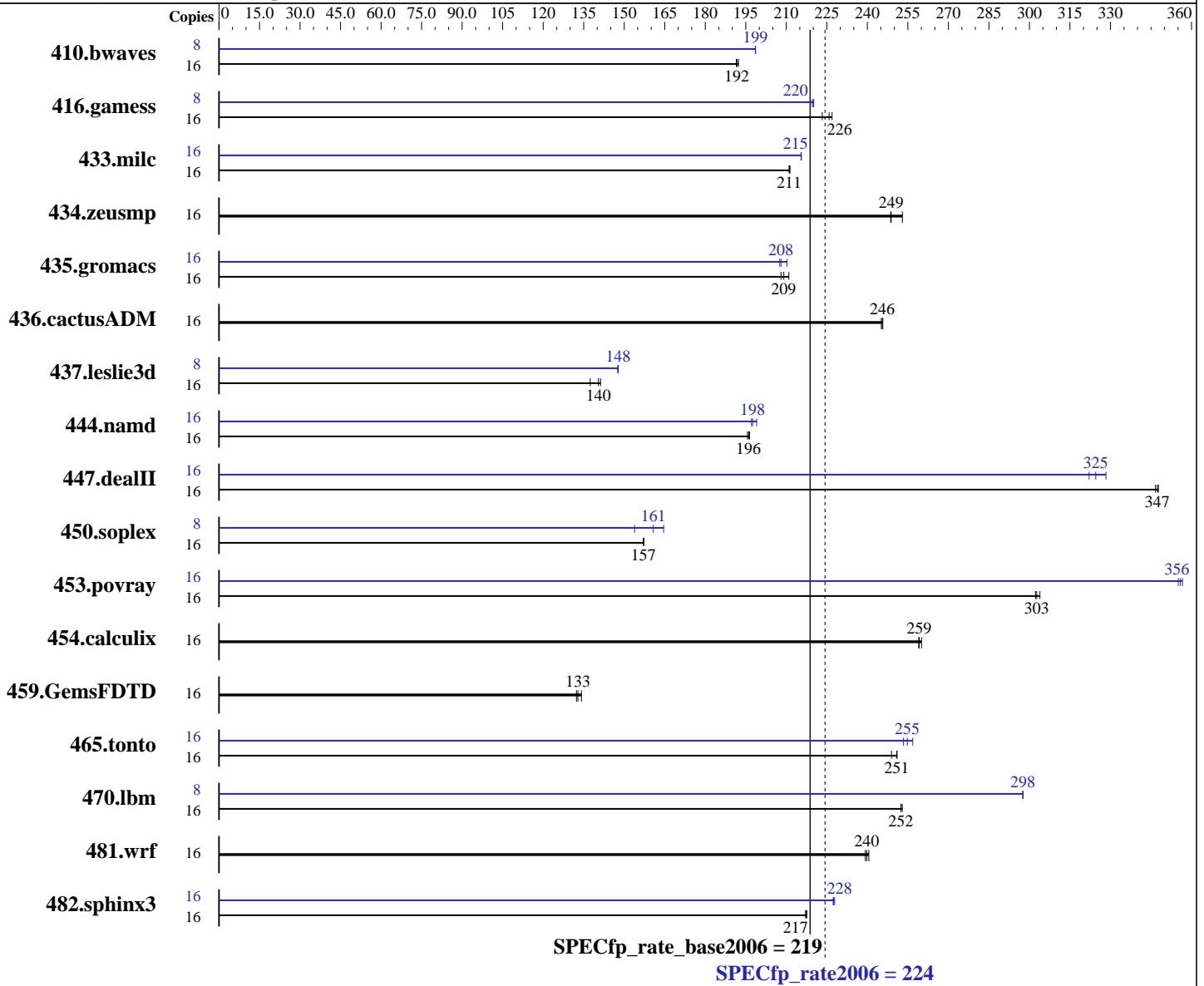
CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2011
Hardware Availability: Jun-2010
Software Availability: Jan-2011

Copies	
410.bwaves	8: 199 / 16: 192
416.gamess	8: 220 / 16: 226
433.milc	16: 215 / 16: 211
434.zeusmp	16: 249
435.gromacs	16: 208 / 16: 209
436.cactusADM	16: 246
437.leslie3d	8: 148 / 16: 140
444.namd	16: 198 / 16: 196
447.dealII	16: 325 / 16: 347
450.soplex	8: 161 / 16: 157
453.povray	16: 356 / 16: 303
454.calculix	16: 259
459.GemsFDTD	16: 133
465.tonto	16: 255 / 16: 251
470.lbm	8: 298 / 16: 252
481.wrf	16: 240
482.sphinx3	16: 228 / 16: 217

SPECfp_rate_base2006 = 219

SPECfp_rate2006 = 224

Hardware

CPU Name: Intel Xeon X5667
CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz: 3067
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 64-bit

Continued on next page

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5667)

SPECfp_rate2006 =   224

SPECfp_rate_base2006 =   219

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Jun-2010

Software Availability: Jan-2011

L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 300 GB SATA, 10000 RPM
Other Hardware:	None
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1131	192	**1134**	**192**	1135	192	8	547	199	548	199	**547**	**199**
416.gamess	16	**1387**	**226**	1381	227	1403	223	8	**712**	**220**	713	220	712	220
433.milc	16	**696**	**211**	695	211	696	211	16	**682**	**215**	681	216	682	215
434.zeusmp	16	576	253	**585**	**249**	586	249	16	576	253	**585**	**249**	586	249
435.gromacs	16	542	211	**546**	**209**	549	208	16	543	210	550	208	**549**	**208**
436.cactusADM	16	780	245	778	246	**779**	**246**	16	780	245	778	246	**779**	**246**
437.leslie3d	16	1065	141	**1071**	**140**	1094	137	8	509	148	**509**	**148**	510	148
444.namd	16	653	196	**655**	**196**	656	196	16	645	199	**650**	**198**	651	197
447.dealII	16	528	347	**527**	**347**	526	348	16	**564**	**325**	557	328	568	322
450.soplex	16	849	157	**849**	**157**	849	157	8	434	154	**415**	**161**	405	165
453.povray	16	**281**	**303**	282	302	280	304	16	**239**	**356**	240	355	239	357
454.calculix	16	508	260	510	259	**509**	**259**	16	508	260	510	259	**509**	**259**
459.GemsFDTD	16	1265	134	1283	132	**1278**	**133**	16	1265	134	1283	132	**1278**	**133**
465.tonto	16	**628**	**251**	632	249	627	251	16	613	257	621	253	**618**	**255**
470.lbm	16	869	253	**871**	**252**	871	252	8	**369**	**298**	369	298	369	298
481.wrf	16	**745**	**240**	747	239	743	241	16	**745**	**240**	747	239	743	241
482.sphinx3	16	1433	218	1436	217	**1435**	**217**	16	1372	227	1368	228	**1371**	**228**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set environment stack size
    Large pages were disabled for this run

Platform Notes

    BIOS Settings:
    Fan speed = full speed (Default = Balanced)
    Data Reuse = Disabled (Default = Enabled)

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5667)

SPECfp_rate2006 =   224

SPECfp_rate_base2006 =   219

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Jun-2010

Software Availability: Jan-2011

General Notes

```
Binaries compiled on RHEL5.5
This result was measured on the Gateway GR380 F1.
The Acer AR360 F1, AR380 F1, Gateway GR360 F1 are electronically equivalent
```

Base Compiler Invocation

C benchmarks:
        icc   -m64

C++ benchmarks:
        icpc   -m64

Fortran benchmarks:
        ifort -m64

Benchmarks using both Fortran and C:
        icc   -m64 ifort -m64

Base Portability Flags

```
        410.bwaves: -DSPEC_CPU_LP64
        416.gamess: -DSPEC_CPU_LP64
          433.milc: -DSPEC_CPU_LP64
        434.zeusmp: -DSPEC_CPU_LP64
       435.gromacs: -DSPEC_CPU_LP64 -nofor_main
      436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
       437.leslie3d: -DSPEC_CPU_LP64
          444.namd: -DSPEC_CPU_LP64
         447.dealII: -DSPEC_CPU_LP64
        450.soplex: -DSPEC_CPU_LP64
        453.povray: -DSPEC_CPU_LP64
       454.calculix: -DSPEC_CPU_LP64 -nofor_main
      459.GemsFDTD: -DSPEC_CPU_LP64
         465.tonto: -DSPEC_CPU_LP64
          470.lbm: -DSPEC_CPU_LP64
           481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
       482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Continued on next page

Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5667)

SPECfp_rate2006 =   224

SPECfp_rate_base2006 =   219

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Jun-2010

Software Availability: Jan-2011

Base Optimization Flags (Continued)

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
```
icpc   -m64
```

450.soplex: `icpc -m32`

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

SPEC CFP2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5667)

SPECfp_rate2006 = 224

SPECfp_rate_base2006 = 219

CPU2006 license: 97	**Test date:** Apr-2011
Test sponsor: Acer Incorporated	**Hardware Availability:** Jun-2010
Tested by: Acer Incorporated	**Software Availability:** Jan-2011

Peak Optimization Flags

C benchmarks:

```
433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
         -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
         -ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2
```

C++ benchmarks:

```
444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
          -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
            -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
            -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Fortran benchmarks:

```
410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
            -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
              -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
           -inline-calloc -opt-malloc-options=3
           -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Acer Incorporated

Gateway GR380 F1 (Intel Xeon X5667)

SPECfp_rate2006 =   224

SPECfp_rate_base2006 =   219

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Apr-2011
Hardware Availability: Jun-2010
Software Availability: Jan-2011

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
             -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes
```

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml

SPEC and SPECfp are registered trademarks of the Standard Performance
Evaluation Corporation. All other brand and product names appearing in
this result are trademarks or registered trademarks of their respective
holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 20:48:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 May 2011.