



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

**SPECint®\_rate2006 = 269**

IBM System x3550 M3 (Intel Xeon X5647)

**SPECint\_rate\_base2006 = 253**

CPU2006 license: 11

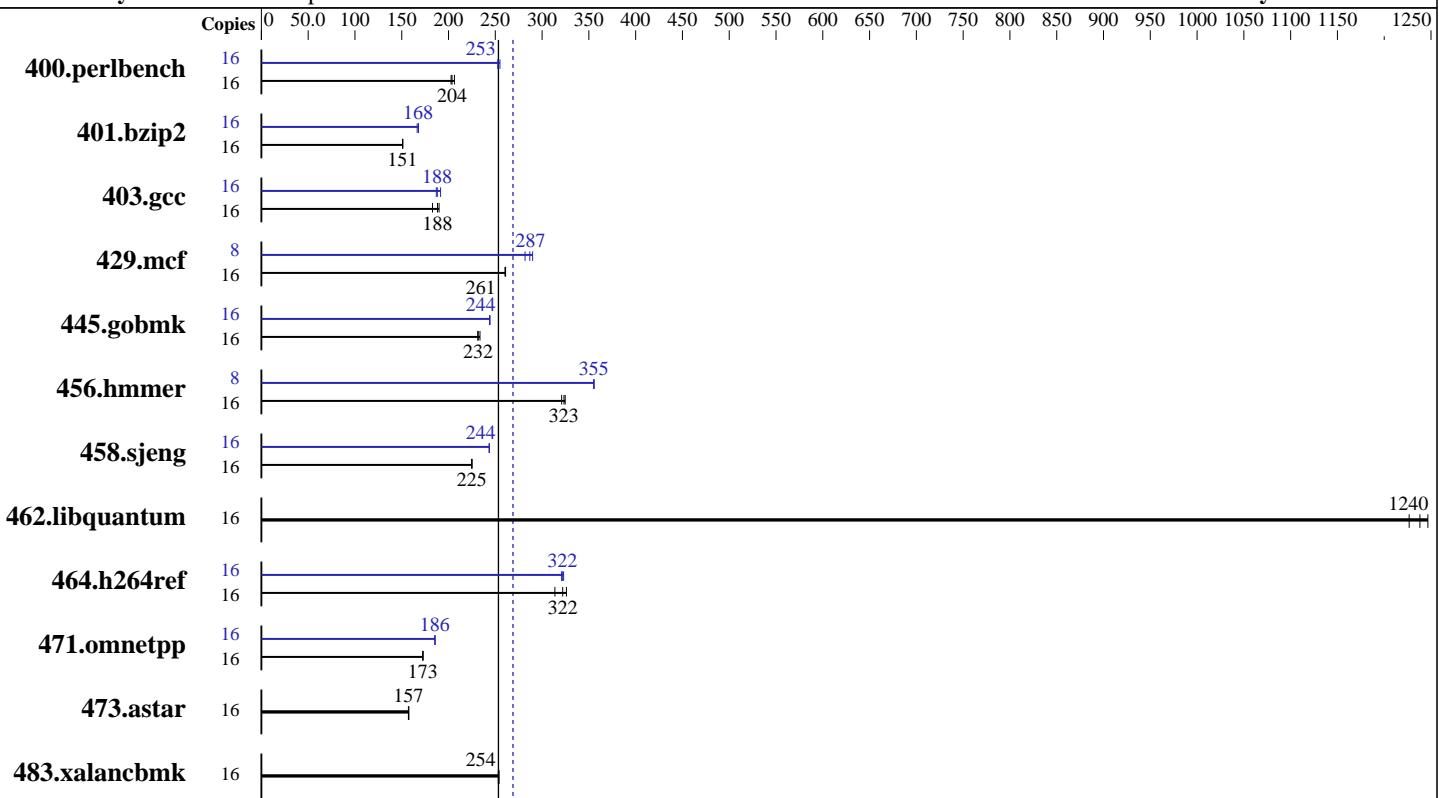
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



**SPECint\_rate\_base2006 = 253**

**SPECint\_rate2006 = 269**

## Hardware

CPU Name:	Intel Xeon X5647
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz)
Disk Subsystem:	1 x 146 GB SAS, 15000 RPM
Other Hardware:	None

## Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

**SPECint\_rate2006 = 269**

**IBM System x3550 M3 (Intel Xeon X5647)**

**SPECint\_rate\_base2006 = 253**

**CPU2006 license:** 11

**Test date:** Apr-2011

**Test sponsor:** IBM Corporation

**Hardware Availability:** Feb-2011

**Tested by:** IBM Corporation

**Software Availability:** Jan-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	758	206	<b>767</b>	<b>204</b>	771	203	16	<b>618</b>	<b>253</b>	618	253	613	255
401.bzip2	16	<b>1023</b>	<b>151</b>	1021	151	1024	151	16	927	166	<b>921</b>	<b>168</b>	919	168
403.gcc	16	<b>684</b>	<b>188</b>	678	190	704	183	16	672	192	<b>685</b>	<b>188</b>	689	187
429.mcf	16	<b>559</b>	<b>261</b>	559	261	561	260	8	259	282	<b>254</b>	<b>287</b>	252	290
445.gobmk	16	718	234	726	231	<b>724</b>	<b>232</b>	16	688	244	687	244	<b>687</b>	<b>244</b>
456.hammer	16	460	325	465	321	<b>462</b>	<b>323</b>	8	210	355	210	356	<b>210</b>	<b>355</b>
458.sjeng	16	860	225	<b>861</b>	<b>225</b>	861	225	16	795	243	794	244	<b>795</b>	<b>244</b>
462.libquantum	16	266	1250	<b>268</b>	<b>1240</b>	270	1230	16	266	1250	<b>268</b>	<b>1240</b>	270	1230
464.h264ref	16	<b>1099</b>	<b>322</b>	1129	314	1086	326	16	1104	321	<b>1100</b>	<b>322</b>	1096	323
471.omnetpp	16	579	173	<b>579</b>	<b>173</b>	579	173	16	<b>539</b>	<b>186</b>	539	186	539	186
473.astar	16	713	158	715	157	<b>714</b>	<b>157</b>	16	713	158	715	157	<b>714</b>	<b>157</b>
483.xalancbmk	16	435	254	436	253	<b>435</b>	<b>254</b>	16	435	254	436	253	<b>435</b>	<b>254</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

Turbo Mode enabled in BIOS  
 Turbo Boost set to Traditional in BIOS  
 Power C-State enabled in BIOS  
 Data Reuse disabled in BIOS  
 Demand Scrub disabled in BIOS

## General Notes

Binaries compiled on RHEL5.5



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

**SPECint\_rate2006 = 269**

IBM System x3550 M3 (Intel Xeon X5647)

**SPECint\_rate\_base2006 = 253**

CPU2006 license: 11

**Test date:** Apr-2011

Test sponsor: IBM Corporation

**Hardware Availability:** Feb-2011

Tested by: IBM Corporation

**Software Availability:** Jan-2011

## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfsl -Wl,-hugetlbfsl-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmr: icc -m64

458.sjeng: icc -m64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint\_rate2006 = 269

IBM System x3550 M3 (Intel Xeon X5647)

SPECint\_rate\_base2006 = 253

CPU2006 license: 11

Test date: Apr-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011

## Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT  
  
429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32  
  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32  
  
456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
462.libquantum: basepeak = yes

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

**SPECint\_rate2006 = 269**

IBM System x3550 M3 (Intel Xeon X5647)

**SPECint\_rate\_base2006 = 253**

CPU2006 license: 11

Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Apr-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 20:52:54 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 May 2011.