



# SPEC® CINT2006 Result

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Fujitsu

PRIMERGY RX100 S7, Intel Xeon E3-1260L, 2.40 GHz

**SPECint\_rate2006 = 128**

**SPECint\_rate\_base2006 = 122**

CPU2006 license: 19

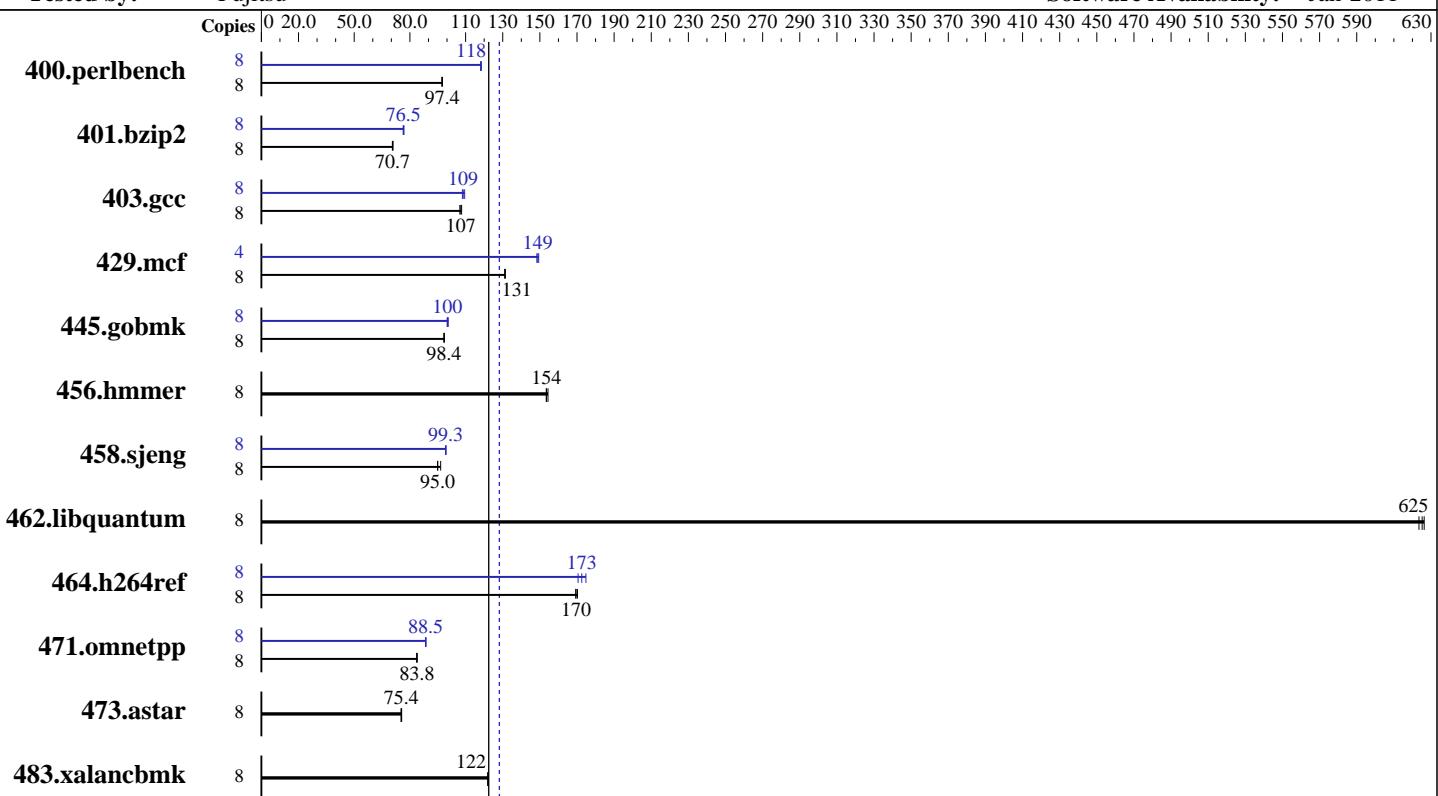
Test date: May-2011

Test sponsor: Fujitsu

Hardware Availability: Jun-2011

Tested by: Fujitsu

Software Availability: Jan-2011



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## Hardware

CPU Name: Intel Xeon E3-1260L  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)  
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM  
 Other Hardware: --

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	802	97.5	804	97.2	<b>803</b>	<b>97.4</b>	8	662	118	<b>660</b>	<b>118</b>	660	118
401.bzip2	8	1089	70.9	<b>1091</b>	<b>70.7</b>	1094	70.6	8	1005	76.8	<b>1009</b>	<b>76.5</b>	1011	76.4
403.gcc	8	602	107	<b>600</b>	<b>107</b>	597	108	8	595	108	588	109	<b>592</b>	<b>109</b>
429.mcf	8	556	131	555	131	<b>556</b>	<b>131</b>	4	244	149	246	148	<b>245</b>	<b>149</b>
445.gobmk	8	852	98.5	<b>853</b>	<b>98.4</b>	853	98.3	8	839	100	<b>837</b>	<b>100</b>	834	101
456.hmmer	8	<b>486</b>	<b>154</b>	483	154	486	153	8	<b>486</b>	<b>154</b>	483	154	486	153
458.sjeng	8	<b>1019</b>	<b>95.0</b>	1020	94.9	1002	96.6	8	975	99.3	974	99.4	<b>974</b>	<b>99.3</b>
462.libquantum	8	266	624	<b>265</b>	<b>625</b>	265	626	8	266	624	<b>265</b>	<b>625</b>	265	626
464.h264ref	8	1046	169	1040	170	<b>1044</b>	<b>170</b>	8	1013	175	<b>1026</b>	<b>173</b>	1038	171
471.omnetpp	8	597	83.7	596	83.9	<b>597</b>	<b>83.8</b>	8	565	88.4	<b>565</b>	<b>88.5</b>	564	88.7
473.astar	8	743	75.6	<b>745</b>	<b>75.4</b>	746	75.3	8	743	75.6	<b>745</b>	<b>75.4</b>	746	75.3
483.xalancbmk	8	452	122	<b>452</b>	<b>122</b>	452	122	8	452	122	<b>452</b>	<b>122</b>	452	122

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smarterheap -lsmarterheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmr: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll14  
-auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll12  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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