SPEC® CINT2006 Result
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Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon E5645, 2.40GHz)

SPECint®_rate2006 =   313

SPECint_rate_base2006 =   294

CPU2006 license: 97
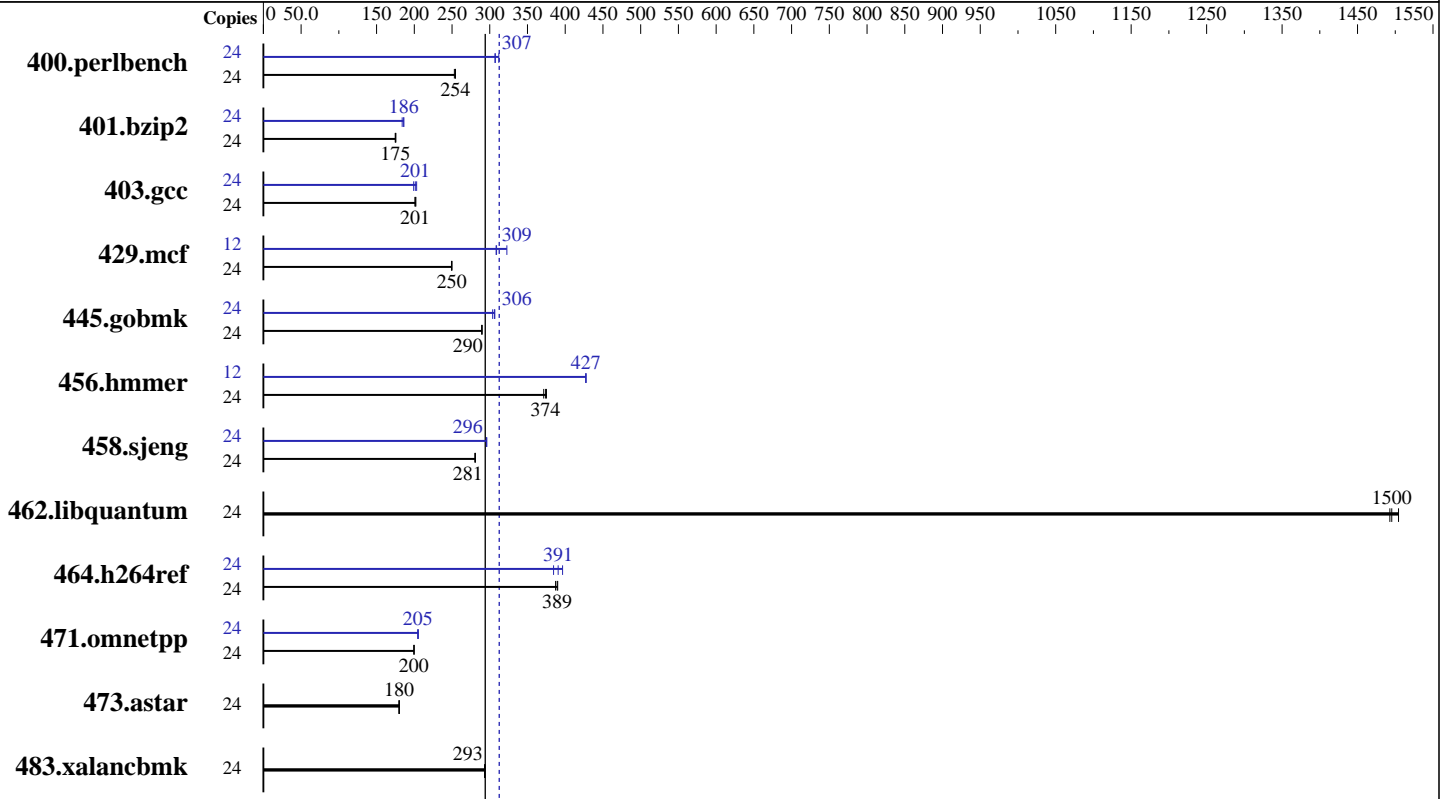
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

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Hardware

CPU Name: Intel Xeon E5645

CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz

CPU MHz: 2400

FPU: Integrated

CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core

CPU(s) orderable: 1, 2 chips

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 12 MB I+D on chip per chip

Other Cache: None

Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)

Disk Subsystem: 1 x 1000 GB SATA 7200RPM

Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default

Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.1.116 Build 20101116

Auto Parallel: No

File System: ReiserFS

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	926	253	921	255	**922**	**254**	24	**763**	**307**	764	307	752	312
401.bzip2	24	1319	176	1325	175	**1324**	**175**	24	1245	186	1259	184	**1245**	**186**
403.gcc	24	**960**	**201**	962	201	956	202	24	**959**	**201**	951	203	970	199
429.mcf	24	876	250	878	249	**877**	**250**	12	**354**	**309**	355	308	339	323
445.gobmk	24	**869**	**290**	869	290	870	289	24	**822**	**306**	821	307	829	304
456.hmmer	24	597	375	603	372	**599**	**374**	12	**262**	**427**	262	427	262	428
458.sjeng	24	1034	281	**1035**	**281**	1035	280	24	981	296	982	296	**982**	**296**
462.libquantum	24	331	1500	**333**	**1500**	333	1490	24	331	1500	**333**	**1500**	333	1490
464.h264ref	24	**1364**	**389**	1362	390	1372	387	24	**1360**	**391**	1339	397	1381	385
471.omnetpp	24	**752**	**200**	751	200	752	200	24	733	205	730	205	**732**	**205**
473.astar	24	**937**	**180**	935	180	938	180	24	**937**	**180**	935	180	938	180
483.xalancbmk	24	565	293	**564**	**293**	564	293	24	565	293	**564**	**293**	564	293

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was used to set environment stack size
    Large pages were disabled for this run
```

Platform Notes

```
    BIOS Settings:
    Fan speed = full speed (Default = Energy Saving)
    Data Reuse = Disabled (Default = Enabled)
```

General Notes

```
    Binaries compiled on RHEL5.5
     The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1  and Gateway GW2000ht-GW170ht F1
     are electronically equivalent.  This result was measured on Gateway GW2000ht-GW170ht F1.
```

Base Compiler Invocation

C benchmarks:
```
        icc  -m32
```

Continued on next page

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

      400.perlbench: -DSPEC_CPU_LINUX_IA32
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

      401.bzip2: icc -m64

    456.hmmer: icc -m64

      458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
   400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
       458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
   400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -opt-prefetch -auto-ilp32 -ansi-alias
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

         403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
                  -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

         429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -auto-ilp32

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                  -ansi-alias -auto-ilp32

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

       458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -unroll4 -auto-ilp32
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

   462.libquantum: basepeak = yes

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -unroll2 -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

471.omnetpp (continued):
```
-L/smartheap -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

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