SPEC® CFP2006 Result

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Intel Corporation

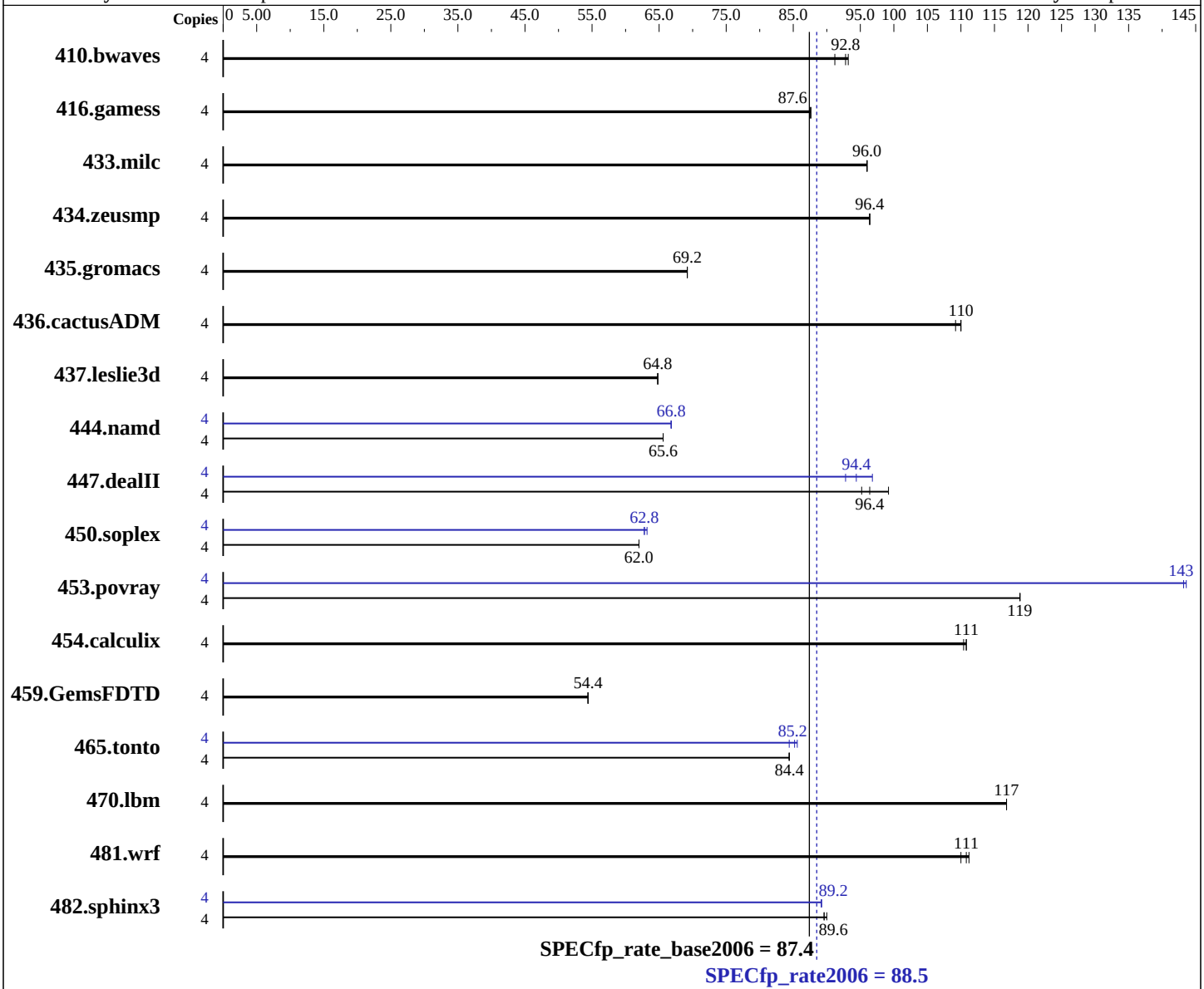
Intel DH67BLB3 Motherboard (Intel Core i5-2500T)

SPECfp®_rate2006 = 88.5

SPECfp_rate_base2006 = 87.4

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Core i5-2500T
CPU Characteristics:	Intel Turbo Boost Technology up to 3.3 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	597	91.2	584	93.2	**585**	**92.8**	4	597	91.2	584	93.2	**585**	**92.8**
416.gamess	4	894	87.6	894	87.6	**894**	**87.6**	4	894	87.6	894	87.6	**894**	**87.6**
433.milc	4	382	96.0	382	96.0	**382**	**96.0**	4	382	96.0	382	96.0	**382**	**96.0**
434.zeusmp	4	378	96.4	**377**	**96.4**	377	96.4	4	378	96.4	**377**	**96.4**	377	96.4
435.gromacs	4	412	69.2	**412**	**69.2**	412	69.2	4	412	69.2	**412**	**69.2**	412	69.2
436.cactusADM	4	435	110	**435**	**110**	437	109	4	435	110	**435**	**110**	437	109
437.leslie3d	4	580	64.8	580	64.8	**580**	**64.8**	4	580	64.8	580	64.8	**580**	**64.8**
444.namd	4	489	65.6	**489**	**65.6**	489	65.6	4	481	66.8	**481**	**66.8**	482	66.8
447.dealII	4	**476**	**96.4**	462	99.2	480	95.2	4	492	92.8	**485**	**94.4**	472	96.8
450.soplex	4	537	62.0	537	62.0	**537**	**62.0**	4	530	62.8	529	63.2	**530**	**62.8**
453.povray	4	179	119	179	119	**179**	**119**	4	**148**	**143**	148	144	148	143
454.calculix	4	299	110	297	111	**298**	**111**	4	299	110	297	111	**298**	**111**
459.GemsFDTD	4	780	54.4	777	54.4	**777**	**54.4**	4	780	54.4	777	54.4	**777**	**54.4**
465.tonto	4	466	84.4	**467**	**84.4**	467	84.4	4	460	85.6	466	84.4	**462**	**85.2**
470.lbm	4	471	117	471	117	**471**	**117**	4	471	117	471	117	**471**	**117**
481.wrf	4	406	110	402	111	**403**	**111**	4	406	110	402	111	**403**	**111**
482.sphinx3	4	869	89.6	**868**	**89.6**	867	90.0	4	**873**	**89.2**	872	89.2	873	89.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 /names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER /names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

C++ benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

447.dealII: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

450.soplex: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

453.povray: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml

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For other inquiries, please contact webmaster@spec.org.

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