



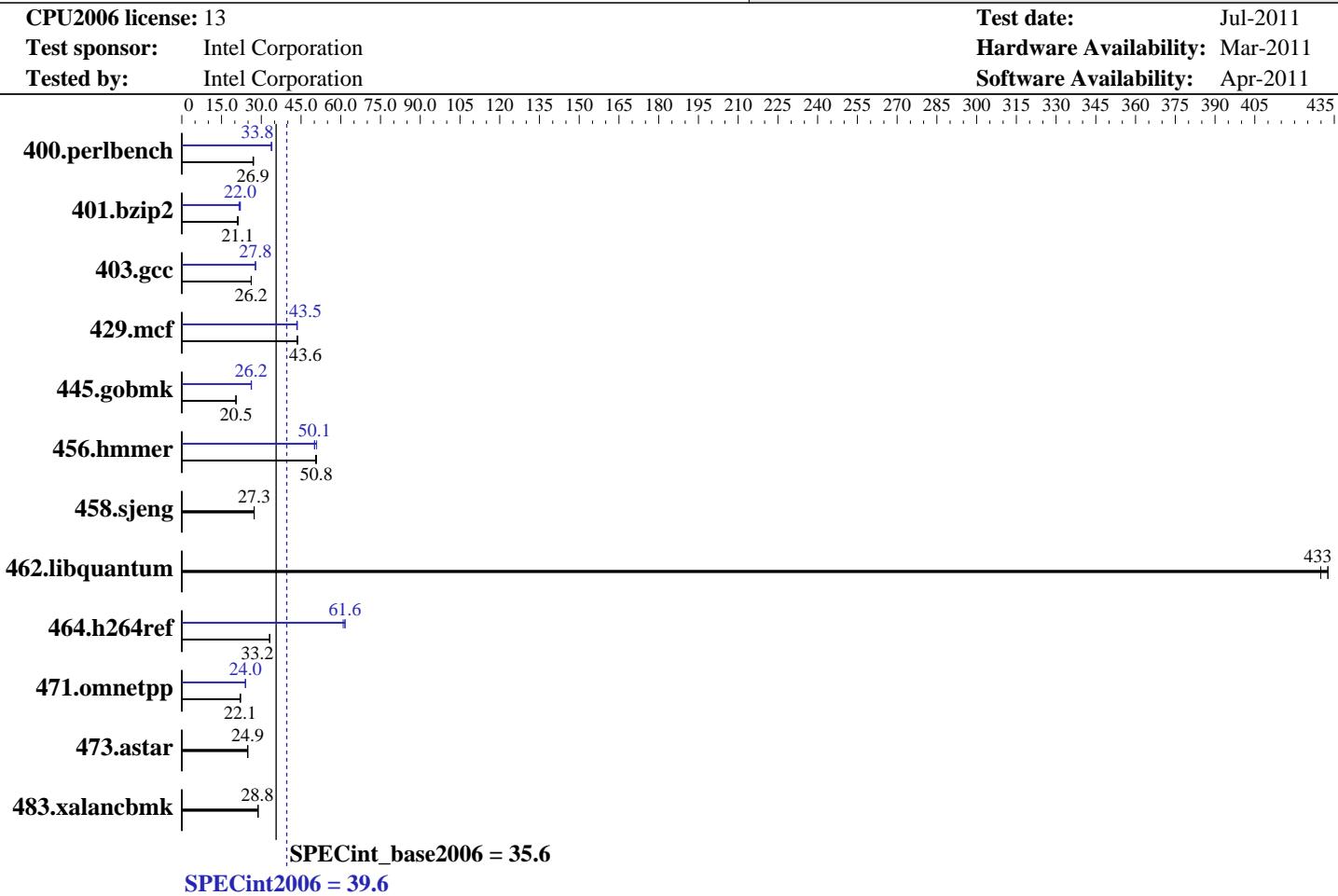
# SPEC® CINT2006 Result

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## Intel Corporation

Intel DH67BLB3 Motherboard (Intel Core i5-2390T)

**SPECint®2006 = 39.6**



Hardware		Software	
CPU Name:	Intel Core i5-2390T	Operating System:	Windows 7 Ultimate (64-bit)
CPU Characteristics:	Intel Turbo Boost Technology up to 3.5 GHz	Compiler:	Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.176 Build 20110309
CPU MHz:	2700		Microsoft Visual Studio 2008 Professional SP1 (for libraries)
FPU:	Integrated	Auto Parallel:	Yes
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core	File System:	NTFS
CPU(s) orderable:	1 chip	System State:	Default
Primary Cache:	32 KB I + 32 KB D on chip per core	Base Pointers:	32/64-bit
Secondary Cache:	256 KB I+D on chip per core	Peak Pointers:	32/64-bit
L3 Cache:	3 MB I+D on chip per chip	Other Software:	SmartHeap Library Version 9.01 from <a href="http://www.microquill.com/">http://www.microquill.com/</a>
Other Cache:	None		
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)		
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM		
Other Hardware:	None		



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CPU2006 license: 13

Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	363	26.9	<b>363</b>	<b>26.9</b>	361	27.1	289	33.8	287	34.0	<b>289</b>	<b>33.8</b>
401.bzip2	460	21.0	452	21.3	<b>458</b>	<b>21.1</b>	<b>439</b>	<b>22.0</b>	446	21.6	437	22.1
403.gcc	307	26.2	<b>307</b>	<b>26.2</b>	307	26.2	<b>290</b>	<b>27.8</b>	289	27.8	290	27.8
429.mcf	209	43.6	209	43.7	<b>209</b>	<b>43.6</b>	210	43.4	<b>210</b>	<b>43.5</b>	209	43.6
445.gobmk	<b>513</b>	<b>20.5</b>	513	20.4	512	20.5	398	26.3	401	26.2	<b>400</b>	<b>26.2</b>
456.hmmer	<b>184</b>	<b>50.8</b>	185	50.5	184	50.8	<b>186</b>	<b>50.1</b>	186	50.1	183	50.9
458.sjeng	444	27.3	<b>444</b>	<b>27.3</b>	444	27.3	444	27.3	<b>444</b>	<b>27.3</b>	444	27.3
462.libquantum	47.9	433	<b>47.9</b>	<b>433</b>	48.2	430	47.9	433	<b>47.9</b>	<b>433</b>	48.2	430
464.h264ref	668	33.1	<b>667</b>	<b>33.2</b>	667	33.2	359	61.6	<b>359</b>	<b>61.6</b>	364	60.9
471.omnetpp	283	22.1	282	22.1	<b>283</b>	<b>22.1</b>	<b>260</b>	<b>24.0</b>	260	24.1	260	24.0
473.astar	283	24.8	282	24.9	<b>282</b>	<b>24.9</b>	283	24.8	282	24.9	<b>282</b>	<b>24.9</b>
483.xalancbmk	240	28.8	241	28.7	<b>240</b>	<b>28.8</b>	240	28.8	241	28.7	<b>240</b>	<b>28.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with Shin-G ATX case,  
PC Power and Cooling 1200W power supply  
OMP\_NUM\_THREADS set to number of processors cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
                  -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64
```

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## Base Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_P64  
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES  
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64  
473.astar: -DSPEC_CPU_P64  
483.xalancbmk: -DSPEC_CPU_P64 -Qoption_cpp,--no_wchar_t_keyword
```

## Base Optimization Flags

C benchmarks:

```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32  
/F512000000
```

C++ benchmarks:

```
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features  
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32  
/F512000000 shlw64M.lib -link /FORCE:MULTIPLE
```

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## Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32  
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F512000000

458.jeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32  
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000  
shlw64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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