# SPEC® CFP2006 Result

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## Dell Inc.

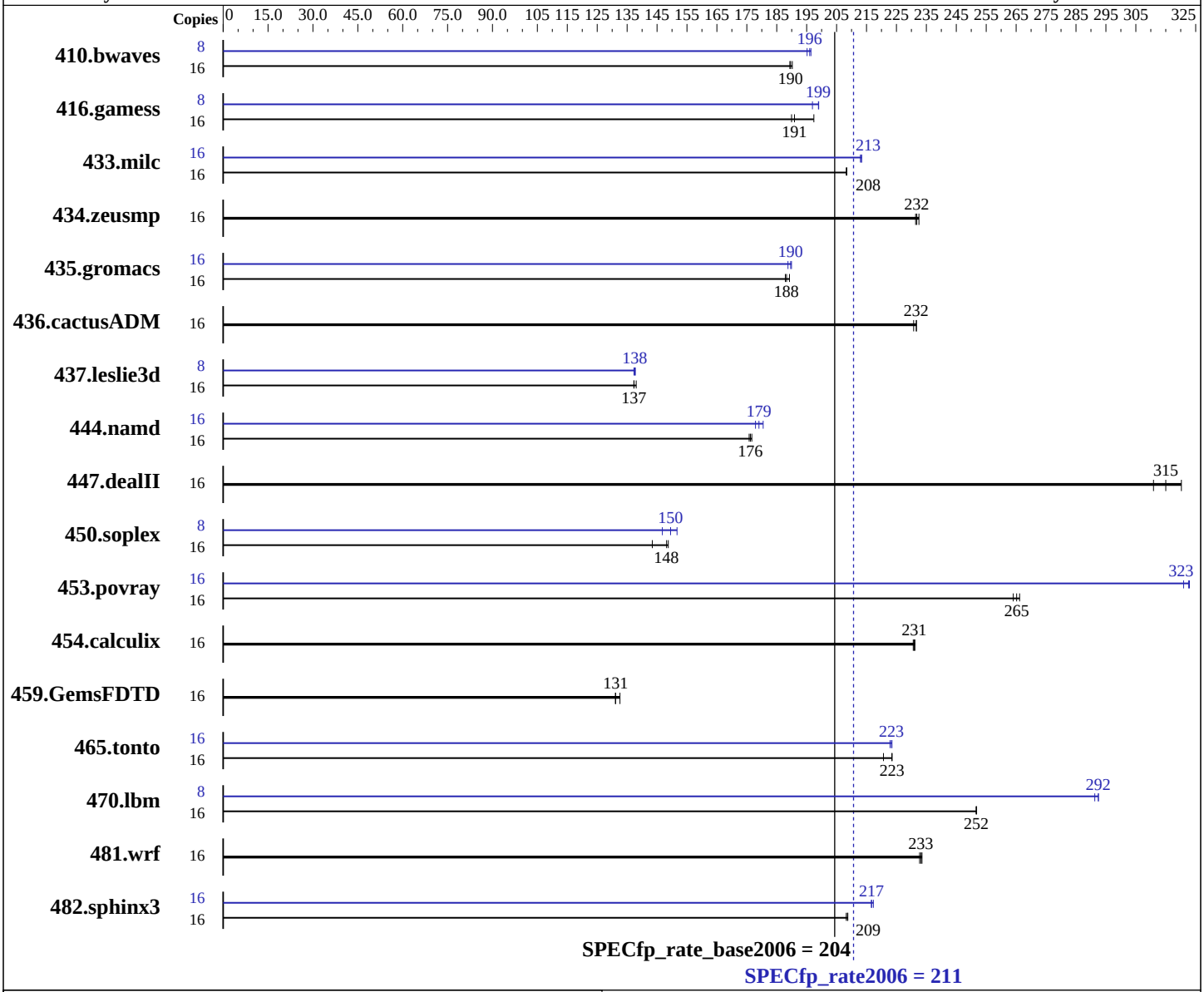
PowerEdge M610 (Intel Xeon X5560, 2.80 GHz)

SPECfp®_rate2006 = 211

SPECfp_rate_base2006 = 204

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Jul-2011
**Hardware Availability:** Mar-2009
**Software Availability:** Dec-2009




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5560 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.20 GHz |
| CPU MHz: | 2800 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB 10000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 16 | 1143 | 190 | **1147** | **190** | 1148 | 189 | 8 | 553 | 196 | **555** | **196** | 557 | 195 |
| 416.gamess | 16 | 1649 | 190 | **1641** | **191** | 1587 | 197 | 8 | 795 | 197 | 787 | 199 | **787** | **199** |
| 433.milc | 16 | 705 | 208 | 705 | 208 | **705** | **208** | 16 | 688 | 213 | 690 | 213 | **689** | **213** |
| 434.zeusmp | 16 | 629 | 231 | **628** | **232** | 626 | 233 | 16 | 629 | 231 | **628** | **232** | 626 | 233 |
| 435.gromacs | 16 | 604 | 189 | **607** | **188** | 608 | 188 | 16 | **602** | **190** | 601 | 190 | 605 | 189 |
| 436.cactusADM | 16 | **825** | **232** | 825 | 232 | 829 | 231 | 16 | **825** | **232** | 825 | 232 | 829 | 231 |
| 437.leslie3d | 16 | **1095** | **137** | 1096 | 137 | 1090 | 138 | 8 | 546 | 138 | 548 | 137 | **547** | **138** |
| 444.namd | 16 | 730 | 176 | **728** | **176** | 726 | 177 | 16 | **717** | **179** | 721 | 178 | 711 | 180 |
| 447.dealII | 16 | 572 | 320 | 589 | 311 | **581** | **315** | 16 | 572 | 320 | 589 | 311 | **581** | **315** |
| 450.soplex | 16 | 930 | 143 | **901** | **148** | 897 | 149 | 8 | 455 | 147 | **446** | **150** | 440 | 152 |
| 453.povray | 16 | 320 | 266 | 322 | 264 | **321** | **265** | 16 | 264 | 323 | **264** | **323** | 265 | 321 |
| 454.calculix | 16 | 572 | 231 | **572** | **231** | 571 | 231 | 16 | 572 | 231 | **572** | **231** | 571 | 231 |
| 459.GemsFDTD | 16 | 1280 | 133 | 1295 | 131 | **1294** | **131** | 16 | 1280 | 133 | 1295 | 131 | **1294** | **131** |
| 465.tonto | 16 | 704 | 224 | 713 | 221 | **704** | **223** | 16 | 706 | 223 | **705** | **223** | 704 | 224 |
| 470.lbm | 16 | 873 | 252 | **874** | **252** | 874 | 252 | 8 | **376** | **292** | 376 | 293 | 377 | 291 |
| 481.wrf | 16 | 765 | 234 | **766** | **233** | 768 | 233 | 16 | 765 | 234 | **766** | **233** | 768 | 233 |
| 482.sphinx3 | 16 | 1498 | 208 | 1494 | 209 | **1495** | **209** | 16 | 1435 | 217 | 1440 | 217 | **1439** | **217** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)
```

## General Notes

```
Binaries were compiled on RHEL5.5
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

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# Peak Optimization Flags (Continued)

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml`


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