SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

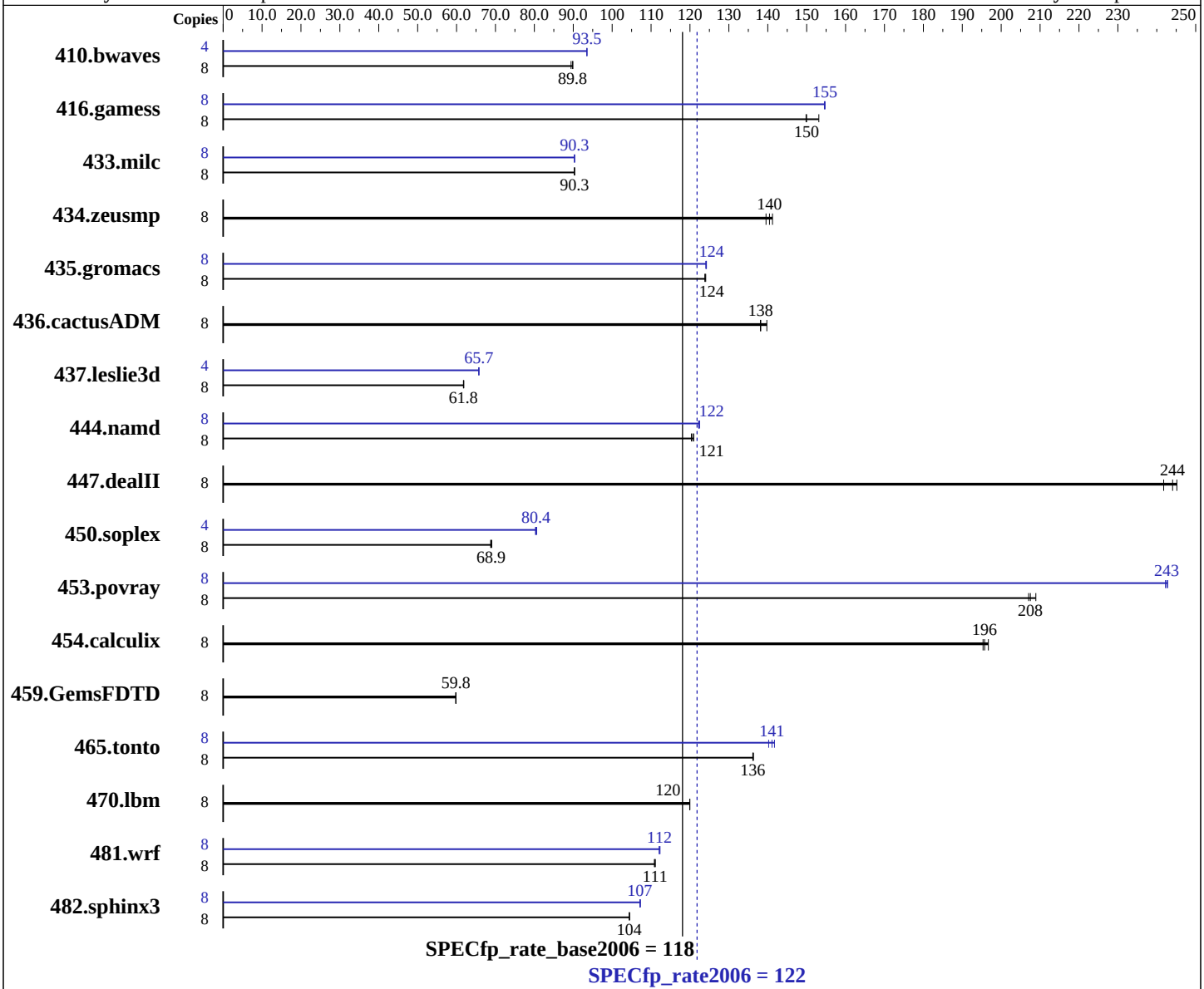
Intel DZ68DB motherboard (Intel Core i7-2600K)

SPECfp®_rate2006 = 122

SPECfp_rate_base2006 = 118

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2011
Hardware Availability: May-2011
Software Availability: Sep-2011



Hardware

CPU Name:	Intel Core i7-2600K
CPU Characteristics:	Intel Turbo Boost Technology up to 3.8 GHz
CPU MHz:	3401
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 5 (X11)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

Intel DZ68DB motherboard (Intel Core i7-2600K)

SPECfp_rate2006 = 122

SPECfp_rate_base2006 = 118

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Aug-2011
Hardware Availability: May-2011
Software Availability: Sep-2011

L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1216	89.4	**1210**	**89.8**	1209	89.9	4	582	93.5	581	93.5	**581**	**93.5**
416.gamess	8	1045	150	**1044**	**150**	1023	153	8	1013	155	**1013**	**155**	1013	155
433.milc	8	813	90.3	**813**	**90.3**	813	90.3	8	813	90.4	**813**	**90.3**	813	90.3
434.zeusmp	8	516	141	522	140	**518**	**140**	8	516	141	522	140	**518**	**140**
435.gromacs	8	**461**	**124**	460	124	461	124	8	460	124	**460**	**124**	460	124
436.cactusADM	8	**692**	**138**	684	140	692	138	8	**692**	**138**	684	140	692	138
437.leslie3d	8	**1217**	**61.8**	1215	61.9	1217	61.8	4	572	65.8	572	65.7	**572**	**65.7**
444.namd	8	530	121	**532**	**121**	533	120	8	**525**	**122**	525	122	524	122
447.dealII	8	373	245	**375**	**244**	379	242	8	373	245	**375**	**244**	379	242
450.soplex	8	**969**	**68.9**	966	69.1	970	68.8	4	416	80.3	414	80.6	**415**	**80.4**
453.povray	8	206	207	**205**	**208**	204	209	8	**175**	**243**	176	242	175	243
454.calculix	8	338	195	**337**	**196**	336	197	8	338	195	**337**	**196**	336	197
459.GemsFDTD	8	1420	59.8	**1418**	**59.8**	1418	59.9	8	1420	59.8	**1418**	**59.8**	1418	59.9
465.tonto	8	577	136	**578**	**136**	578	136	8	**558**	**141**	555	142	561	140
470.lbm	8	916	120	917	120	**916**	**120**	8	916	120	917	120	**916**	**120**
481.wrf	8	806	111	**805**	**111**	804	111	8	797	112	796	112	**797**	**112**
482.sphinx3	8	**1493**	**104**	1494	104	1493	104	8	1453	107	1456	107	**1455**	**107**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Sysinfo program /home/spec/cpu2006.1.2/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on rhel61-rahul.sc.intel.com Sat Aug 27 09:30:53 2011
```

Continued on next page

Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Core(TM) i7-2600K CPU @ 3.40GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

From /proc/meminfo
   MemTotal:        7966960 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   Red Hat Enterprise Linux Server release 6.1 (Santiago)

From /etc/*release* /etc/*version*
   redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
   system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
   Linux rhel61-rahul.sc.intel.com 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
   15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Aug 24 19:39

SPEC is set to: /home/spec/cpu2006.1.2
   Filesystem     Type    Size  Used Avail Use% Mounted on
   /dev/mapper/vg_rhel61rahul-lv_home
                  ext4    862G   29G  790G   4% /home

(End of data from sysinfo program)
```

Component Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/spec/cpu2006.1.2/smartheap:/home/spec/cpu2006.1.2/ic12.1-libs/ia32/:/home/spec/cpu2006.1.2/ic12.1-libs/intel64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages disabled with:
 echo never  > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Continued on next page

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `-xAVX -ipo -O3 -no-prec-div -static -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Intel-Platform-Settings-V1.2-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Intel-Platform-Settings-V1.2-revA.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 01:42:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 October 2011.