SPEC® CFP2006 Result

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Supermicro

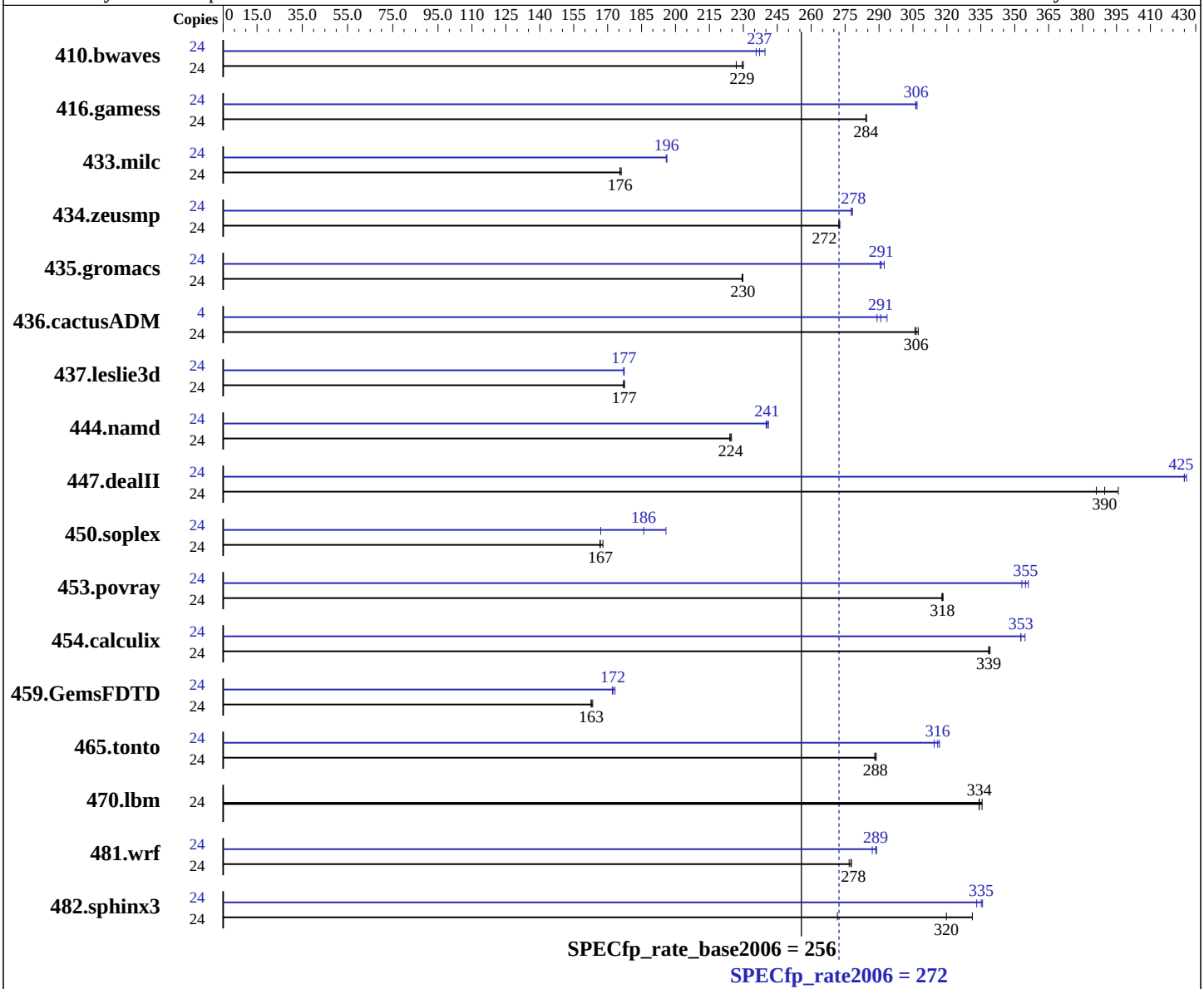
Supermicro SuperBlade SBA-7222G-T2, AMD Opteron 6166 HE

SPECfp®_rate2006 = 272

SPECfp_rate_base2006 = 256

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jul-2011
Hardware Availability: Sep-2010
Software Availability: Jul-2011



Hardware

CPU Name:	AMD Opteron 6166 HE
CPU Characteristics:	
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, Advanced Platform with patch RHSA-2009:1670, Kernel 2.6.32-131.0.15.el6
Compiler:	C/C++: Version 4.2.4 of Open64 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 6 cores
Other Cache: None
Memory: 64 GB (8 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 300 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	**1421**	**229**	1418	230	1438	227	24	1384	236	**1375**	**237**	1361	240
416.gamess	24	1652	284	**1653**	**284**	1654	284	24	**1534**	**306**	1532	307	1535	306
433.milc	24	1252	176	1256	175	**1255**	**176**	24	**1123**	**196**	1125	196	1123	196
434.zeusmp	24	802	272	801	273	**802**	**272**	24	785	278	786	278	**786**	**278**
435.gromacs	24	747	229	746	230	**746**	**230**	24	586	292	**589**	**291**	590	290
436.cactusADM	24	933	307	938	306	**936**	**306**	4	**164**	**291**	163	294	165	289
437.leslie3d	24	1272	177	1275	177	**1272**	**177**	24	1272	177	1274	177	**1273**	**177**
444.namd	24	859	224	**858**	**224**	856	225	24	802	240	**800**	**241**	798	241
447.dealII	24	694	396	711	386	**704**	**390**	24	**646**	**425**	646	425	645	426
450.soplex	24	1201	167	**1200**	**167**	1192	168	24	1199	167	**1076**	**186**	1022	196
453.povray	24	**401**	**318**	401	318	402	318	24	362	353	**360**	**355**	359	356
454.calculix	24	585	339	**585**	**339**	584	339	24	561	353	**561**	**353**	558	355
459.GemsFDTD	24	**1564**	**163**	1565	163	1558	163	24	**1477**	**172**	1470	173	1480	172
465.tonto	24	820	288	818	289	**819**	**288**	24	751	314	746	317	**747**	**316**
470.lbm	24	983	336	**986**	**334**	986	334	24	983	336	**986**	**334**	986	334
481.wrf	24	965	278	**966**	**278**	969	277	24	934	287	928	289	**929**	**289**
482.sphinx3	24	1722	272	1412	331	**1463**	**320**	24	1404	333	1393	336	**1395**	**335**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=10800 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "450"
LD_LIBRARY_PATH = "/spec/amd1002-rate-libs-revC/64:/spec/amd1002-rate-libs-revC/32"
OMP_NUM_THREADS = "6"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
Binaries were compiled on RHEL 5.5 with binutils 2.18
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m
```

C++ benchmarks:
```
-march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-OPT:malloc_alg=1 -HP:bdt=2m
```

Fortran benchmarks:
```
-march=barcelona -mso -Ofast -HP
```

Benchmarks using both Fortran and C:
```
-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP
```

Peak Compiler Invocation

C benchmarks:
```
opencc
```

C++ benchmarks:
```
openCC
```

Fortran benchmarks:
```
openf95
```

Benchmarks using both Fortran and C:
```
opencc openf95
```

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

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Peak Optimization Flags (Continued)

437.leslie3d: `-march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m`

459.GemsFDTD: `-march=barcelona -mso -Ofast -LNO:fission=2 -LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1 -HP`

465.tonto: `-march=barcelona -mso -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP`

Benchmarks using both Fortran and C:

435.gromacs: `-march=barcelona -mso -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m`

436.cactusADM: `-march=barcelona -mso -fb_create fbdata`(pass 1) `-fb_opt fbdata`(pass 2) `-Ofast -apo -LNO:prefetch_ahead=1 -HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100`

454.calculix: `-march=barcelona -mso -Ofast -CG:load_exe=0 -CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on -LNO:prefetch_ahead=30 -WOPT:unroll=2 -GRA:optimize_boundary=on -HP:bdt=2m:heap=2m`

481.wrf: `-march=barcelona -mso -Ofast -LNO:blocking=off -LNO:prefetch_ahead=10 -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow -HP`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html`
`http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml`
`http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml`

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