SPEC® CFP2006 Result

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Dell Inc.

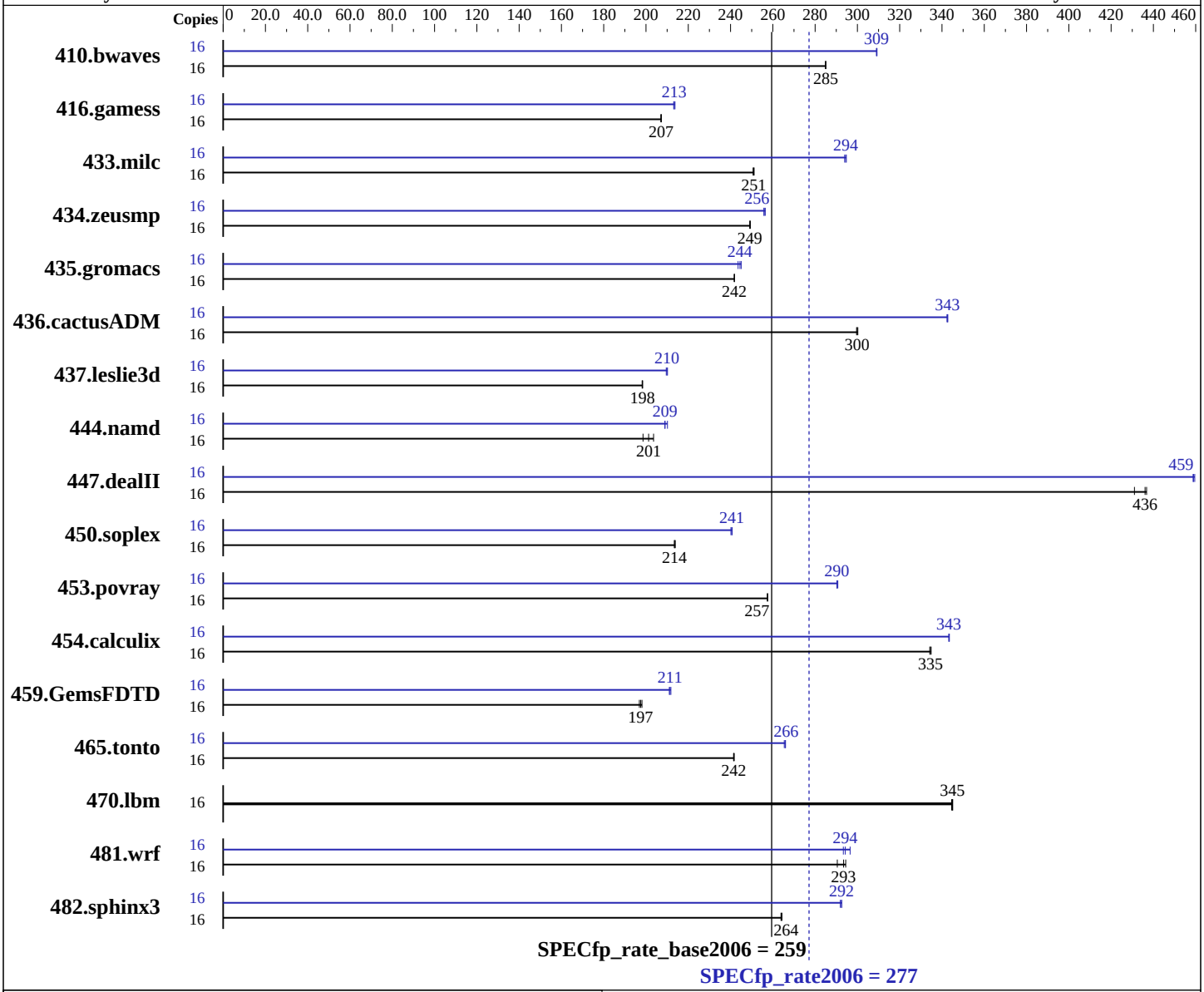
PowerEdge R715
(AMD Opteron 6220, 3.00 GHz)

SPECfp®_rate2006 = 277

SPECfp_rate_base2006 = 259

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2011
Hardware Availability: Nov-2011
Software Availability: Jul-2011



Hardware

CPU Name:	AMD Opteron 6220
CPU Characteristics:	AMD Turbo CORE technology up to 3.60 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	2 chips

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++/Fortran: Version 4.2.5.2 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit

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Primary Cache: 256 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 4 cores
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 2 x 160 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: SmartHeap 10.0 32-bit Library for Linux

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	763	285	763	285	**763**	**285**	16	703	309	**703**	**309**	704	309
416.gamess	16	**1512**	**207**	1511	207	1513	207	16	**1468**	**213**	1470	213	1466	214
433.milc	16	586	251	**585**	**251**	585	251	16	498	295	500	294	**499**	**294**
434.zeusmp	16	584	249	584	249	**584**	**249**	16	**569**	**256**	568	256	569	256
435.gromacs	16	473	242	472	242	**472**	**242**	16	**467**	**244**	469	244	466	245
436.cactusADM	16	**638**	**300**	637	300	638	300	16	558	343	**558**	**343**	558	342
437.leslie3d	16	**758**	**198**	759	198	758	198	16	716	210	**716**	**210**	718	210
444.namd	16	630	204	**638**	**201**	646	199	16	614	209	**614**	**209**	611	210
447.dealII	16	425	431	419	437	**420**	**436**	16	**399**	**459**	399	459	398	460
450.soplex	16	**625**	**214**	624	214	625	213	16	554	241	556	240	**555**	**241**
453.povray	16	331	257	**331**	**257**	331	258	16	293	291	293	290	**293**	**290**
454.calculix	16	394	335	**395**	**335**	395	334	16	**384**	**343**	385	343	384	344
459.GemsFDTD	16	862	197	**860**	**197**	857	198	16	804	211	802	212	**803**	**211**
465.tonto	16	**652**	**242**	651	242	652	241	16	**592**	**266**	592	266	593	265
470.lbm	16	637	345	638	345	**637**	**345**	16	637	345	638	345	**637**	**345**
481.wrf	16	615	290	**609**	**293**	607	294	16	603	297	**607**	**294**	609	293
482.sphinx3	16	1182	264	**1181**	**264**	1180	264	16	1068	292	**1066**	**292**	1066	293

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Binaries were compiled on a system with 2x AMD Opteron 6276 chips + 128GB Memory using RHEL 6.1
```

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Operating System Notes (Continued)

Huge pages, transparent Huge pages, and space randomization is controlled with the following settings:

```
echo 57344 > /proc/sys/vm/nr_hugepages
mount -t hugetlbfs nodev /mnt/hugepages
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled
echo 0 > /proc/sys/kernel/randomize_va_space=0
```

Platform Notes

'Power Management' set to 'Maximum Performance' in BIOS

General Notes

Environment variables set by runspec before the start of the run:

```
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1104-rate-libs-revA/32:/root/cpu2006-1.1/amd1104-rate-libs-revA/64"
```

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at http://developer.amd.com/cpu/open64

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso

C++ benchmarks:
-march=bdver1 -Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET

Fortran benchmarks:
-march=bdver1 -Ofast -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso

Benchmarks using both Fortran and C:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=bdver1 -Ofast -CG:movnti=1 -CG:locs_best=on -HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200 -OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso

470.lbm: basepeak = yes

482.sphinx3: -march=bdver1 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2 -CG:cmp_peep=on -CG:local_sched_alg=2 -INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch_ahead=4 -mso

C++ benchmarks:

444.namd: -march=bdver1 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off -CG:local_sched_alg=2 -CG:load_exe=0 -OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=bdver1 -Ofast -D__OPEN64_FAST_SET -static -INLINE:aggressive=on -LNO:opt=0 -LNO:simd=0 -fno-emit-exceptions -m32 -OPT:unroll_times_max=8 -OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off -TENV:frame_pointer=off

450.soplex: -march=bdver1 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on -OPT:RO=1 -OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off

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Peak Optimization Flags (Continued)

450.soplex (continued):
-OPT:fold_unsigned_relops=on -fno-exceptions -m32
-HP:bdt=2m:heap=2m -WOPT:sib=on

453.povray: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:pre_local_sched=off
-INLINE:aggressive=on -HP:bd=2m:heap=2m -OPT:transform=2
-OPT:alias=disjoint -WOPT:aggcm=0

Fortran benchmarks:

410.bwaves: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:ignore_feedback=off -LNO:fu=4
-LNO:loop_model_simd=on -LNO:simd_rm_unity_remainder=on
-WOPT:aggstr=0 -HP:bdt=2m:heap=2m -CG:cmp_peep=on

416.gamess: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:simd=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m -WOPT:sib=on

434.zeusmp: -march=bdver1 -Ofast -LNO:blocking=off -LNO:interchange=off
-HP:bdt=2m:heap=2m

437.leslie3d: -march=bdver1 -Ofast -CG:pre_minreg_level=2 -LNO:simd=0
-LNO:fusion=2 -HP:bdt=2m:heap=2m -mso

459.GemsFDTD: -march=bdver1 -Ofast -OPT:unroll_size=0 -LNO:fission=2
-CG:load_exe=0 -CG:local_sched_alg=2 -HP

465.tonto: -march=bdver1 -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525
-HP:bdt=2m:heap=2m

Benchmarks using both Fortran and C:

435.gromacs: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:blocking=off
-LNO:prefetch=2 -HP -CG:locs_shallow_depth=1 -CG:load_exe=0
-WOPT:sib=on

454.calculix: -march=bdver1 -Ofast -OPT:unroll_size=256
-GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

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Peak Optimization Flags (Continued)

481.wrf: `-march=bdver1 -Ofast -LNO:blocking=off -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -CG:load_exe=1 -HP -WOPT:sib=on`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.20111122.html`
`http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.20111122.xml`
`http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

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