IBM Corporation

IBM System x3250 M4 (Intel Core i3-2130)

SPECint\_rate2006 = 92.0
SPECint\_rate_base2006 = 87.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Nov-2011
Hardware Availability: Oct-2011

Tested by: IBM Corporation
Software Availability: Sep-2011

### Hardware

- **CPU Name:** Intel Core i3-2130
- **CPU Characteristics:**
  - CPU MHz: 3400
  - FPU: Integrated
  - CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
  - CPU(s) orderable: 1 chip
  - Primary Cache: 32 KB I + 32 KB D on chip per core
  - Secondary Cache: 256 KB I+D on chip per core
  - L3 Cache: 3 MB I+D on chip per chip
  - Other Cache: None
  - Memory: 16 GB (2 x 8 GB 2Rx8 PC3-10600E-9, ECC)
  - Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
  - Other Hardware: None

### Software

- **Operating System:** Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
- **Compiler:** C/C++: Version 12.1.0.225 of Intel Compiler XE Build 20110803
- **Auto Parallel:** No
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Microquill SmartHeap V9.01
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Results Table

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<td>97.8</td>
<td>282</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind copies to the cores

Platform Notes

BIOS Settings:
C-State enabled in BIOS

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/root/SPECcpu12.1/smartheap:/root/SPECcpu12.1/ici12.1-libs/ia32:/root/SPECcpu12.1/ici12.1-libs/intel64"*

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5 with binutils-2.17.50.0.6-14.el5
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
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Base Compiler Invocation

C benchmarks:
  icc -m32

C++ benchmarks:
  icpc -m32

Base Portability Flags

  400.perlbench: -DSPEC_CPU_LINUX_IA32
  462.libquantum: -DSPEC_CPU_LINUX
  483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
  -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
  -xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
  -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
  icc -m32

  400.perlbench: icc -m64
  401.bzip2: icc -m64
  456.hmmer: icc -m64
  458.sjeng: icc -m64

C++ benchmarks:
  icpc -m32
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Peak Portability Flags

-DSPEC_CPU_LP64
-DSPEC_CPU_LINUX_X64

Peak Optimization Flags

C benchmarks:

-DSPEC_CPU_LP64
-DSPEC_CPU_LINUX_X64

C++ benchmarks:
**IBM Corporation**

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**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

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The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html](http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml](http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml)

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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