SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

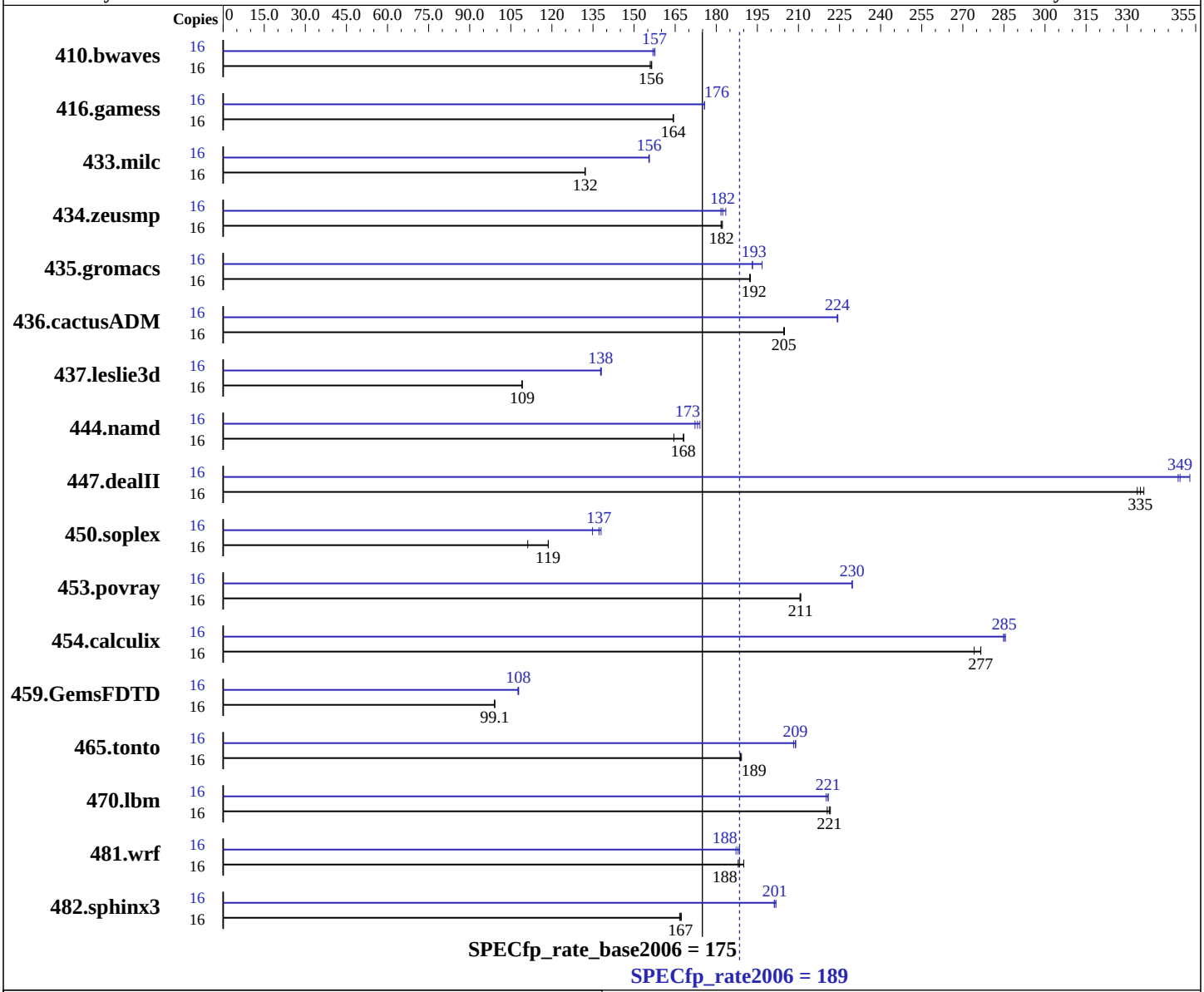
PowerEdge R415
(AMD Opteron 4274 HE, 2.50 GHz)

SPECfp®_rate2006 = 189

SPECfp_rate_base2006 = 175

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2011
Hardware Availability: Dec-2011
Software Availability: Jul-2011



Hardware

CPU Name:	AMD Opteron 4274 HE
CPU Characteristics:	AMD Turbo CORE technology up to 3.50 GHz
CPU MHz:	2500
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++/Fortran: Version 4.2.5.2 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R415
(AMD Opteron 4274 HE, 2.50 GHz)

SPECfp_rate2006 = 189

SPECfp_rate_base2006 = 175

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2011
Hardware Availability: Dec-2011
Software Availability: Jul-2011

Primary Cache: 256 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (4 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 2 x 73 GB SAS, 15000 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	**1391**	**156**	1390	156	1395	156	16	1386	157	1379	158	**1381**	**157**
416.gamess	16	**1907**	**164**	1908	164	1906	164	16	1782	176	**1783**	**176**	1783	176
433.milc	16	1111	132	**1112**	**132**	1112	132	16	944	156	945	155	**945**	**156**
434.zeusmp	16	799	182	801	182	**800**	**182**	16	793	183	**798**	**182**	801	182
435.gromacs	16	**594**	**192**	594	192	594	192	16	581	197	592	193	**591**	**193**
436.cactusADM	16	934	205	933	205	**934**	**205**	16	**853**	**224**	852	224	853	224
437.leslie3d	16	**1378**	**109**	1379	109	1378	109	16	1089	138	1092	138	**1091**	**138**
444.namd	16	763	168	780	165	**764**	**168**	16	**741**	**173**	737	174	745	172
447.dealII	16	**547**	**335**	549	334	545	336	16	**524**	**349**	519	353	525	349
450.soplex	16	1200	111	1124	119	**1125**	**119**	16	990	135	**973**	**137**	967	138
453.povray	16	404	211	**404**	**211**	404	211	16	**371**	**230**	371	230	371	230
454.calculix	16	**477**	**277**	477	277	481	274	16	462	286	463	285	**463**	**285**
459.GemsFDTD	16	**1713**	**99.1**	1713	99.1	1713	99.1	16	**1576**	**108**	1577	108	1574	108
465.tonto	16	832	189	834	189	**834**	**189**	16	**754**	**209**	756	208	753	209
470.lbm	16	992	222	**994**	**221**	997	220	16	**996**	**221**	995	221	999	220
481.wrf	16	951	188	**948**	**188**	940	190	16	955	187	948	189	**951**	**188**
482.sphinx3	16	1871	167	1865	167	**1867**	**167**	16	1545	202	1550	201	**1549**	**201**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit
Large pages were not enabled for this run
Binaries were compiled on a system with 2x AMD Opteron 6276 chips + 128GB Memory using RHEL 6.1
```

Continued on next page

Operating System Notes (Continued)

```
Set kernel/randomize_va_space=0 in /etc/sysctl.conf
```

Platform Notes

```
'Power Management' set to 'Maximum Performance' in BIOS
```

General Notes

```
environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1104-rate-libs-revA/32:/root/cpu2006-1.1/amd1104-rate-libs-revA/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
```

Base Compiler Invocation

C benchmarks:
`opencc`

C++ benchmarks:
`openCC`

Fortran benchmarks:
`openf95`

Benchmarks using both Fortran and C:
`opencc openf95`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`

Continued on next page

Base Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso

C++ benchmarks:
-march=bdver1 -Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET

Fortran benchmarks:
-march=bdver1 -Ofast -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso

Benchmarks using both Fortran and C:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=bdver1 -Ofast -CG:movnti=1 -CG:locs_best=on
-HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200
-OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso

470.lbm: -march=bdver1 -Ofast -CG:cmp_peep=on
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -HP:bdt=2m:heap=2m
-IPA:plimit=8000 -IPA:small_pu=100 -mso

482.sphinx3: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:cmp_peep=on -CG:local_sched_alg=2 -INLINE:aggressive=on
-LNO:prefetch=2 -LNO:prefetch_ahead=4 -mso

C++ benchmarks:

444.namd: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -OPT:unroll_size=256
-fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=bdver1 -Ofast -D__OPEN64_FAST_SET -static
-INLINE:aggressive=on -LNO:opt=0 -LNO:simd=0
-fno-emit-exceptions -m32 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m
-GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off
-TENV:frame_pointer=off

450.soplex: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on -OPT:RO=1
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off

Continued on next page

Peak Optimization Flags (Continued)

450.soplex (continued):
-OPT:fold_unsigned_relops=on -fno-exceptions -m32
-HP:bdt=2m:heap=2m -WOPT:sib=on

453.povray: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:pre_local_sched=off
-INLINE:aggressive=on -HP:bd=2m:heap=2m -OPT:transform=2
-OPT:alias=disjoint -WOPT:aggcm=0

Fortran benchmarks:

410.bwaves: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:ignore_feedback=off -LNO:fu=4
-LNO:loop_model_simd=on -LNO:simd_rm_unity_remainder=on
-WOPT:aggstr=0 -HP:bdt=2m:heap=2m -CG:cmp_peep=on

416.gamess: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:simd=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m -WOPT:sib=on

434.zeusmp: -march=bdver1 -Ofast -LNO:blocking=off -LNO:interchange=off
-HP:bdt=2m:heap=2m

437.leslie3d: -march=bdver1 -Ofast -CG:pre_minreg_level=2 -LNO:simd=0
-LNO:fusion=2 -HP:bdt=2m:heap=2m -mso

459.GemsFDTD: -march=bdver1 -Ofast -OPT:unroll_size=0 -LNO:fission=2
-CG:load_exe=0 -CG:local_sched_alg=2 -HP

465.tonto: -march=bdver1 -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525
-HP:bdt=2m:heap=2m

Benchmarks using both Fortran and C:

435.gromacs: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:blocking=off
-LNO:prefetch=2 -HP -CG:locs_shallow_depth=1 -CG:load_exe=0
-WOPT:sib=on

454.calculix: -march=bdver1 -Ofast -OPT:unroll_size=256
-GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `-march=bdver1 -Ofast -LNO:blocking=off -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -CG:load_exe=1 -HP -WOPT:sib=on`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.20111122.html`
`http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.20111122.xml`
`http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.xml`

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Thu Jul 24 00:38:20 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 December 2011.