SPEC® CFP2006 Result

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NEC Corporation

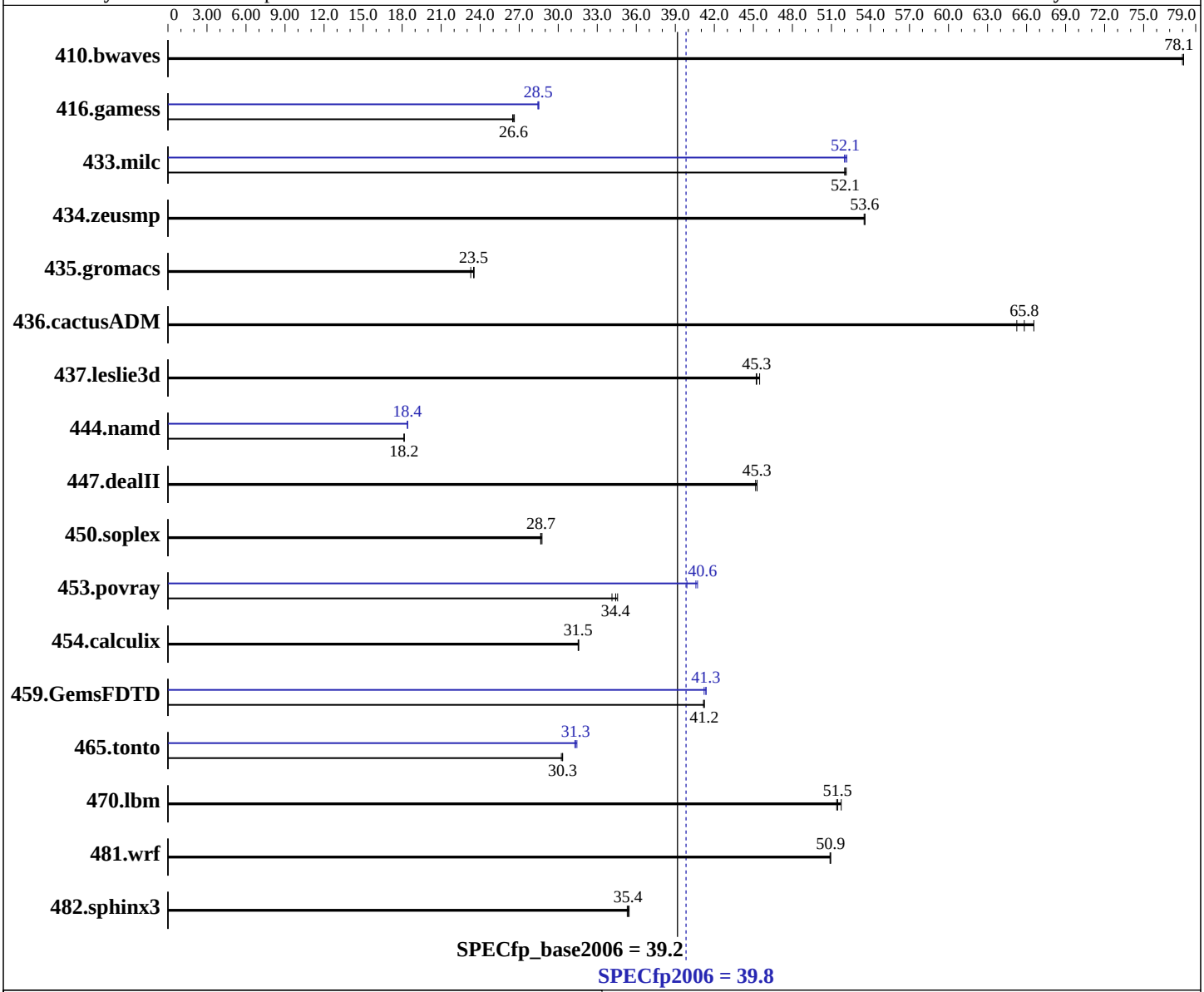
Express5800/GT110d (Intel Pentium G630)

SPECfp®2006 = 39.8

SPECfp_base2006 = 39.2

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Mar-2012
Hardware Availability: Jan-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Pentium G630
CPU Characteristics:	
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 (Santiago) 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1066 MHz and CL7)
Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**174**	**78.1**	174	78.0	174	78.1	**174**	**78.1**	174	78.0	174	78.1
416.gamess	735	26.6	**737**	**26.6**	739	26.5	689	28.4	687	28.5	**688**	**28.5**
433.milc	176	52.0	176	52.1	**176**	**52.1**	177	52.0	**176**	**52.1**	176	52.2
434.zeusmp	**170**	**53.6**	170	53.5	170	53.6	**170**	**53.6**	170	53.5	170	53.6
435.gromacs	303	23.5	307	23.3	**304**	**23.5**	303	23.5	307	23.3	**304**	**23.5**
436.cactusADM	183	65.3	180	66.6	**182**	**65.8**	183	65.3	180	66.6	**182**	**65.8**
437.leslie3d	208	45.2	**208**	**45.3**	207	45.5	208	45.2	**208**	**45.3**	207	45.5
444.namd	442	18.2	442	18.2	**442**	**18.2**	436	18.4	435	18.4	**436**	**18.4**
447.dealII	253	45.3	253	45.2	**253**	**45.3**	253	45.3	253	45.2	**253**	**45.3**
450.soplex	291	28.6	290	28.8	**291**	**28.7**	291	28.6	290	28.8	**291**	**28.7**
453.povray	**155**	**34.4**	156	34.1	154	34.6	133	39.9	**131**	**40.6**	131	40.7
454.calculix	261	31.6	262	31.5	**262**	**31.5**	261	31.6	262	31.5	**262**	**31.5**
459.GemsFDTD	**257**	**41.2**	257	41.2	258	41.2	256	41.4	**257**	**41.3**	257	41.2
465.tonto	**324**	**30.3**	324	30.3	325	30.2	313	31.4	**314**	**31.3**	315	31.3
470.lbm	267	51.4	265	51.8	**267**	**51.5**	267	51.4	265	51.8	**267**	**51.5**
481.wrf	**219**	**50.9**	219	50.9	219	50.9	**219**	**50.9**	219	50.9	219	50.9
482.sphinx3	552	35.3	550	35.4	**551**	**35.4**	552	35.3	550	35.4	**551**	**35.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
Default BIOS settings were used.
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "2"
```

Continued on next page

General Notes (Continued)

```
The Express5800/GT110d-S and
the Express5800/GT110d models are electronically equivalent.
The results have been measured on the Express5800/GT110d-S model.

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Added glibc-static-2.12-1.25.el6.x86_64.rpm
to enable static linking
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
       470.lbm: -DSPEC_CPU_LP64
       481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -ansi-alias`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -inline-level=0 -scalar-rep- -static`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2 -inline-level=0 -opt-prefetch -parallel`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-inline-calloc -opt-malloc-options=3 -auto -unroll4`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at

`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html`
`http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-R110d-RevA.html`

You can also download the XML flags sources by saving the following links:

`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`
`http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-R110d-RevA.xml`

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