# SPEC® CINT2006 Result

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## Fujitsu

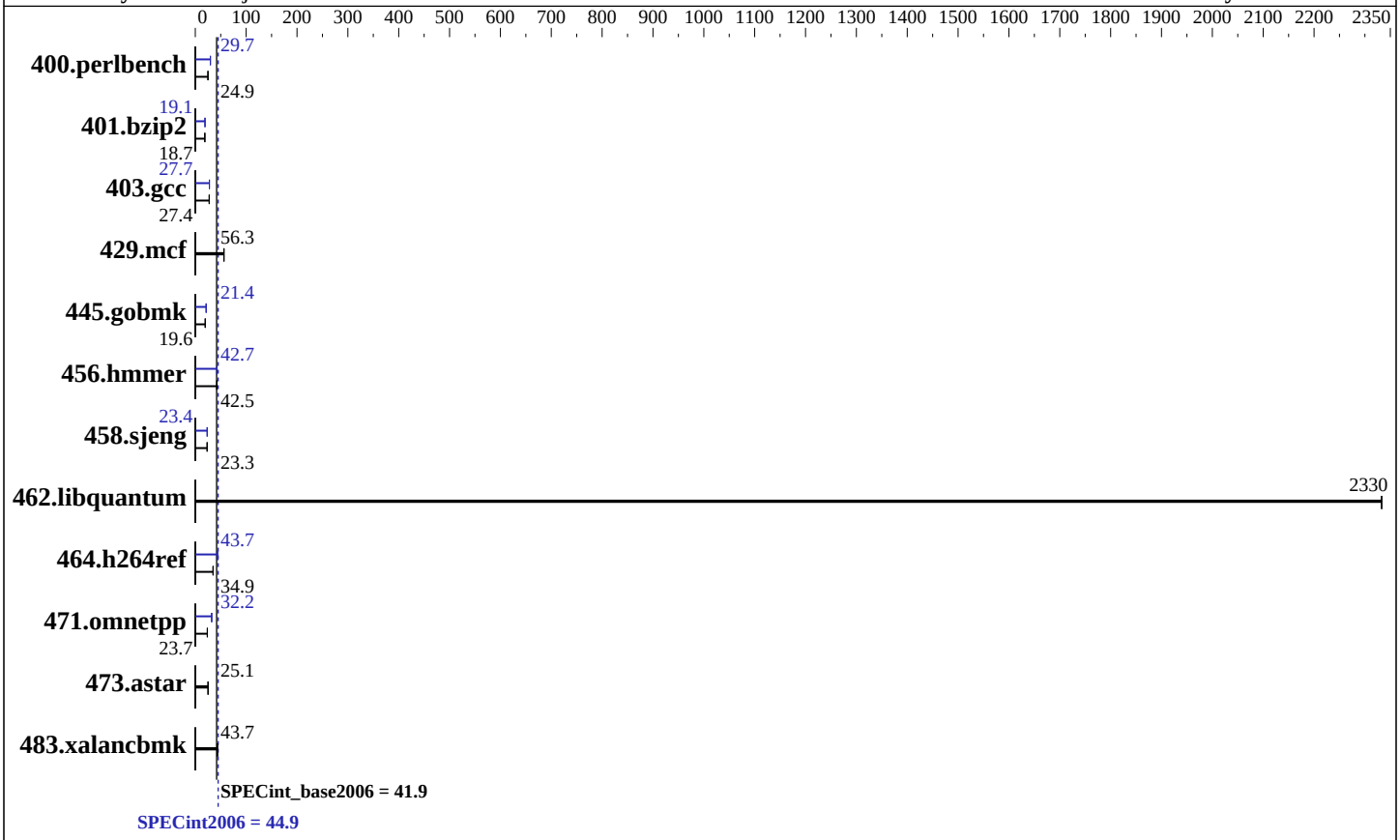
PRIMERGY RX200 S7, Intel Xeon E5-2650, 2.0 GHz

SPECint®2006 = 44.9

SPECint_base2006 = 41.9

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Dec-2011
**Hardware Availability:** Mar-2012
**Software Availability:** Dec-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2650 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 16 cores, 2 chips, 8 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 20 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC) |
| Disk Subsystem: | 1 x SATA, 500 GB, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago)<br>2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux |
| Auto Parallel: | Yes |
| File System: | ext4 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 392 | 24.9 | 392 | 24.9 | **392** | **24.9** | 329 | 29.7 | **329** | **29.7** | 330 | 29.6 |
| 401.bzip2 | 514 | 18.8 | 515 | 18.7 | **515** | **18.7** | 506 | 19.1 | **506** | **19.1** | 504 | 19.1 |
| 403.gcc | **294** | **27.4** | 294 | 27.4 | 294 | 27.4 | 290 | 27.7 | 291 | 27.7 | **290** | **27.7** |
| 429.mcf | 161 | 56.6 | 162 | 56.2 | **162** | **56.3** | 161 | 56.6 | 162 | 56.2 | **162** | **56.3** |
| 445.gobmk | **536** | **19.6** | 536 | 19.6 | 536 | 19.6 | 490 | 21.4 | **491** | **21.4** | 491 | 21.4 |
| 456.hmmer | 220 | 42.5 | 219 | 42.5 | **219** | **42.5** | 219 | 42.7 | 219 | 42.7 | **219** | **42.7** |
| 458.sjeng | 520 | 23.3 | **519** | **23.3** | 519 | 23.3 | 518 | 23.4 | **517** | **23.4** | 517 | 23.4 |
| 462.libquantum | 8.88 | 2330 | **8.88** | **2330** | 8.88 | 2330 | 8.88 | 2330 | **8.88** | **2330** | 8.88 | 2330 |
| 464.h264ref | **634** | **34.9** | 632 | 35.0 | 635 | 34.9 | 503 | 44.0 | 512 | 43.2 | **506** | **43.7** |
| 471.omnetpp | 264 | 23.7 | 265 | 23.6 | **264** | **23.7** | **194** | **32.2** | 193 | 32.4 | 195 | 32.0 |
| 473.astar | **280** | **25.1** | 279 | 25.1 | 280 | 25.1 | **280** | **25.1** | 279 | 25.1 | 280 | 25.1 |
| 483.xalancbmk | 159 | 43.5 | 158 | 43.7 | **158** | **43.7** | 159 | 43.5 | 158 | 43.7 | **158** | **43.7** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Platform Notes

```
BIOS configuration:
Intel HT Technology = Disable
Frequency Floor Override = Enable
```

## General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml


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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 07:18:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 April 2012.