SPEC® CFP2006 Result

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Fujitsu

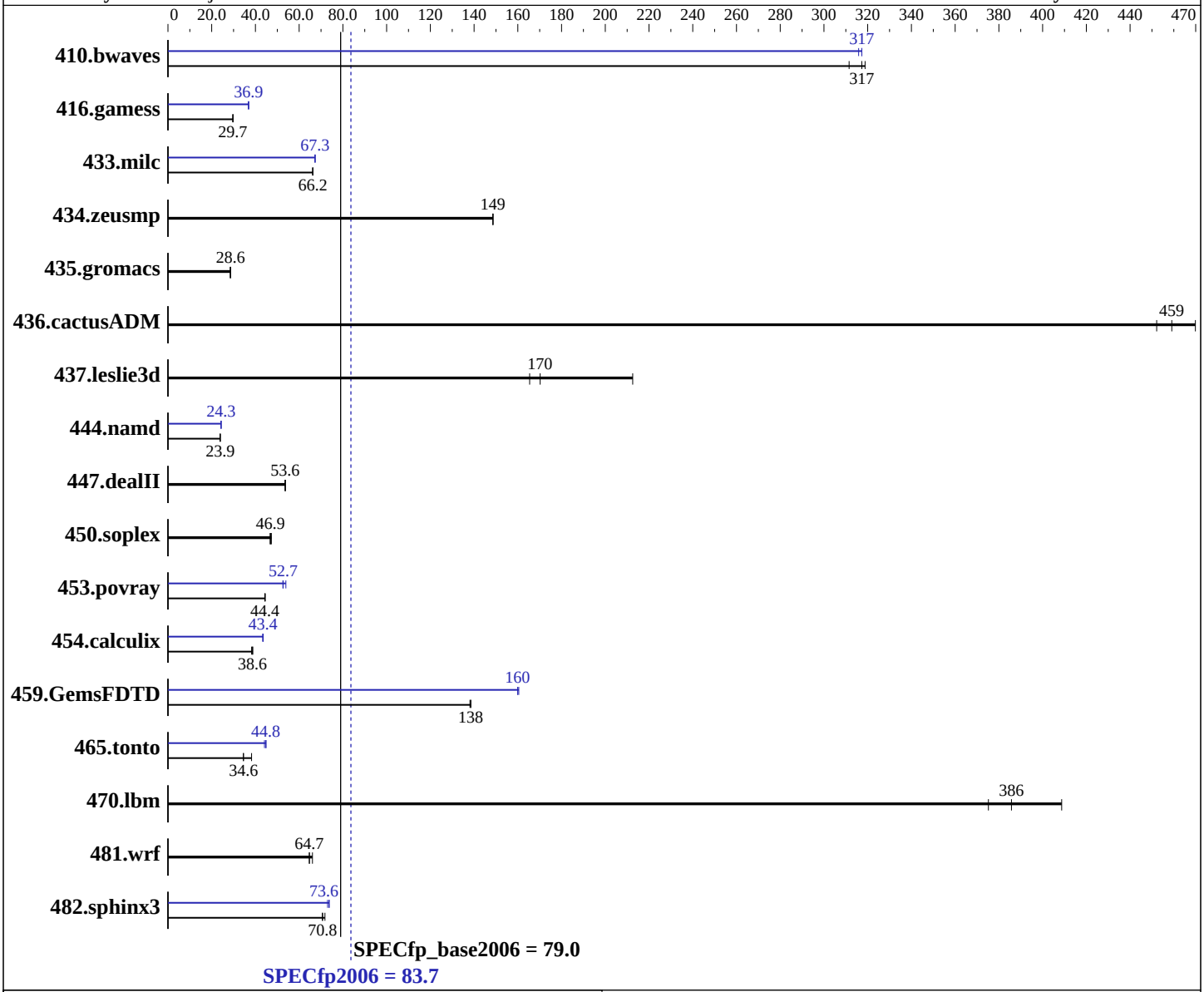
PRIMERGY TX300 S7, Intel Xeon E5-2680, 2.70 GHz

SPECfp®2006 = 83.7

SPECfp_base2006 = 79.0

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Feb-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E5-2680
CPU Characteristics:	Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz:	2700
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

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L3 Cache:	20 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	43.6	312	42.6	319	**42.8**	**317**	43.0	316	42.8	317	**42.8**	**317**
416.gamess	657	29.8	660	29.7	**660**	**29.7**	531	36.9	**531**	**36.9**	533	36.8
433.milc	**139**	**66.2**	138	66.3	139	66.2	136	67.3	137	67.2	**136**	**67.3**
434.zeusmp	61.2	149	**61.2**	**149**	61.2	149	61.2	149	**61.2**	**149**	61.2	149
435.gromacs	**250**	**28.6**	250	28.6	251	28.5	**250**	**28.6**	250	28.6	251	28.5
436.cactusADM	**26.0**	**459**	26.4	452	25.4	470	**26.0**	**459**	26.4	452	25.4	470
437.leslie3d	56.8	165	44.2	213	**55.2**	**170**	56.8	165	44.2	213	**55.2**	**170**
444.namd	**336**	**23.9**	336	23.9	336	23.9	**330**	**24.3**	330	24.3	330	24.3
447.dealII	214	53.6	213	53.7	**213**	**53.6**	214	53.6	213	53.7	**213**	**53.6**
450.soplex	176	47.3	**178**	**46.9**	179	46.7	176	47.3	**178**	**46.9**	179	46.7
453.povray	120	44.5	120	44.3	**120**	**44.4**	101	52.6	**101**	**52.7**	98.7	53.9
454.calculix	216	38.3	**213**	**38.6**	213	38.8	190	43.3	**190**	**43.4**	189	43.6
459.GemsFDTD	**76.6**	**138**	76.6	138	76.8	138	66.1	160	66.3	160	**66.3**	**160**
465.tonto	**285**	**34.6**	257	38.2	285	34.5	**220**	**44.8**	223	44.1	219	44.9
470.lbm	36.6	375	**35.6**	**386**	33.6	409	36.6	375	**35.6**	**386**	33.6	409
481.wrf	**173**	**64.7**	169	66.1	173	64.6	**173**	**64.7**	169	66.1	173	64.6
482.sphinx3	271	71.8	**275**	**70.8**	276	70.6	**265**	**73.6**	267	73.0	264	73.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
Intel HT Technology = Disable
Frequency Floor Override = Enable
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"
```

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General Notes (Continued)

```
OMP_NUM_THREADS = "16"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 This result was measured on the PRIMERGY RX350 S7. The PRIMERGY RX350 S7
 and the PRIMERGY TX300 S7 are electronically equivalent.
 For information about Fujitsu please visit: http://www.fujitsu.com
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:

`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml`

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For other inquiries, please contact webmaster@spec.org.

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