SPEC® CFP2006 Result

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Itautec

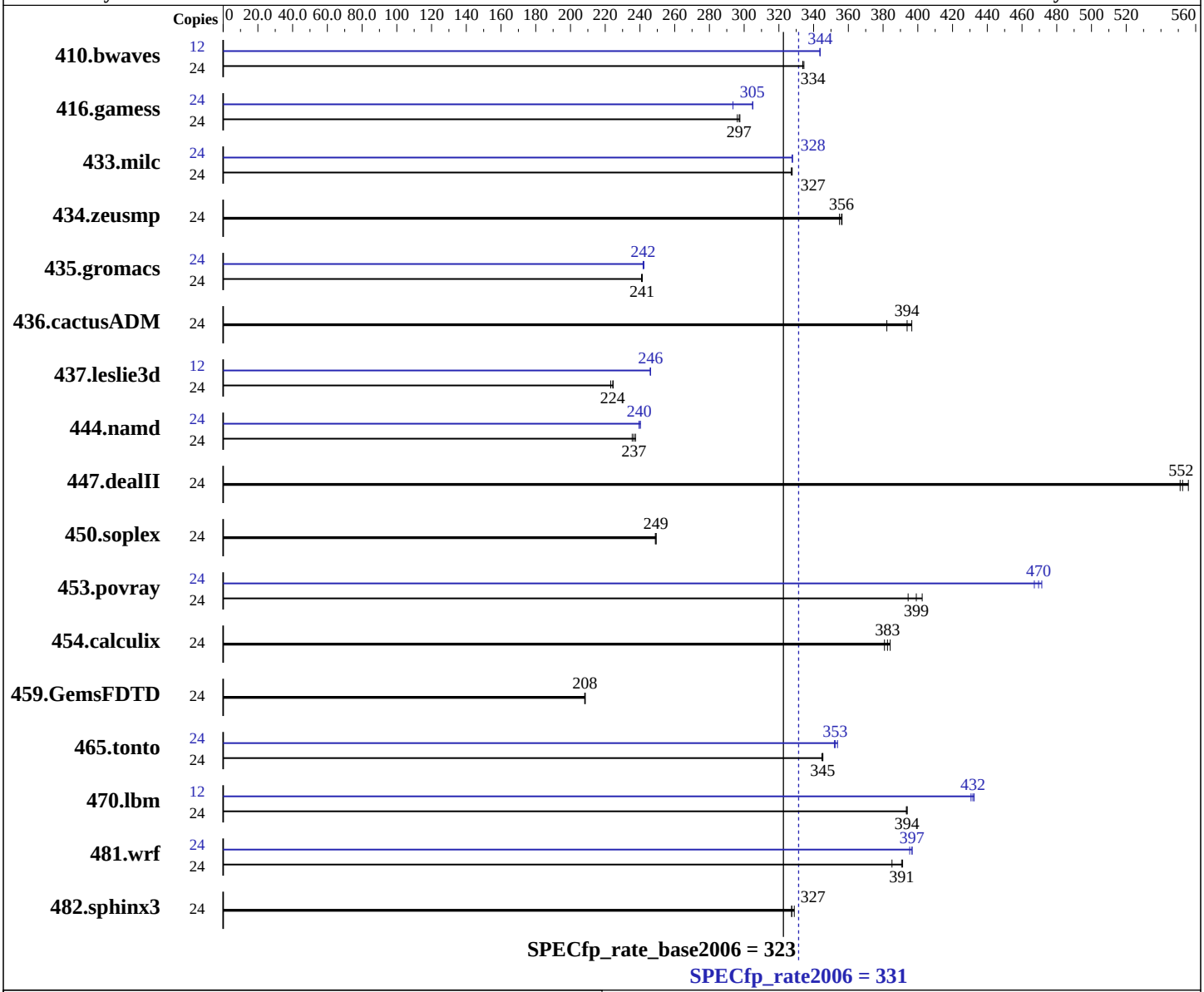
Servidor Itautec MX205 (Intel Xeon E5-2630L)

SPECfp®_rate2006 = 331

SPECfp_rate_base2006 = 323

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E5-2630L
CPU Characteristics:	Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server Release 6.2, 2.6.32-220.el6.x86_64
Compiler:	C/C++/Fortran: Version 12.1.0 of Intel Compiler XE Build 20111011
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

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L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 64 GB (16 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 500 GB, SATA-2, 7200 RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	975	334	977	334	**977**	**334**	12	**474**	**344**	475	344	474	344
416.gamess	24	**1581**	**297**	1579	298	1587	296	24	**1542**	**305**	1540	305	1601	294
433.milc	24	673	327	672	328	**673**	**327**	24	**672**	**328**	672	328	672	328
434.zeusmp	24	615	355	613	356	**613**	**356**	24	615	355	613	356	**613**	**356**
435.gromacs	24	710	241	711	241	**710**	**241**	24	707	242	709	242	**708**	**242**
436.cactusADM	24	723	397	750	382	**728**	**394**	24	723	397	750	382	**728**	**394**
437.leslie3d	24	1011	223	1005	225	**1006**	**224**	12	459	246	458	246	**458**	**246**
444.namd	24	817	236	810	237	**814**	**237**	24	**803**	**240**	801	240	804	239
447.dealII	24	**497**	**552**	494	556	498	551	24	**497**	**552**	494	556	498	551
450.soplex	24	**803**	**249**	804	249	803	249	24	**803**	**249**	804	249	803	249
453.povray	24	317	403	**320**	**399**	324	394	24	**272**	**470**	271	471	273	467
454.calculix	24	520	381	**518**	**383**	515	384	24	520	381	**518**	**383**	515	384
459.GemsFDTD	24	1223	208	**1222**	**208**	1221	209	24	1223	208	**1222**	**208**	1221	209
465.tonto	24	685	345	684	345	**684**	**345**	24	668	354	**670**	**353**	671	352
470.lbm	24	**837**	**394**	838	393	837	394	12	**382**	**432**	383	431	381	432
481.wrf	24	696	385	685	391	**686**	**391**	24	676	397	**676**	**397**	678	395
482.sphinx3	24	**1428**	**327**	1422	329	1429	327	24	**1428**	**327**	1422	329	1429	327

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

General Notes

```
This result was measured on the Servidor Itautec MX205.
The Servidor Itautec LX205, Servidor Itautec MX205 and
the Servidor Itautec MX225+ are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32 -ansi-alias -opt-mem-layout-trans=3`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -opt-mem-layout-trans=3`

470.lbm: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3 -ansi-alias -opt-prefetch -auto-ilp32 -opt-mem-layout-trans=3`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Itautec-Servidor_Itautec-Intel-Linux-Platform.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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