



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp®2006 = 76.1

PowerEdge R720 (Intel Xeon E5-2660, 2.20 GHz)

SPECfp_base2006 = 72.3

CPU2006 license: 55

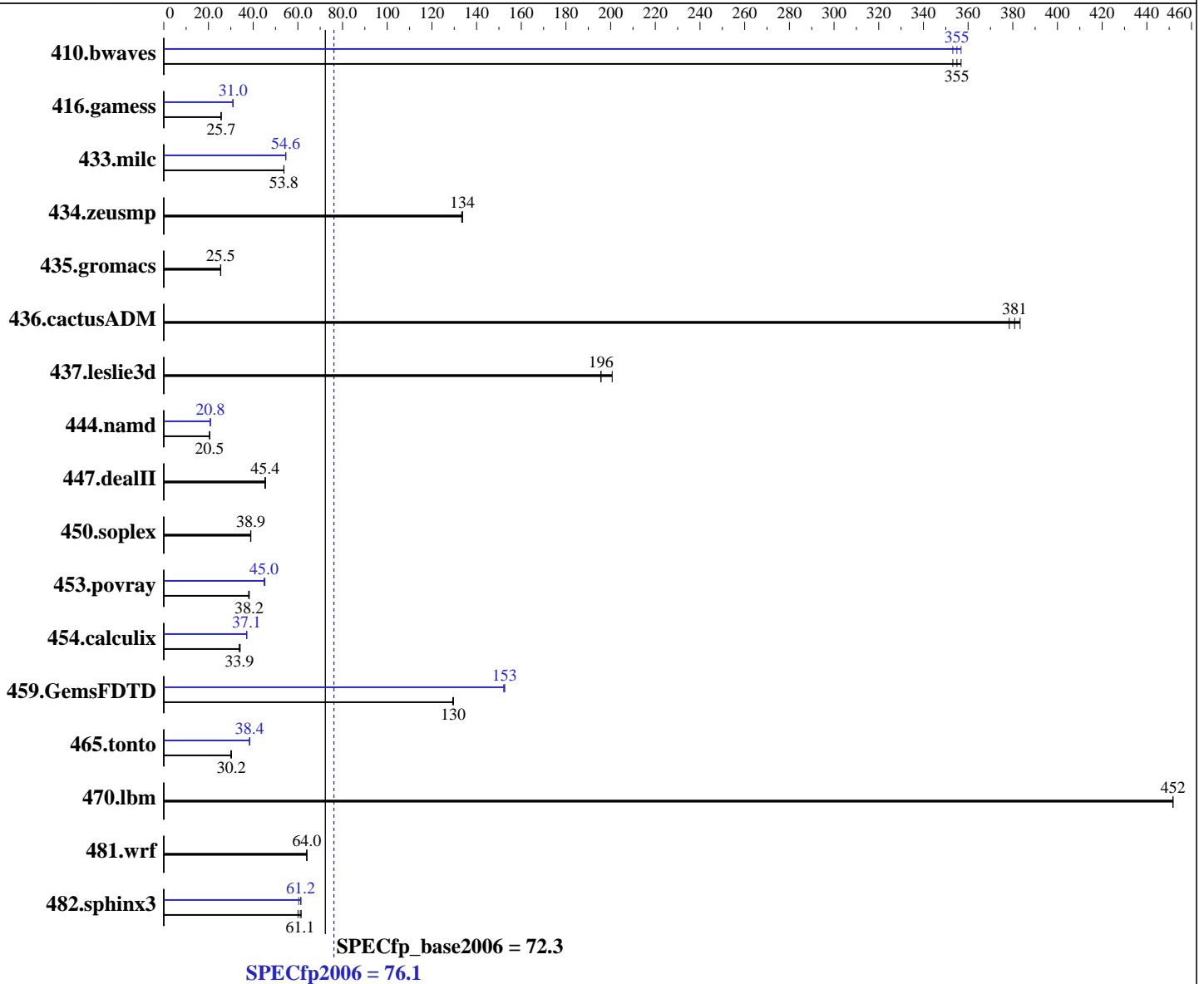
Test date: Mar-2012

Test sponsor: Dell Inc.

Hardware Availability: Mar-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



Hardware

CPU Name: Intel Xeon E5-2660
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
CPU MHz: 2200
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	38.5	353	38.1	357	<u>38.3</u>	<u>355</u>	38.1	357	38.5	353	<u>38.3</u>	<u>355</u>
416.gamess	762	25.7	<u>763</u>	<u>25.7</u>	764	25.6	632	31.0	633	30.9	<u>633</u>	<u>31.0</u>
433.milc	<u>171</u>	<u>53.8</u>	171	53.7	171	53.8	168	54.6	<u>168</u>	<u>54.6</u>	168	54.6
434.zeusmp	68.1	134	68.3	133	<u>68.1</u>	<u>134</u>	68.1	134	68.3	133	<u>68.1</u>	<u>134</u>
435.gromacs	<u>280</u>	<u>25.5</u>	280	25.5	281	25.4	<u>280</u>	<u>25.5</u>	280	25.5	281	25.4
436.cactusADM	31.2	383	31.6	378	<u>31.4</u>	<u>381</u>	31.2	383	31.6	378	<u>31.4</u>	<u>381</u>
437.leslie3d	46.8	201	<u>48.0</u>	<u>196</u>	48.0	196	46.8	201	<u>48.0</u>	<u>196</u>	48.0	196
444.namd	<u>391</u>	<u>20.5</u>	391	20.5	391	20.5	385	20.8	385	20.8	<u>385</u>	<u>20.8</u>
447.dealII	251	45.5	253	45.2	<u>252</u>	<u>45.4</u>	251	45.5	253	45.2	<u>252</u>	<u>45.4</u>
450.soplex	215	38.8	<u>214</u>	<u>38.9</u>	214	39.0	215	38.8	<u>214</u>	<u>38.9</u>	214	39.0
453.povray	<u>139</u>	<u>38.2</u>	139	38.3	140	38.0	<u>118</u>	<u>45.0</u>	119	44.9	118	45.3
454.calculix	241	34.2	244	33.8	<u>243</u>	<u>33.9</u>	<u>222</u>	<u>37.1</u>	222	37.1	222	37.1
459.GemsFDTD	81.9	130	<u>81.9</u>	<u>130</u>	82.1	129	69.7	152	<u>69.5</u>	<u>153</u>	69.5	153
465.tonto	325	30.3	328	30.0	<u>326</u>	<u>30.2</u>	256	38.5	<u>256</u>	<u>38.4</u>	257	38.3
470.lbm	30.4	452	<u>30.4</u>	<u>452</u>	30.4	452	30.4	452	<u>30.4</u>	<u>452</u>	30.4	452
481.wrf	174	64.0	174	64.0	<u>174</u>	<u>64.0</u>	174	64.0	174	64.0	<u>174</u>	<u>64.0</u>
482.sphinx3	317	61.5	324	60.1	<u>319</u>	<u>61.1</u>	323	60.4	317	61.4	<u>318</u>	<u>61.2</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

System Profile set to Custom
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on R720-Latency2 Thu Mar 29 00:29:20 2012

This section contains SUT (System Under Test) info as seen by
Continued on next page



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Platform Notes (Continued)

some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2660 0 @ 2.20GHz
  2 "physical id"s (chips)
  32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 8
    siblings  : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
  cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      132089860 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux R720-Latency2 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux

```

```
run-level 3 Mar 29 00:26 last=S
```

```

SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       ext3  143G  7.5G  135G   6% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64"
OMP_NUM_THREADS = "16"

```

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General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
 Transparent Huge Pages disabled with:
 echo never > /sys/kernel/mm/transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1> /proc/sys/vm/drop_caches
 The Dell PowerEdge R720 and the Bull NovaScale R460 F3 models are electronically equivalent.
 The results have been measured on a Dell PowerEdge R720 model

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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