SPEC® CFP2006 Result

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Supermicro

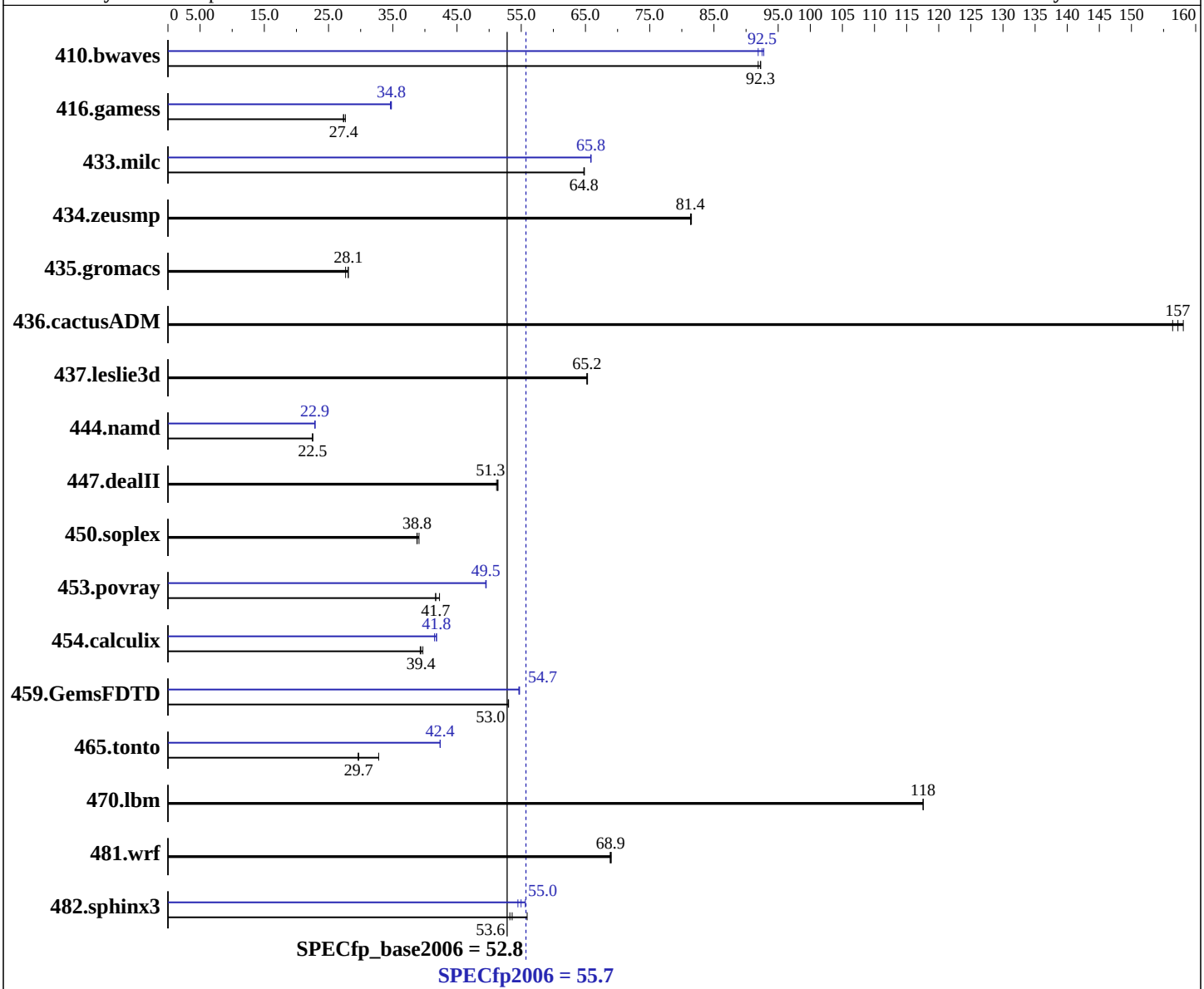
SuperServer 5037MC-H8TRF (X9SCD-F, Intel E3-1260L)

SPECfp®2006 = 55.7

SPECfp_base2006 = 52.8

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Mar-2012
Hardware Availability: Aug-2011
Software Availability: Oct-2011



Hardware

CPU Name:	Intel Xeon E3-1260L
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem: 1 x 500 GB SATA III, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**147**	**92.3**	147	92.3	148	91.9	148	91.9	146	92.8	**147**	**92.5**
416.gamess	**716**	**27.4**	716	27.3	709	27.6	563	34.8	565	34.6	**563**	**34.8**
433.milc	142	64.8	**142**	**64.8**	142	64.8	139	65.9	139	65.8	**139**	**65.8**
434.zeusmp	112	81.5	**112**	**81.4**	112	81.4	112	81.5	**112**	**81.4**	112	81.4
435.gromacs	258	27.7	**254**	**28.1**	254	28.1	258	27.7	**254**	**28.1**	254	28.1
436.cactusADM	**76.0**	**157**	75.6	158	76.4	156	**76.0**	**157**	75.6	158	76.4	156
437.leslie3d	**144**	**65.2**	144	65.3	144	65.2	**144**	**65.2**	144	65.3	144	65.2
444.namd	**356**	**22.5**	356	22.5	356	22.5	350	22.9	351	22.9	**350**	**22.9**
447.dealII	223	51.4	**223**	**51.3**	224	51.2	223	51.4	**223**	**51.3**	224	51.2
450.soplex	213	39.1	215	38.8	**215**	**38.8**	213	39.1	215	38.8	**215**	**38.8**
453.povray	**128**	**41.7**	128	41.7	126	42.3	107	49.6	108	49.5	**107**	**49.5**
454.calculix	208	39.7	**209**	**39.4**	210	39.3	**197**	**41.8**	199	41.5	197	41.8
459.GemsFDTD	200	53.0	**200**	**53.0**	200	53.0	194	54.7	**194**	**54.7**	194	54.6
465.tonto	**331**	**29.7**	300	32.8	332	29.6	**232**	**42.4**	232	42.4	232	42.4
470.lbm	**117**	**118**	117	118	117	118	**117**	**118**	117	118	117	118
481.wrf	162	68.8	**162**	**68.9**	162	69.0	162	68.8	**162**	**68.9**	162	69.0
482.sphinx3	**364**	**53.6**	349	55.9	366	53.2	**355**	**55.0**	358	54.5	350	55.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "4"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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For other inquiries, please contact webmaster@spec.org.

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