SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

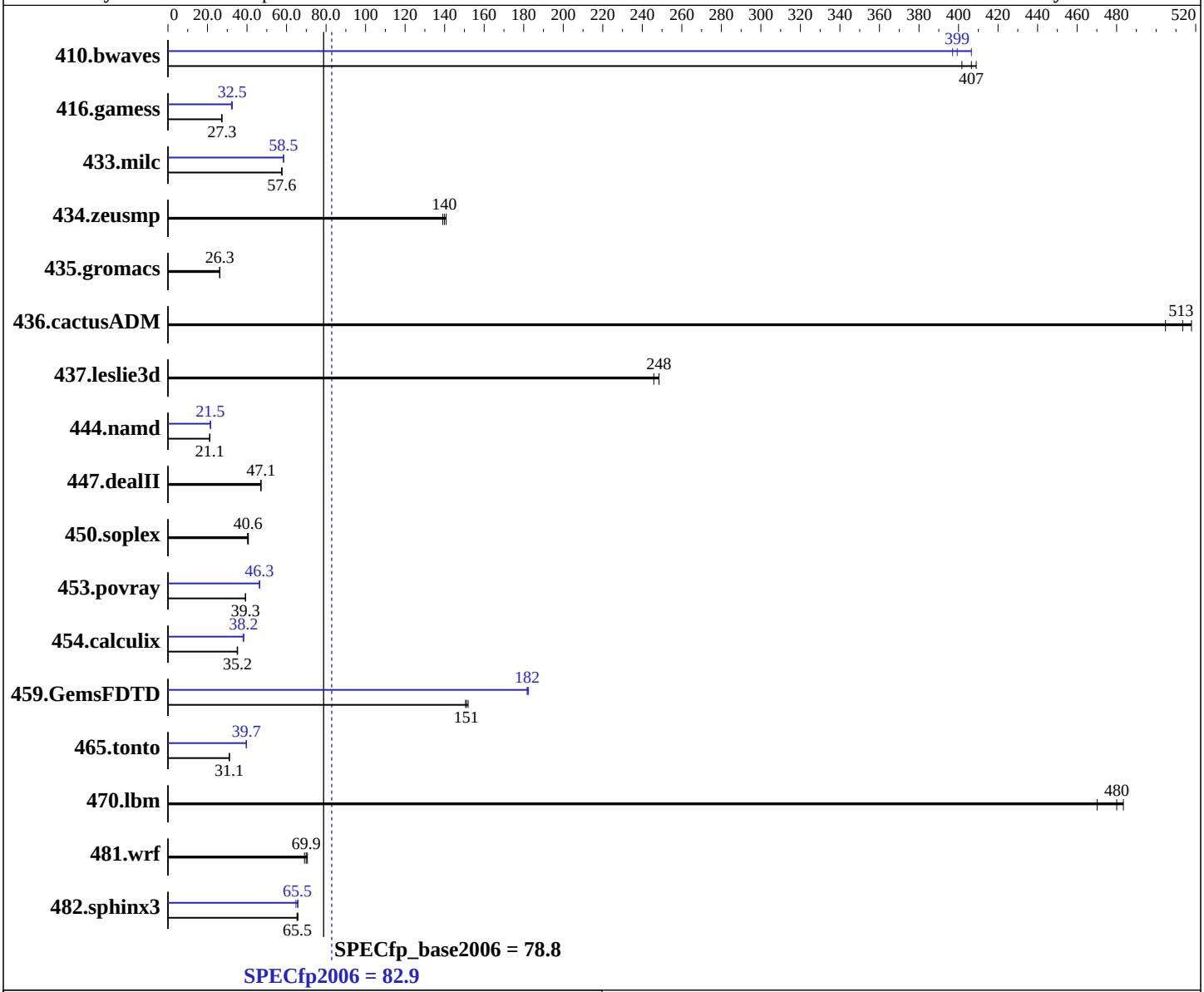
IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2665)

SPECfp®2006 = 82.9

SPECfp_base2006 = 78.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011



Hardware

CPU Name:	Intel Xeon E5-2665
CPU Characteristics:	Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x iDataPlex dx360 M4 (Intel Xeon E5-2665)

SPECfp2006 = 82.9

SPECfp_base2006 = 78.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	33.8	402	33.2	409	**33.4**	**407**	34.2	397	33.4	407	**34.0**	**399**
416.gamess	721	27.1	**717**	**27.3**	717	27.3	602	32.5	609	32.1	**603**	**32.5**
433.milc	159	57.6	**159**	**57.6**	159	57.6	**157**	**58.5**	157	58.5	157	58.5
434.zeusmp	64.6	141	**65.0**	**140**	65.4	139	64.6	141	**65.0**	**140**	65.4	139
435.gromacs	**272**	**26.3**	272	26.3	273	26.1	**272**	**26.3**	272	26.3	273	26.1
436.cactusADM	23.1	518	23.7	505	**23.3**	**513**	23.1	518	23.7	505	**23.3**	**513**
437.leslie3d	**37.8**	**248**	38.2	246	37.8	248	**37.8**	**248**	38.2	246	37.8	248
444.namd	380	21.1	**379**	**21.1**	379	21.1	373	21.5	**374**	**21.5**	374	21.5
447.dealII	243	47.1	244	46.9	**243**	**47.1**	243	47.1	244	46.9	**243**	**47.1**
450.soplex	207	40.3	**206**	**40.6**	205	40.7	207	40.3	**206**	**40.6**	205	40.7
453.povray	135	39.3	**135**	**39.3**	136	39.1	115	46.3	**115**	**46.3**	115	46.4
454.calculix	**234**	**35.2**	234	35.2	236	35.0	215	38.3	217	38.0	**216**	**38.2**
459.GemsFDTD	**70.3**	**151**	70.5	151	69.9	152	58.4	182	**58.4**	**182**	58.2	182
465.tonto	317	31.0	**317**	**31.1**	316	31.1	248	39.7	248	39.6	**248**	**39.7**
470.lbm	29.2	470	28.4	483	**28.6**	**480**	29.2	470	28.4	483	**28.6**	**480**
481.wrf	158	70.6	**160**	**69.9**	162	69.1	158	70.6	**160**	**69.9**	162	69.1
482.sphinx3	**298**	**65.5**	299	65.3	296	65.8	**297**	**65.5**	296	65.8	301	64.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

```
BIOS Settings:
Operating Mode set to Maximum Performance
Sysinfo program /root/SPECcpu-v1.2/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on tianden Wed May  2 10:56:13 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2665 0 @ 2.40GHz
          2 "physical id"s (chips)
          32 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 8
          siblings  : 16
          physical 0: cores 0 1 2 3 4 5 6 7
          physical 1: cores 0 1 2 3 4 5 6 7
       cache size : 20480 KB

    From /proc/meminfo
       MemTotal:       132093704 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    /usr/bin/lsb_release -d
       Red Hat Enterprise Linux Server release 6.2 (Santiago)

    From /etc/*release* /etc/*version*
       redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
       system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
       system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

    uname -a:
       Linux tianden 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011 x86_64
       x86_64 x86_64 GNU/Linux

    run-level 3 Apr 30 11:56

    SPEC is set to: /root/SPECcpu-v1.2
       Filesystem     Type    Size  Used Avail Use% Mounted on
       /dev/mapper/vg_tianden-lv_root
                      ext4    145G   74G   64G  54% /

    (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,compact,1,0"
 LD_LIBRARY_PATH = "/root/SPECcpu-v1.2/libs/32:/root/SPECcpu-v1.2/libs/64"
 OMP_NUM_THREADS = "16"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RHEL5.5
  Transparent Huge Pages enabled with:
```

Continued on next page

General Notes (Continued)

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

`Same as Base Portability Flags`

Peak Optimization Flags

C benchmarks:

433.milc: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 08:57:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 May 2012.