



# SPEC® CINT2006 Result

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## NEC Corporation

SPECint®\_rate2006 = 331

Express5800/R120d-1M (Intel Xeon E5-2670)

SPECint\_rate\_base2006 = 318

CPU2006 license: 9006

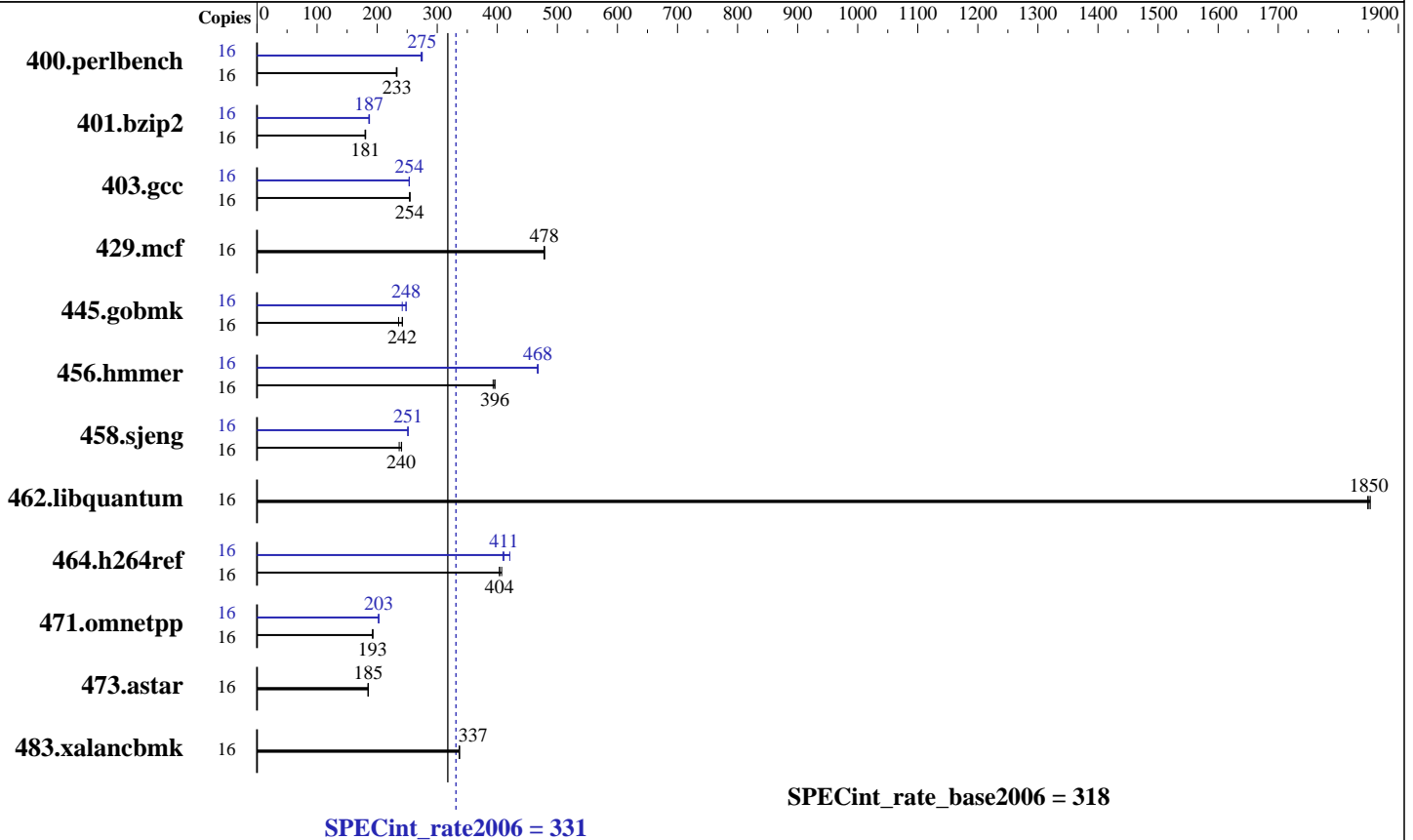
Test date: Apr-2012

Test sponsor: NEC Corporation

Hardware Availability: Apr-2012

Tested by: NEC Corporation

Software Availability: Dec-2011



### Hardware

CPU Name: Intel Xeon E5-2670  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 1 chip, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<b><u>672</u></b>	<b><u>233</u></b>	671	233	674	232	16	568	275	<b><u>569</u></b>	<b><u>275</u></b>	572	273
401.bzip2	16	854	181	859	180	<b><u>855</u></b>	<b><u>181</u></b>	16	829	186	825	187	<b><u>827</u></b>	<b><u>187</u></b>
403.gcc	16	507	254	<b><u>507</u></b>	<b><u>254</u></b>	506	255	16	508	254	509	253	<b><u>508</u></b>	<b><u>254</u></b>
429.mcf	16	305	478	<b><u>305</u></b>	<b><u>478</u></b>	305	479	16	305	478	<b><u>305</u></b>	<b><u>478</u></b>	305	479
445.gobmk	16	693	242	712	236	<b><u>694</u></b>	<b><u>242</u></b>	16	<b><u>676</u></b>	<b><u>248</u></b>	676	248	694	242
456.hammer	16	380	393	<b><u>377</u></b>	<b><u>396</u></b>	377	396	16	<b><u>319</u></b>	<b><u>468</u></b>	319	468	320	467
458.sjeng	16	804	241	818	237	<b><u>806</u></b>	<b><u>240</u></b>	16	771	251	<b><u>771</u></b>	<b><u>251</u></b>	770	251
462.libquantum	16	<b><u>179</u></b>	<b><u>1850</u></b>	179	1850	179	1850	16	<b><u>179</u></b>	<b><u>1850</u></b>	179	1850	179	1850
464.h264ref	16	<b><u>877</u></b>	<b><u>404</u></b>	869	407	877	404	16	842	421	865	409	<b><u>861</u></b>	<b><u>411</u></b>
471.omnetpp	16	<b><u>519</u></b>	<b><u>193</u></b>	519	193	519	193	16	493	203	<b><u>493</u></b>	<b><u>203</u></b>	494	202
473.astar	16	606	185	<b><u>607</u></b>	<b><u>185</u></b>	608	185	16	606	185	<b><u>607</u></b>	<b><u>185</u></b>	608	185
483.xalancbmk	16	<b><u>328</u></b>	<b><u>337</u></b>	327	337	328	337	16	<b><u>328</u></b>	<b><u>337</u></b>	327	337	328	337

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
Energy Performance: Performance  
Memory Voltage: 1.5 V

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"

The Express5800/R120d-1M and the Express5800/R120d-2M models are electronically equivalent. The results have been measured on the Express5800/R120d-2M model.

Added glibc-static-2.12-1.47.el6.x86\_64.rpm to enable static linking

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## General Notes (Continued)

Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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