SPEC® CFP2006 Result

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Dell Inc.

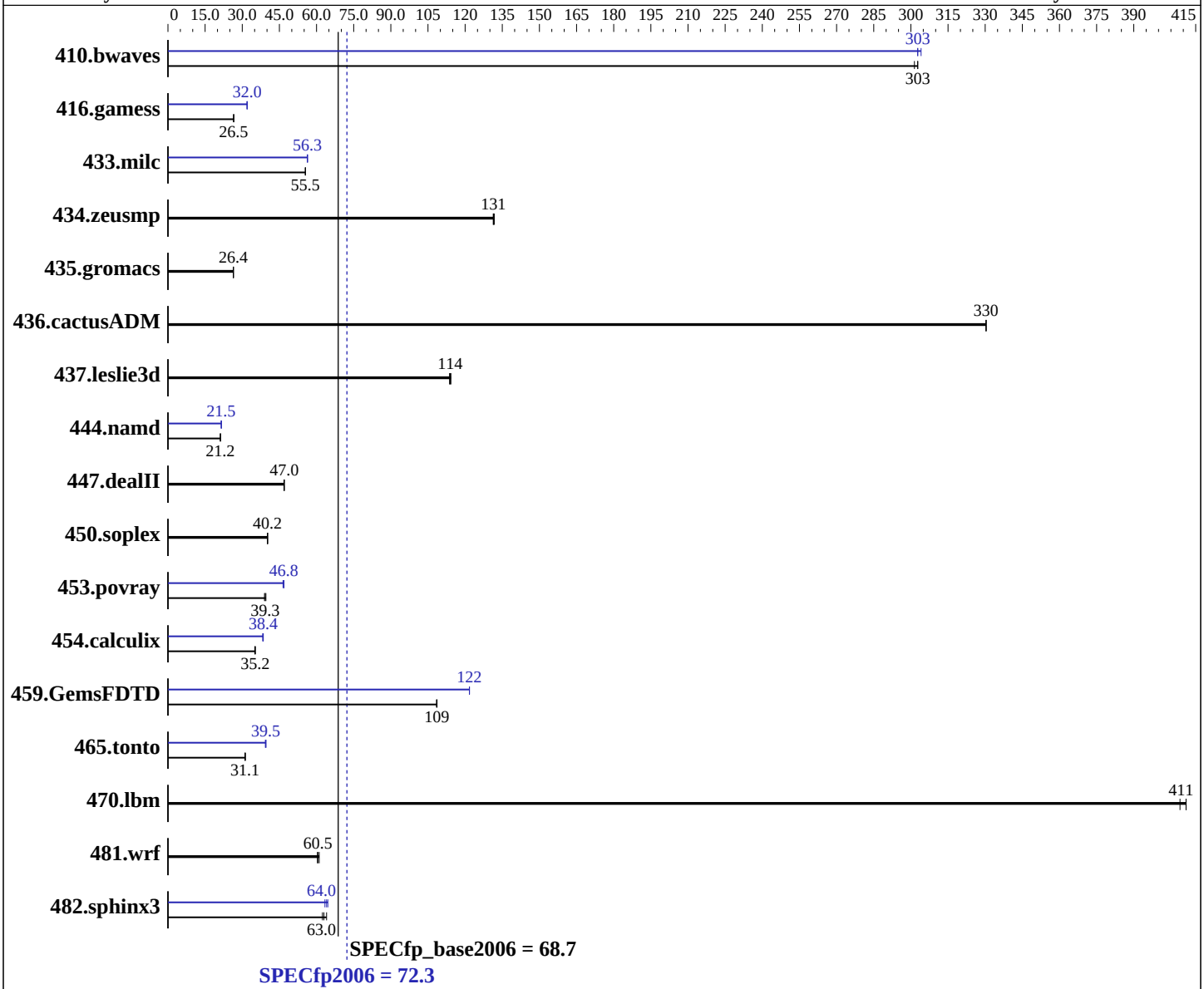
PowerEdge R520 (Intel Xeon E5-2470, 2.30 GHz)

SPECfp®2006 = 72.3

SPECfp_base2006 = 68.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Feb-2012
Hardware Availability: May-2012
Software Availability: Feb-2012



Hardware

CPU Name:	Intel Xeon E5-2470
CPU Characteristics:	Intel Turbo Boost Technology up to 3.10 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP2 (x86_64) 3.0.13-0.27-default
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

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L3 Cache: 20 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 2 x 600 GB 15000 RPM SAS, RAID 1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	45.1	301	44.9	303	**44.9**	**303**	**44.9**	**303**	44.7	304	44.9	303
416.gamess	737	26.6	738	26.5	**738**	**26.5**	**612**	**32.0**	613	31.9	612	32.0
433.milc	165	55.5	**165**	**55.5**	165	55.5	**163**	**56.3**	163	56.3	163	56.3
434.zeusmp	**69.3**	**131**	69.3	131	69.1	132	**69.3**	**131**	69.3	131	69.1	132
435.gromacs	**270**	**26.4**	270	26.5	271	26.4	**270**	**26.4**	270	26.5	271	26.4
436.cactusADM	**36.2**	**330**	36.2	330	36.2	330	**36.2**	**330**	36.2	330	36.2	330
437.leslie3d	82.3	114	82.7	114	**82.5**	**114**	82.3	114	82.7	114	**82.5**	**114**
444.namd	**378**	**21.2**	379	21.2	378	21.2	372	21.5	**372**	**21.5**	372	21.5
447.dealII	**244**	**47.0**	244	46.8	244	47.0	**244**	**47.0**	244	46.8	244	47.0
450.soplex	207	40.3	208	40.1	**208**	**40.2**	207	40.3	208	40.1	**208**	**40.2**
453.povray	135	39.6	136	39.0	**136**	**39.3**	114	46.9	**114**	**46.8**	115	46.5
454.calculix	234	35.3	235	35.1	**234**	**35.2**	214	38.5	**215**	**38.4**	216	38.2
459.GemsFDTD	97.7	109	97.9	108	**97.7**	**109**	**87.2**	**122**	87.2	122	87.2	122
465.tonto	316	31.1	315	31.3	**316**	**31.1**	250	39.3	**249**	**39.5**	249	39.6
470.lbm	33.6	409	**33.4**	**411**	33.4	411	33.6	409	**33.4**	**411**	33.4	411
481.wrf	**184**	**60.5**	183	61.1	185	60.4	**184**	**60.5**	183	61.1	185	60.4
482.sphinx3	**309**	**63.0**	304	64.1	312	62.4	302	64.6	307	63.4	**304**	**64.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
CPU Power Management set to Maximum Performance
Memory Frequency set to Maximum Performance
Turbo Boost set to Enabled
C States/C1E set to Enabled
Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on R520 Thu Feb  2 07:03:52 2012

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
```

Continued on next page

Platform Notes (Continued)

```
      http://www.spec.org/cpu2006/Docs/config.html#sysinfo

    From /proc/cpuinfo
       model name : Intel(R) Xeon(R) CPU E5-2470 0 @ 2.30GHz
          2 "physical id"s (chips)
          32 "processors"
       cores, siblings (Caution: counting these is hw and system dependent.  The
       following excerpts from /proc/cpuinfo might not be reliable.  Use with
       caution.)
          cpu cores : 8
          siblings  : 16
          physical 0: cores 0 1 2 3 4 5 6 7
          physical 1: cores 0 1 2 3 4 5 6 7
       cache size : 20480 KB

    From /proc/meminfo
       MemTotal:       98994188 kB
       HugePages_Total:       0
       Hugepagesize:       2048 kB

    /usr/bin/lsb_release -d
       SUSE Linux Enterprise Server 11 (x86_64)

    From /etc/*release* /etc/*version*
       SuSE-release:
          SUSE Linux Enterprise Server 11 (x86_64)
          VERSION = 11
          PATCHLEVEL = 2

    uname -a:
       Linux R520 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012 (d73692b)
       x86_64 x86_64 x86_64 GNU/Linux

    run-level 3 Feb 2 00:19 last=S

    SPEC is set to: /root/CPU2006-1.2
       Filesystem     Type  Size  Used Avail Use% Mounted on
       /dev/sda2      ext3  143G   12G  131G   9% /

    Additional information from dmidecode:

    (End of data from sysinfo program)
```

General Notes

```
  Environment variables set by runspec before the start of the run:
  KMP_AFFINITY = "granularity=fine,scatter"
  LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
  OMP_NUM_THREADS = "16"

   Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
```

Continued on next page

General Notes (Continued)

```
memory using RHEL5.5
Transparent Huge Pages disabled with:
echo never  > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>       /proc/sys/vm/drop_caches
The Dell PowerEdge R520 and
the Bull NovaScale R450 F3 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R520 model
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml

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For other inquiries, please contact webmaster@spec.org.

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