



# SPEC® CINT2006 Result

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## Supermicro

Supermicro C7P67 motherboard (Intel Core i7-3770, 3.40 GHz)

SPECint®\_rate2006 = 194

SPECint\_rate\_base2006 = 190

CPU2006 license: 001176

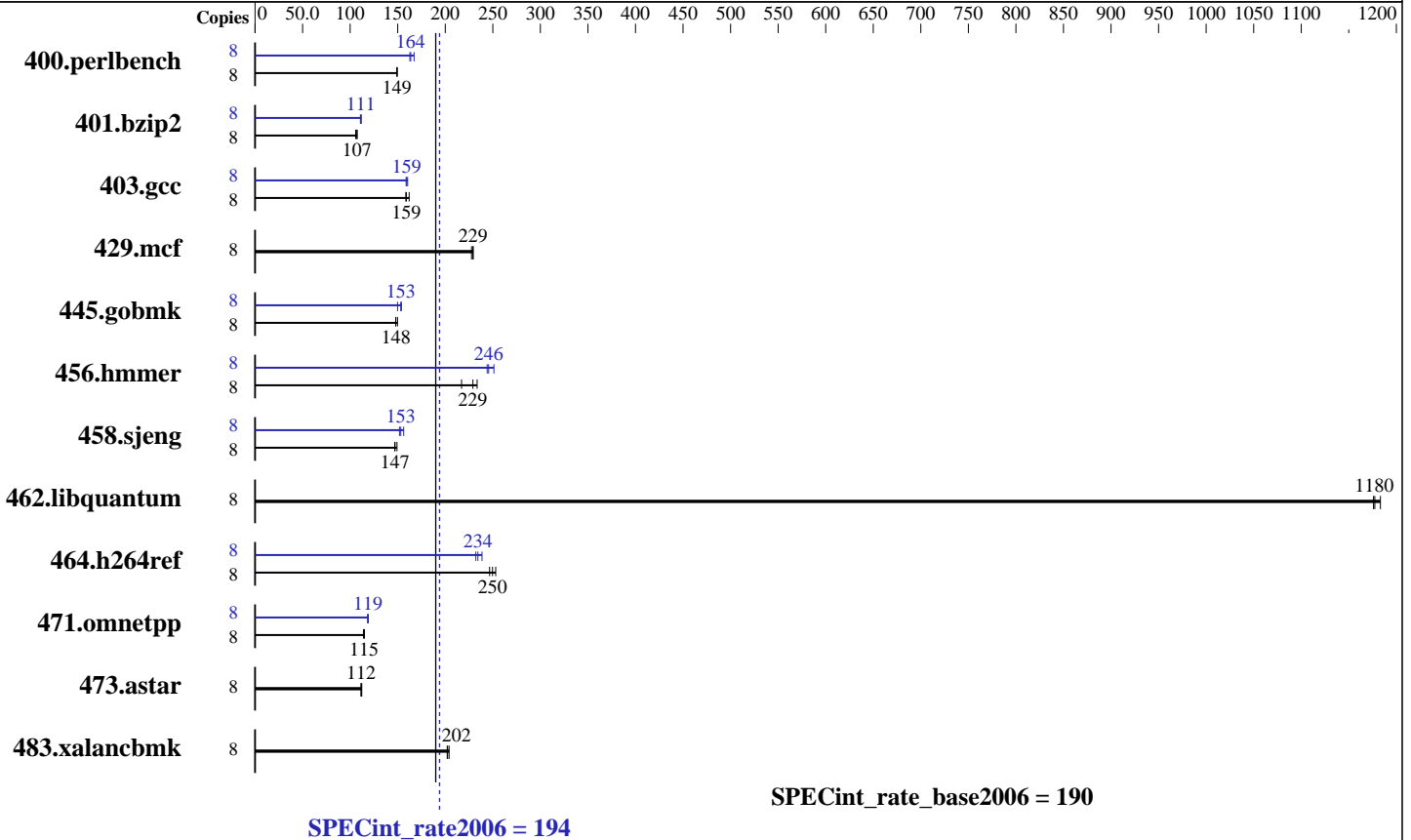
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2012

Hardware Availability: Apr-2012

Software Availability: Dec-2011



SPECint\_rate\_base2006 = 190

### Hardware

CPU Name: Intel Core i7-3770  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-14900U-13)  
 Disk Subsystem: 1 x 120 GB OCZ SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86\_64  
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	524	149	<u>524</u>	<u>149</u>	523	150	8	467	167	<u>477</u>	<u>164</u>	480	163
401.bzip2	8	719	107	<u>722</u>	<u>107</u>	730	106	8	<u>693</u>	<u>111</u>	697	111	692	112
403.gcc	8	398	162	<u>405</u>	<u>159</u>	406	159	8	401	160	405	159	<u>404</u>	<u>159</u>
429.mcf	8	318	229	321	228	<u>318</u>	<u>229</u>	8	318	229	321	228	<u>318</u>	<u>229</u>
445.gobmk	8	567	148	560	150	<u>567</u>	<u>148</u>	8	545	154	<u>548</u>	<u>153</u>	560	150
456.hmmr	8	320	233	<u>326</u>	<u>229</u>	344	217	8	297	251	<u>304</u>	<u>246</u>	306	244
458.sjeng	8	<u>658</u>	<u>147</u>	649	149	659	147	8	619	156	638	152	<u>632</u>	<u>153</u>
462.libquantum	8	141	1180	140	1180	<u>141</u>	<u>1180</u>	8	141	1180	140	1180	<u>141</u>	<u>1180</u>
464.h264ref	8	<u>709</u>	<u>250</u>	700	253	718	247	8	742	239	<u>756</u>	<u>234</u>	763	232
471.omnetpp	8	439	114	<u>435</u>	<u>115</u>	435	115	8	<u>421</u>	<u>119</u>	420	119	422	118
473.astar	8	<u>503</u>	<u>112</u>	504	111	500	112	8	<u>503</u>	<u>112</u>	504	111	500	112
483.xalancbmk	8	<u>273</u>	<u>202</u>	273	202	271	204	8	<u>273</u>	<u>202</u>	273	202	271	204

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Platform Notes

As tested, the system used a Supermicro CSE-732D2-500B chassis.  
The chassis is configured with a PWS-502-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0124L4 front cooling fan and 1 FAN-0124L4 rear exhaust fan.  
BIOS configuration:  
Memory Timing Selection = Manual  
Memory Frequency = Force DDR-1866MHz

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



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## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch  
  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
  
400.perlbench: icc -m64  
  
401.bzip2: icc -m64  
  
456.hmmer: icc -m64  
  
458.sjeng: icc -m64  
  
C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
 -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.20120625.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.20120625.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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