SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

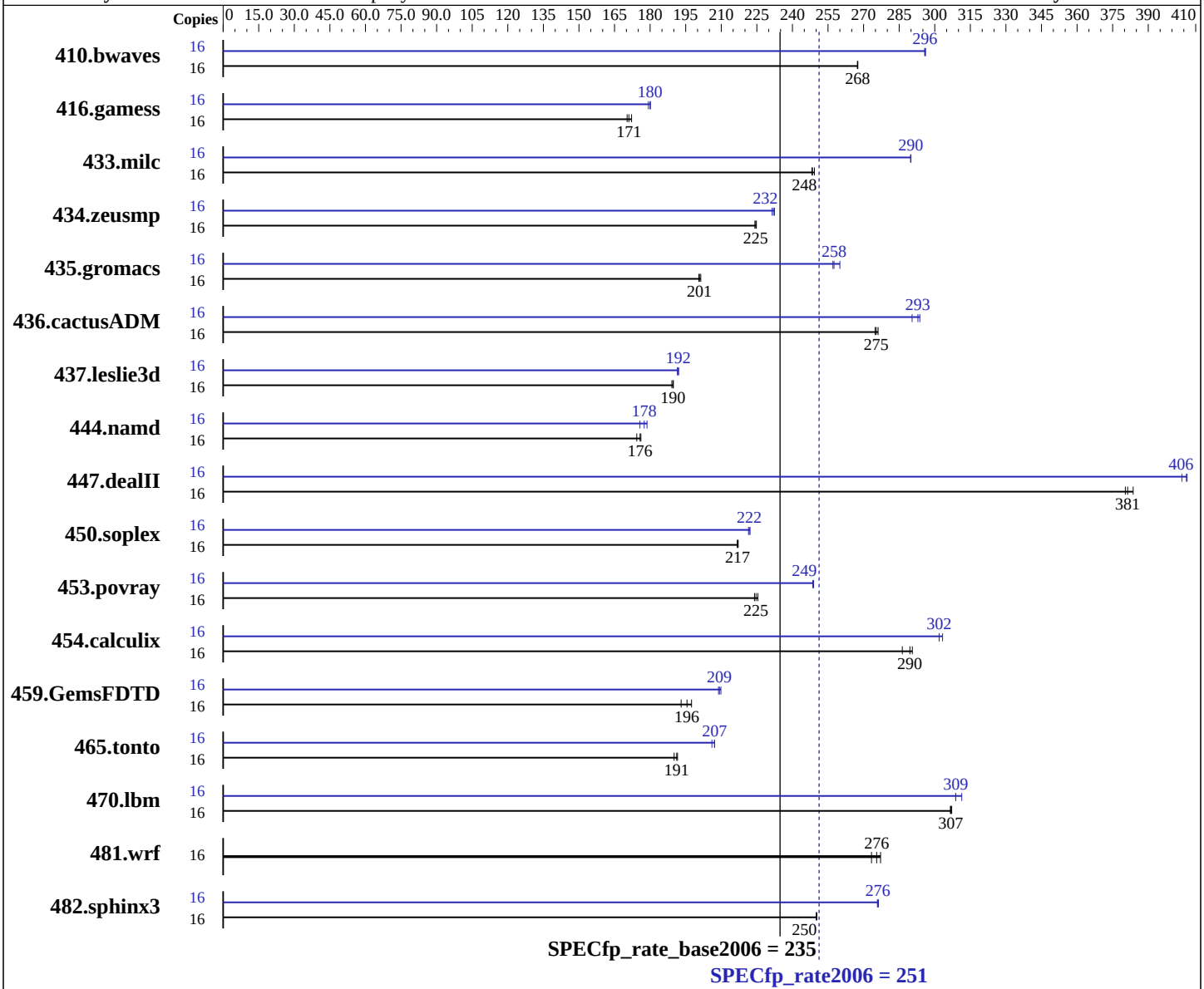
ProLiant BL465c Gen8
(2.60 GHz AMD Opteron 6212)

SPECfp®_rate2006 = 251

SPECfp_rate_base2006 = 235

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011



Hardware

CPU Name:	AMD Opteron 6212
CPU Characteristics:	AMD Turbo CORE technology up to 3.20 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	16 cores, 2 chips, 8 cores/chip
CPU(s) orderable:	1,2 chips

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler:	C/C++/Fortran: Version 4.5.1 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant BL465c Gen8
(2.60 GHz AMD Opteron 6212)

SPECfp_rate2006 = 251

SPECfp_rate_base2006 = 235

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: May-2012
Hardware Availability: Jun-2012
Software Availability: Dec-2011

Primary Cache: 256 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache: 16 MB I+D on chip per chip, 8 MB shared / 4 cores
Other Cache: None
Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 146 GB 15 K SAS
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	813	267	813	268	**813**	**268**	16	**735**	**296**	735	296	734	296
416.gamess	16	1819	172	**1831**	**171**	1840	170	16	1739	180	**1741**	**180**	1748	179
433.milc	16	592	248	**591**	**248**	589	249	16	507	290	**507**	**290**	507	290
434.zeusmp	16	648	225	650	224	**648**	**225**	16	**628**	**232**	626	232	629	231
435.gromacs	16	570	201	567	201	**569**	**201**	16	444	257	439	260	**444**	**258**
436.cactusADM	16	696	275	**694**	**275**	692	276	16	**653**	**293**	651	294	658	290
437.leslie3d	16	**793**	**190**	795	189	792	190	16	**784**	**192**	786	191	783	192
444.namd	16	729	176	736	174	**730**	**176**	16	718	179	**723**	**178**	730	176
447.dealII	16	481	380	**480**	**381**	477	384	16	**451**	**406**	453	404	450	406
450.soplex	16	615	217	616	217	**615**	**217**	16	601	222	**602**	**222**	602	221
453.povray	16	380	224	378	225	**379**	**225**	16	**342**	**249**	342	249	342	249
454.calculix	16	461	286	454	291	**456**	**290**	16	**437**	**302**	437	302	435	303
459.GemsFDTD	16	860	197	**868**	**196**	879	193	16	809	210	**811**	**209**	813	209
465.tonto	16	**823**	**191**	822	192	828	190	16	**760**	**207**	760	207	764	206
470.lbm	16	**717**	**307**	716	307	717	307	16	706	311	**712**	**309**	712	309
481.wrf	16	654	273	645	277	**649**	**276**	16	654	273	645	277	**649**	**276**
482.sphinx3	16	1246	250	1247	250	**1247**	**250**	16	**1130**	**276**	1129	276	1131	276

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set transparent_hugepage=never as a boot parameter in /boot/grub/menu.lst
```

Continued on next page

Operating System Notes (Continued)

```
Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

Platform Notes

```
 BIOS configuration:
  HP Power Profile set to Maximum Performance
  Thermal Configuration set to Maximum Cooling
  System Locality Information Table to Disabled
  Minimum Processor Idle Power C1e State to Enabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "480"
LD_LIBRARY_PATH = "/cpu2006/amd1104-rate-libs-revC/32:/cpu2006/amd1104-rate-libs-revC/64"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64

Binaries were compiled on a system with 2x AMD Opteron 6274 chips + 64GB Memory using RHEL 6.1
```

Base Compiler Invocation

C benchmarks:
`opencc`

C++ benchmarks:
`openCC`

Fortran benchmarks:
`openf95`

Benchmarks using both Fortran and C:
`opencc openf95`

Base Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`

Continued on next page

Base Portability Flags (Continued)

437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso

C++ benchmarks:
-march=bdver1 -Ofast -static -CG:load_exe=0 -OPT:malloc_alg=1 -INLINE:aggressive=on -HP:bd=2m:heap=2m -D__OPEN64_FAST_SET

Fortran benchmarks:
-march=bdver1 -Ofast -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256 -HP:bd=2m:heap=2m -mso

Benchmarks using both Fortran and C:
-march=bdver1 -Ofast -OPT:malloc_alg=1 -HP:bd=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso -LNO:blocking=off -OPT:rsqrt=2 -OPT:unroll_size=256

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LP64 -fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: -march=bdver1 -Ofast -CG:movnti=1 -CG:locs_best=on -HP:bdt=2m:heap=2m -IPA:plimit=7000 -IPA:callee_limit=1200 -OPT:struct_array_copy=2 -OPT:alias=field_sensitive -mso

470.lbm: -march=bdver1 -Ofast -CG:cmp_peep=on -OPT:unroll_times_max=8 -OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on -HP:bdt=2m:heap=2m -IPA:plimit=8000 -IPA:small_pu=100 -mso

482.sphinx3: -march=bdver1 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=1000 -OPT:malloc_alg=2 -CG:cmp_peep=on -CG:local_sched_alg=2 -CG:p2align=0 -INLINE:aggressive=on -LNO:prefetch=2 -LNO:prefetch_ahead=4 -mso

C++ benchmarks:

444.namd: -march=bdver1 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=3000 -LNO:ignore_feedback=off -CG:local_sched_alg=2 -CG:load_exe=0 -OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=bdver1 -Ofast -D__OPEN64_FAST_SET -static -INLINE:aggressive=on -LNO:opt=0 -LNO:simd=0 -fno-emit-exceptions -m32 -OPT:unroll_times_max=8 -OPT:unroll_size=256 -OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on -CG:cmp_peep=on -CG:movext_icmp=off

Continued on next page

Peak Optimization Flags (Continued)

447.dealII (continued):
-TENV:frame_pointer=off

450.soplex: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:ignore_feedback=off
-INLINE:aggressive=on -OPT:RO=1 -OPT:IEEE_arith=3
-OPT:IEEE_NaN_Inf=off -OPT:fold_unsigned_relops=on
-fno-exceptions -CG:p2align=0 -m32 -HP:bdt=2m:heap=2m
-WOPT:sib=on

453.povray: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:pre_local_sched=off
-CG:p2align=0 -CG:p2align_split=on -CG:dsched=on
-INLINE:aggressive=on -HP:bd=2m:heap=2m -OPT:transform=2
-OPT:alias=disjoint -WOPT:aggcm=0

Fortran benchmarks:

410.bwaves: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:ignore_feedback=off -LNO:fu=4
-LNO:loop_model_simd=on -LNO:simd_rm_unity_remainder=on
-WOPT:aggstr=0 -HP:bdt=2m:heap=2m -CG:cmp_peep=on

416.gamess: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:simd=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m -WOPT:sib=on

434.zeusmp: -march=bdver1 -Ofast -LNO:blocking=off -LNO:interchange=off
-IPA:plimit=1500 -HP:bdt=2m:heap=2m

437.leslie3d: -march=bdver1 -Ofast -CG:pre_minreg_level=2 -LNO:simd=0
-LNO:fusion=2 -HP:bdt=2m:heap=2m -mso

459.GemsFDTD: -march=bdver1 -Ofast -IPA:plimit=1500 -OPT:unroll_size=0
-LNO:fission=2 -CG:load_exe=0 -CG:local_sched_alg=2 -HP

465.tonto: -march=bdver1 -Ofast -OPT:alias=no_f90_pointer_alias
-LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525
-HP:bdt=2m:heap=2m

Benchmarks using both Fortran and C:

435.gromacs: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m -CG:local_sched_alg=2 -GRA:unspill=ON
-CG:load_exe=3 -LNO:simd=3

Continued on next page

Peak Optimization Flags (Continued)

436.cactusADM: `-march=bdver1 -fb_create fbdata`(pass 1)
`-fb_opt fbdata`(pass 2) `-Ofast -LNO:blocking=off`
`-LNO:prefetch=2 -HP -CG:locs_shallow_depth=1 -CG:load_exe=0`
`-CG:dsched=on -WOPT:sib=on`

454.calculix: `-march=bdver1 -Ofast -OPT:unroll_size=256`
`-GRA:optimize_boundary=on -CG:dsched=on -HP:bdt=2m:heap=2m`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20120703.html
http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-AMD-V1.2-A.20120703.xml
http://www.spec.org/cpu2006/flags/x86-open64-451-flags-rate-revC.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 09:47:13 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 July 2012.