# SPEC® CFP2006 Result

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## Fujitsu

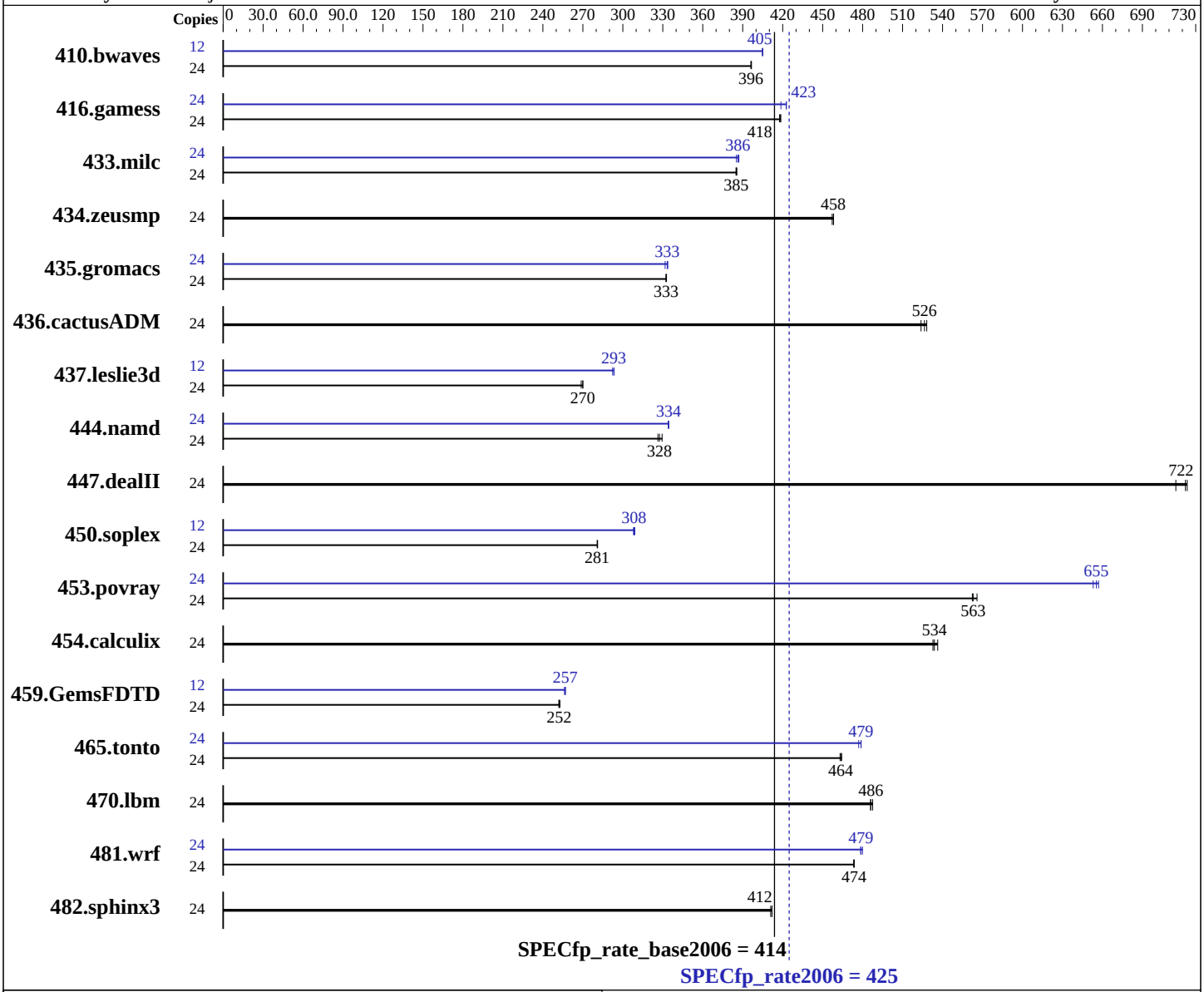
PRIMERGY CX270 S1, Intel Xeon E5-2667, 2.90 GHz

SPECfp®_rate2006 = 425

SPECfp_rate_base2006 = 414

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Mar-2012
**Hardware Availability:** Jun-2012
**Software Availability:** Dec-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E5-2667 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.50 GHz |
| CPU MHz: | 2900 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.2 (Santiago)<br>2.6.32-220.el6.x86_64 |
| Compiler: | C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;<br>Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |

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| | |
|---|---|
| L3 Cache: | 15 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 128 GB (16 x 8 GB 2Rx4 PC3L-12800R-11, ECC) |
| Disk Subsystem: | 1 x SATA, 500 GB, 7200 RPM |
| Other Hardware: | None |

| | |
|---|---|
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | None |

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 24 | 823 | 396 | 822 | 397 | **823** | **396** | 12 | 403 | 405 | 403 | 405 | **403** | **405** |
| 416.gamess | 24 | 1123 | 419 | 1125 | 418 | **1123** | **418** | 24 | 1122 | 419 | 1111 | 423 | **1112** | **423** |
| 433.milc | 24 | 571 | 386 | **572** | **385** | 572 | 385 | 24 | **570** | **386** | 572 | 385 | 569 | 387 |
| 434.zeusmp | 24 | 477 | 458 | **477** | **458** | 478 | 457 | 24 | 477 | 458 | **477** | **458** | 478 | 457 |
| 435.gromacs | 24 | 515 | 333 | 516 | 332 | **515** | **333** | 24 | **514** | **333** | 517 | 332 | 513 | 334 |
| 436.cactusADM | 24 | 543 | 528 | **545** | **526** | 548 | 524 | 24 | 543 | 528 | **545** | **526** | 548 | 524 |
| 437.leslie3d | 24 | 839 | 269 | **836** | **270** | 836 | 270 | 12 | 386 | 292 | 384 | 293 | **385** | **293** |
| 444.namd | 24 | 584 | 329 | 590 | 326 | **588** | **328** | 24 | **576** | **334** | 576 | 334 | 576 | 334 |
| 447.dealII | 24 | 379 | 724 | **380** | **722** | 384 | 715 | 24 | 379 | 724 | **380** | **722** | 384 | 715 |
| 450.soplex | 24 | 712 | 281 | 713 | 281 | **713** | **281** | 12 | 325 | 308 | **324** | **308** | 324 | 309 |
| 453.povray | 24 | 226 | 566 | 227 | 562 | **227** | **563** | 24 | 196 | 653 | **195** | **655** | 194 | 657 |
| 454.calculix | 24 | **371** | **534** | 369 | 536 | 372 | 533 | 24 | **371** | **534** | 369 | 536 | 372 | 533 |
| 459.GemsFDTD | 24 | 1011 | 252 | **1009** | **252** | 1007 | 253 | 12 | 496 | 257 | **496** | **257** | 497 | 256 |
| 465.tonto | 24 | 510 | 463 | 509 | 464 | **509** | **464** | 24 | 495 | 477 | 493 | 479 | **493** | **479** |
| 470.lbm | 24 | 679 | 486 | 676 | 488 | **678** | **486** | 24 | 679 | 486 | 676 | 488 | **678** | **486** |
| 481.wrf | 24 | **566** | **474** | 566 | 473 | 566 | 474 | 24 | 559 | 480 | **560** | **479** | 560 | 479 |
| 482.sphinx3 | 24 | 1135 | 412 | 1138 | 411 | **1136** | **412** | 24 | 1135 | 412 | 1138 | 411 | **1136** | **412** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 This result was measured on the PRIMERGY CX250 S1. The PRIMERGY CX250 S1
 and the PRIMERGY CX270 S1 are electronically equivalent.
 For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
  482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch
```

Benchmarks using both Fortran and C:
```
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks (except as noted below):
```
icpc  -m64
```

450.soplex: `icpc -m32`

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

Continued on next page

## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -static -auto-ilp32 -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-mem-layout-trans=3

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.01.xml


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